



Status report on  
**DNNROI sigproc**

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# Time offsets from DepoFluxSplat

- (Previous) DepoTransform → (Current) DepoFluxSplat
- DepoTransform: grid start point shift was the solution
- DepoFluxSplat: rasterize the depo to a grid, but still correcting by grid start point shift, which gives double correction

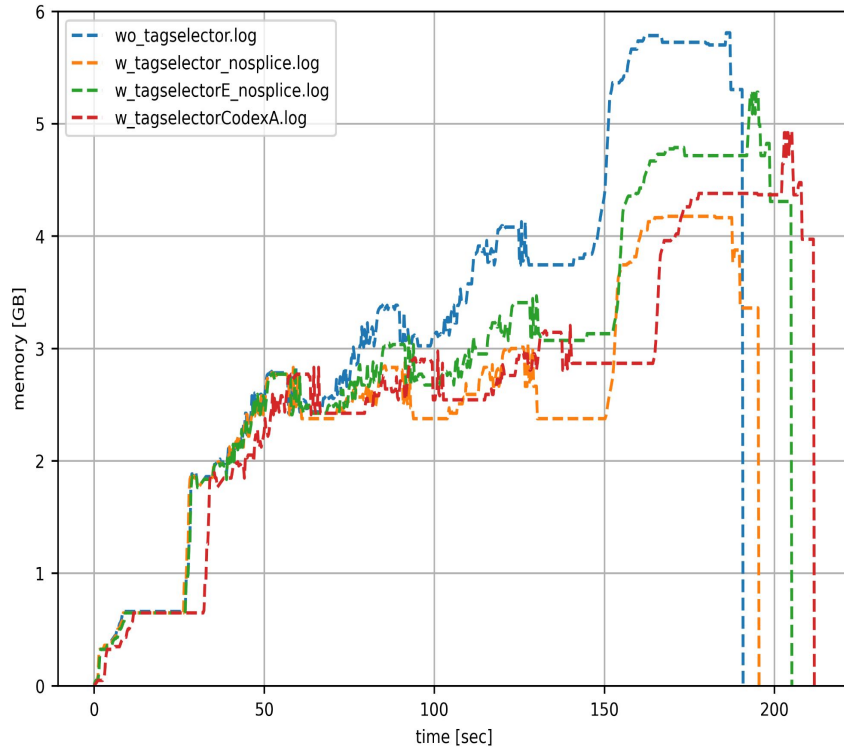
```
292 // Depo is at response plane. Find its time at the collection
293 // plane assuming it were to continue along a uniform field.
294 // After this, all times are nominal up until we add arbitrary
295 // time offsets at output.
296 const double nominal_depo_time = depo->time() + m_origin / m_speed;
```

```
===== Peak Time Comparison (single wire) =====
wire = 9390
DNN SP      peak time mu = 3781.39
Deposplat   peak time mu = 3792.82
delta_t (DNN - Deposplat) = -11.43 ticks
=====
```

```
===== Peak Time Comparison (single wire) =====
wire = 3850
DNN SP      peak time mu = 4418.11
Deposplat   peak time mu = 4413.96
delta_t (DNN - Deposplat) = 4.15 ticks
=====
```

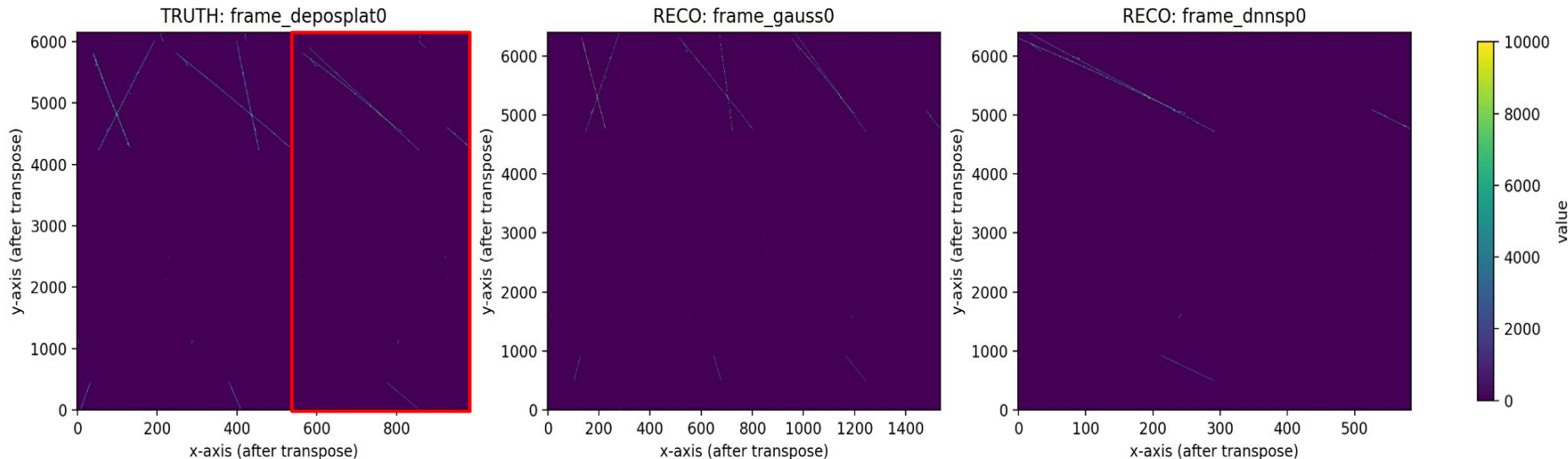
- Remaining time offset of ~ 11 ticks in PDVD
- Remaining time offset of ~ 4 ticks in PDHD

# TagSelector for spliced pipeline



- Blue shows the baseline
- Orange shows the result with TagSelector, But:
  - dnnsr results exist in magnify root file
  - no dnnsr results in artroot file
- By re-arranging the data processing pipeline, I got Green and Red that dnnsr results contained in the final

# PDVD sample production



```
frame_dnnsp0      Dataset {584, 6400}  
frame_gauss0     Dataset {1536, 6400}  
frame_loose_lf0  Dataset {1536, 6400}  
frame_mp2_roi0   Dataset {1536, 6400}  
frame_mp3_roi0   Dataset {1536, 6400}
```

```
channels_deposplat0 Dataset {557}  
frame_deposplat0   Dataset {557, 3474}  
tickinfo_deposplat0 Dataset {3}
```

- CRU, 1536 channels = 476 + 476 + 584
- Are we going to train based on CRU?

- Different {channels, ticks} compared to reco
- Previously resolved in PDHD sample production

# Back Up

# TagSelector for spliced pipeline

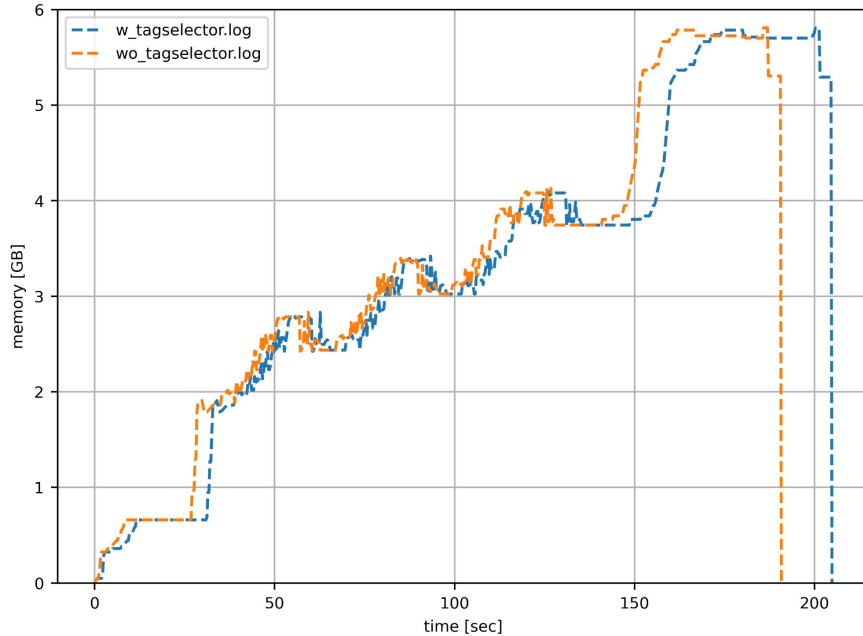
```
sigproc/inc/WireCellSigProc/TagSelector.h
1 + /**
2 +  Make a new output frame with a set of traces selected from the
3 +  input based on trace tags.
4 +  */
5 +
6 + #ifndef WIRECELLSIGPROC_TAGSELECTOR
7 + #define WIRECELLSIGPROC_TAGSELECTOR
8 +
9 + #include "WireCellFace/IFrameFilter.h"
10 + #include "WireCellFace/IConfigurable.h"
11 + #include "WireCellAux/Logger.h"
12 +
13 + #include <string>
14 + #include <vector>
15 +
16 + namespace WireCell {
17 + namespace SigProc {
18 +
19 + class TagSelector : public Aux::Logger,
20 + public IFrameFilter,
21 + public IConfigurable
22 + {
23 + public:
24 + TagSelector();
25 + virtual ~TagSelector();
26 +
27 + // IFrameFilter interface.
28 + virtual bool operator()(const input_pointer& in, output_pointer& out);
29 +
30 + // IConfigurable interface.
31 + virtual void configure(const WireCell::Configuration& config);
32 + virtual WireCell::Configuration default_configuration() const;
33 +
34 + private:
35 + std::vector<std::string> m_tags;
36 + int m_count{0};
37 + };
38 + } // namespace SigProc
39 + } // namespace WireCell
40 +
41 + #endif

404 local sp_tag_selector = g.pnode({
405 type: 'TagSelector',
406 name: 'sp_tag_selector',
407 data: {
408 tags: [
409 tag
410 for n in std.range(0, std.length(tools.anodes) - 1)
411 for tag in ['gauss%' % n, 'wiener%' % n]
412 ],
413 }, n_in=1, n_out=1);
414 local outr = g.pipeline([ofanin, sp_tag_selector, outretagger, wcls_output.sp_signals, osink]);
415 // local outr = g.pipeline([ofanin, outretagger, osink]);
416
417 local edge_selector(e) = std.startsWith(e.tail.node, "OmnibusSigProc:");
418 local fanout_factory(n,e) = { type: 'FrameFanout', name: "splice%" % n, data: {multiplicity: 2} }; // "2-wire" splice
419
420 local spliced_graph =
421 if save_tradsp then
422 g.splice(graph, outr, edge_selector, fanout_factory)
423 else
424 graph;
425
```

```
sigproc/src/TagSelector.cxx
1 + #include "WireCellSigProc/TagSelector.h"
2 + #include "WireCellAux/SigProcFrame.h"
3 + #include "WireCellAux/FrameTools.h"
4 +
5 + #include "WireCellUtil/NamedFactory.h"
6 +
7 + #include <sstream>
8 + #include <unordered_map>
9 +
10 + WIRECELL_FACTORY(TagSelector, WireCell::SigProc::TagSelector, WireCell::IFrameFilter, WireCell::IConfigurable)
11 +
12 + using namespace WireCell;
13 + using namespace WireCell::SigProc;
14 +
15 + TagSelector::TagSelector()
16 + : Aux::Logger("TagSelector", "glue")
17 + {}
18 +
19 + TagSelector::~TagSelector() {}
20 +
21 + WireCell::Configuration TagSelector::default_configuration() const
22 + {
23 + Configuration cfg;
24 +
25 + // Only traces with these tags will be in the output.
26 + cfg["tags"] = Json::arrayValue;
27 +
28 + return cfg;
29 + }
30 +
31 + void TagSelector::configure(const WireCell::Configuration& cfg)
32 + {
33 + auto jtags = cfg["tags"];
34 + int ntags = jtags.size();
35 + m_tags.clear();
36 + m_tags.resize(ntags);
37 + for (int ind = 0; ind < ntags; ++ind) {
38 + m_tags[ind] = jtags[ind].asString();
39 + }
40 + }
41 +
42 + bool TagSelector::operator()(const input_pointer& in, output_pointer& out)
43 + {
44 + out = nullptr;
45 + if (!in) {
46 + log->debug("see EOS at call-1", m_count);
47 + ++m_count;
48 + return true; // eos
49 + }
50 +
51 + if (m_tags.empty()) {
52 + log->warn("TagSelector configured with no tags, passing through input frame");
53 + out = in;
54 + return true;
55 + }
56 + }
```

- To prevent overmemory usage by spliced data pipeline, we need a filter so that only gauss/weiner results will be saved
- A new function named TagSelector is developed and tested

# TagSelector for spliced pipeline



- The memory consumption measured using activity logger based on top command
- The TagSelector didn't work as it is designed for

# Plans

- Debugging TagSelector and make it work as expected
- Make pull request
  - Wire-Cell Toolkit: TagSelector
  - dunereco: updated jsonnet configuration files for PDHD and PDVD
- PDVD tasks
  - Working with Haiwang to solve the time offset issue at its source
  - Running a small PDVD sample production for the DNN training