

EICROC status and plans monthly meeting 19 mar 26

F. Bouyjou, H. Chanal, F. Dulucq, M. El Berni, S. Extier, M. Firlej, T. Fiutowski, K. Guillosoy, F. Guilloux, M. Idzik, N. Kachkachi, B.-Y. Ki, C. de La Taille, J. Moron, D. Marchand, L. Royer, N. Seguin-Moreau, L. Serin, A. Shama, A. Soulier, K. Swientek, D. Thienpont, A. Verplancke

EICROC0A/B/1/1A status

- Chips arrived last month, assembled and wire-bonded beg. of march
- 2 testboards of each 0A/0B/1 : all powered OK
- Issues with the slow control, understood only yesterday
 - Issue with decoder => all columns addressed simultaneously
 - Work-around tested on EICROCOA now to look at data and performance
 - Simple metal fix possible on the wafer on hold
 - Wait to look at the rest of measurements before releasing fix
- Work on EICROC2 digital part advancing nicely in Clermont

