

- EICROC
 - BNL setup
 - Prithwish connected the EICROC1 Testboard ↔ Mike's Adapter ↔ RBv1
 - just to check mechanics of the connectors (no power was applied!) – worked well
 - Zoom today with Omega to go over details on how to connect the Testboard to ZCU106
 - we cleared up all outstanding questions with Kinann from Omega ✓
 - RBv1 connected to AXAU15 – getting there... ⌚
 - ZCU106 progress not great this week (too many other things...) [TL]
 - but Step 1 complete: small VHDL test snippet loaded to blink the LEDs ✓
 - Step 2: start on real EICROC1 readout FW
 - EICROC power is going to be 2 mW/channel (stated by Christophe)
 - the chip designers seem to be giving up on trying for 1 mW/channel (?)
 - not great news for us → we should change all our power calculations accordingly
- Auxiliary AC-LGAD Detectors
 - B0: first RB placement meeting
 - this will continue for a few iterations (Monday)
 - Lumi Tracker – also discussions about RBs
 - the hope is that we standardize on a single RB flavor (RB6 looks likely)
 - aim is to arrive at a reasonable readout architecture for all 4 detectors
- TOF PDR Page Turn (Tue & Wed)
 - lots of good discussion throughout the mtg especially for BTOF
 - FCFD power, board sizes, FPC, etc.

