

# Quick Status (TL, 05-Jun-2026)

- EICROC1 Testboard Connected to ZCU106 at BNL
  - FW is working fine via the USB serial console of the PC
  - I2C testing wrapping up
    - needed help from Kinann/OMEGA due to the non-standard protocol
  - Some issues
    - I2C register reads don't work for this "bad" EICROC1 version
      - but writes works so we need to do this blindly – oh well...
    - I2C issues with pixel row vs column
      - but we can still set the ASIC up in 1/8ths of the pixels at a time
    - both issues expect to be fixed with the forthcoming metal layer fix...
  - Starting on the ASIC Readout using the "old" EICROC0 protocol ⇒ Monday
    - hope to continue having Kinann's support which is invaluable!
  - Once this is understood we start with the Fast Command based readout
    - but still with the FPGA until all protocols are understood and working → IpGBT RBv1 follows
- FCFD & its Testboard
  - meeting this week with Artur et al and TL to discuss interfaces with the FCFD Testboard and RBv1
    - discussions ongoing ⇒ we plan to continue meeting as required