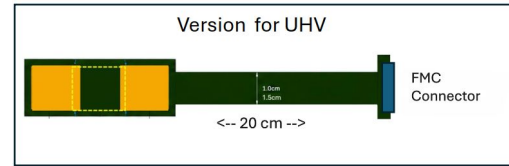


- EICROC1 Testing at BNL

- Commissioning with ZCU106 [TL]
 - Readout of data starting to work
 - both with the legacy EICROC0 interface and the new IpGBT-compatible interface
 - but some observations not understood → need discussions with ASIC designers
 - Continuing with ZCU106...
- Rachel (Rice student) will spend ~2 months at BNL working with Alex and TL
 - will be involved in the EICROC1 testing as well as ASIC configuration specifics
- BNL RBv1 test stand finally works ✓ [Prithwish, TL]

- ASIC Modules

- Roman Pot 3x EICROC first prototype module discussed in meeting with Jack, Alex, TL
 - 20cm long with FMC connector at the end ⇒



- FTOF EICROC Module: decided to start slowly with just 1 ASIC to make sure we understand all its features and have ample space for testpoints and ease of wire & bump bonding [Mike, Wei, TL]
 - schematics ongoing [Mike]

- DC/DC Converter Session in the DAQ/Electronics Weekly Meeting [Fernando]

- all/most groups plan to use bPOL48
- 70% efficiency seems to be a reasonable number [Tim]
- will hold another session in fall [Fernando]