

● EICROC

- Roman Pots 3x ASIC Module is in good progress [Jack]
 - 1st pass of schematics locally reviewed [Jack, TL]
 - 2nd pass expected next week [Jack, Alex, Mike, TL]
 - Board is designed so it can be tested by ZCU106, KCU105 and by RBv1 using the existing adapter at first
- EICROC1 with ZCU106 taking data and in use by Rachel
 - currently looking at pedestal trends and noise → more next week
- Common EICROC0 and EICROC1 ZCU106 firmware moving along [TL]
 - but slowly because we need another ZCU setup – under discussion
- Some progress made on fast ZCU106 Readout [Prithwish]
 - waiting for TL...
- EICROC1 ASICs and 2 Testboards have been shipped from OMEGA and seem to have arrived at BNL – ongoing...

● FCFD

- Artur & Fermilab team made a Testboard for FCFD1.2
- schematics are available and we are looking into how to make an adapter from their Testboard to our RBv1 [Mike, TL, others]

AC-LGAD Readout Status
17Jul2026
TL

