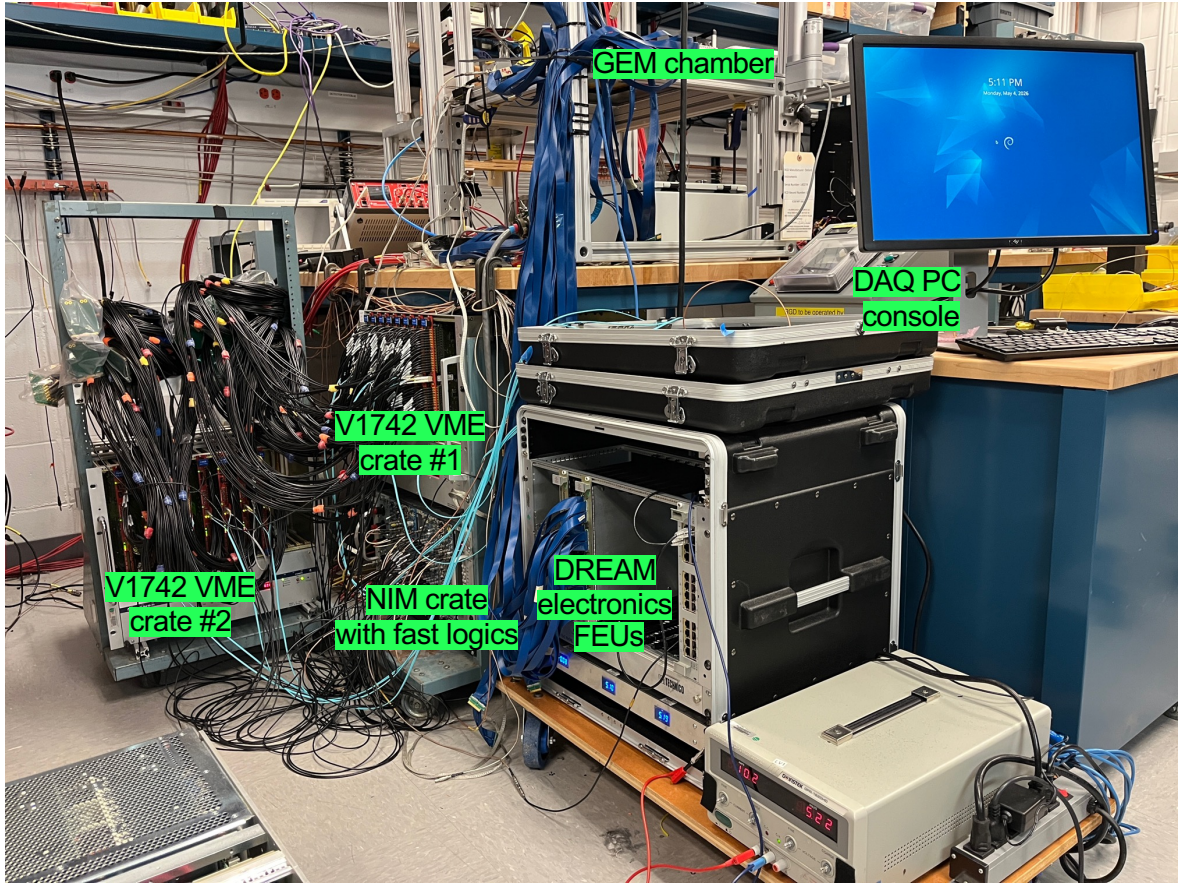
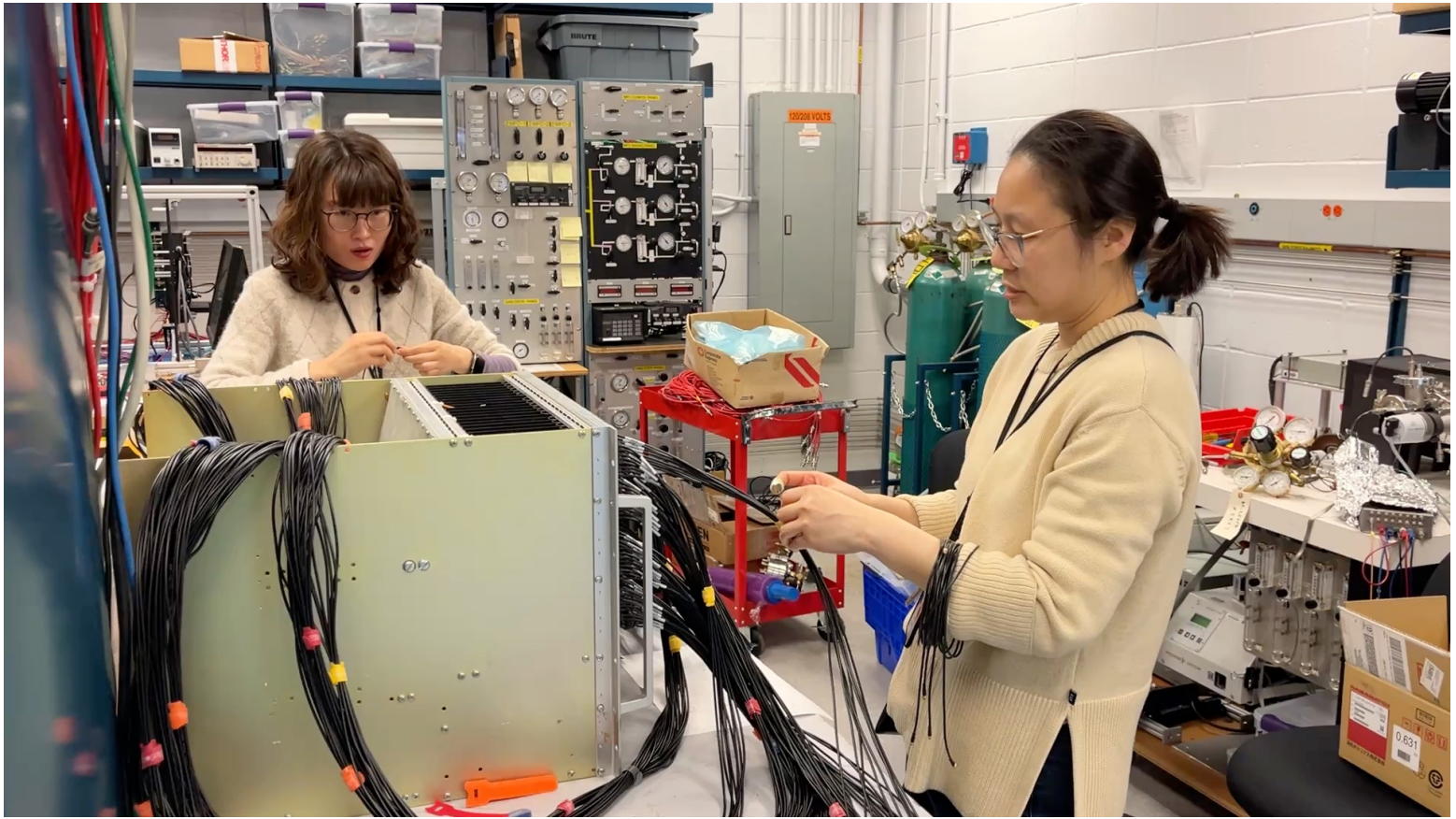
Update on readout hardware integration: V1742s

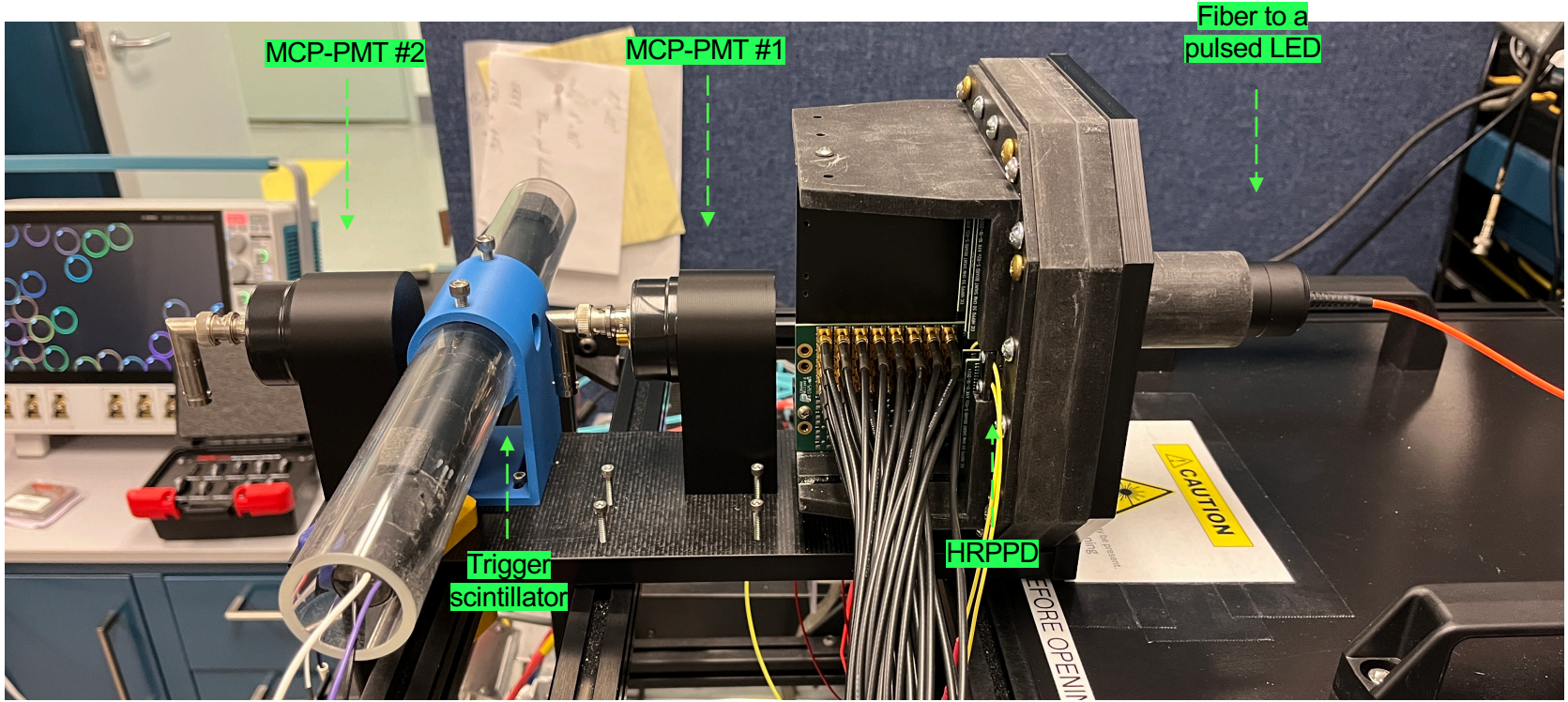


- A full 8+8 V1742 configuration
  - Runs stable for several hours
  - ~95 Hz (sequential; safe mode)
  - ~170 Hz (card-wise parallel; stable)
  - ~400 Hz (link-wise parallel)
    - By now, acquired ~100M events
  - Have ideas how to get to ~1kHz rate
- Received missing MCX adapters
  - All digitizers but one are fully cabled
- Got a brand new VME crate
- Limited testing to DREAM + digitizers running at the same time though

Update on readout hardware integration: V1742s



MCP-PMT / HRPPD / scintillator table



Here shown on a test bench with HRPPD connected to a pulsed LED

Base plate & MCP-PMT / HRPPD / scintillator table

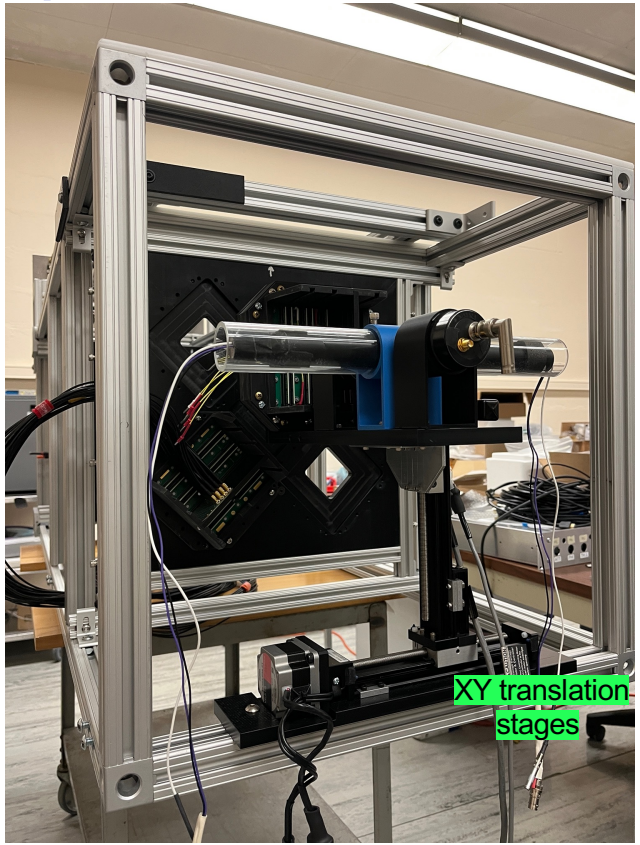
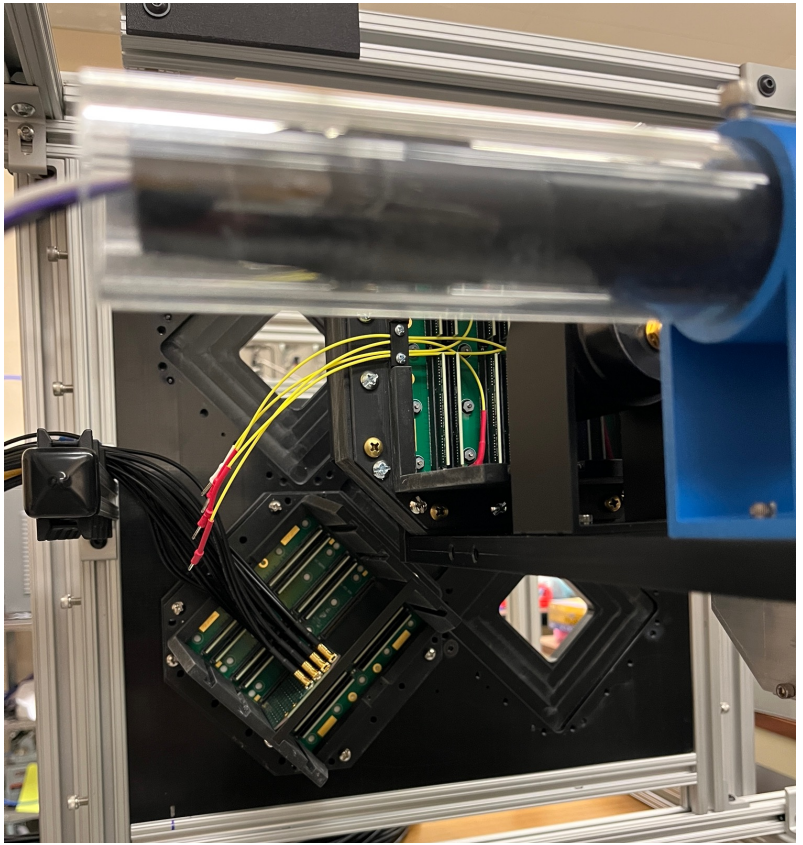
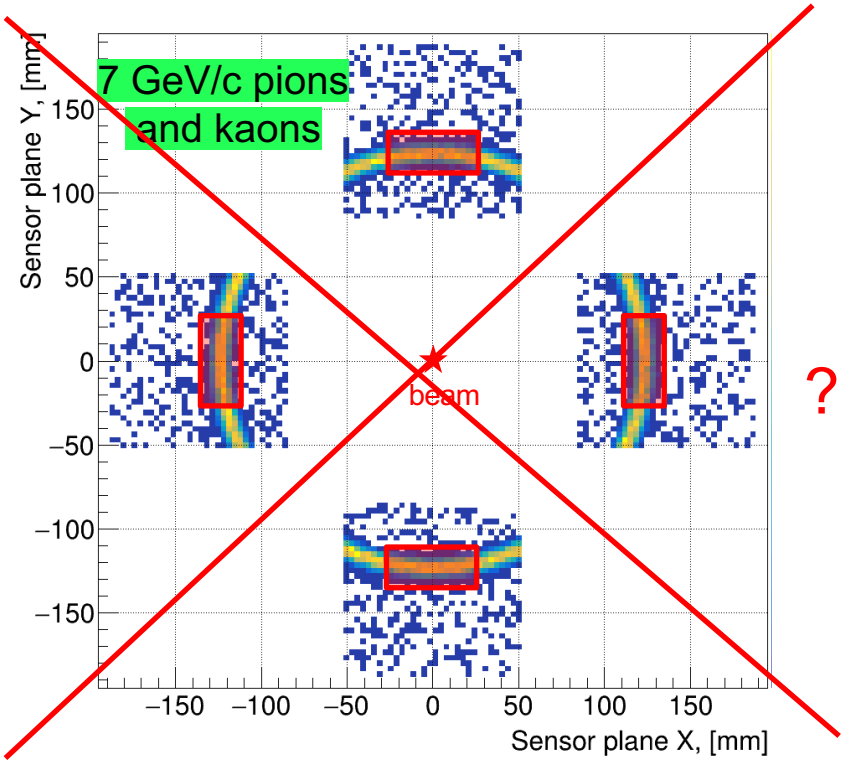


Table mounted on the 8020 chassis



Base plate with two “HRPPDs”

Two possible configurations



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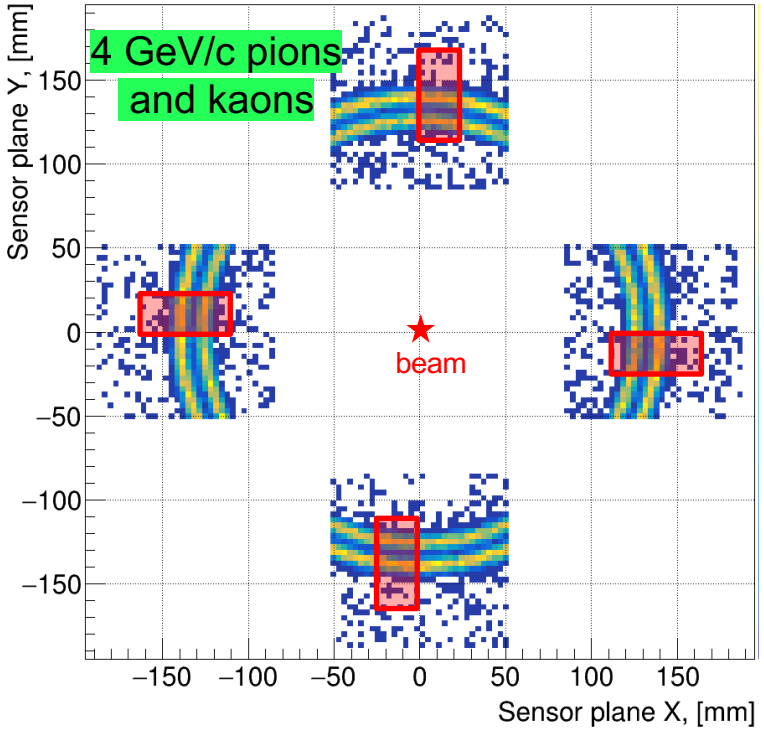

"SPS acceptance"

(~1/4 azimuthally; saturated rings; ~403mm exp. volume)

"PS acceptance"

(2x smaller acceptance; π/K; ~453mm exp. volume)

Same HRPPD instrumented area (no need in re-cabling; just a 90^0 rotation of every HRPPD)