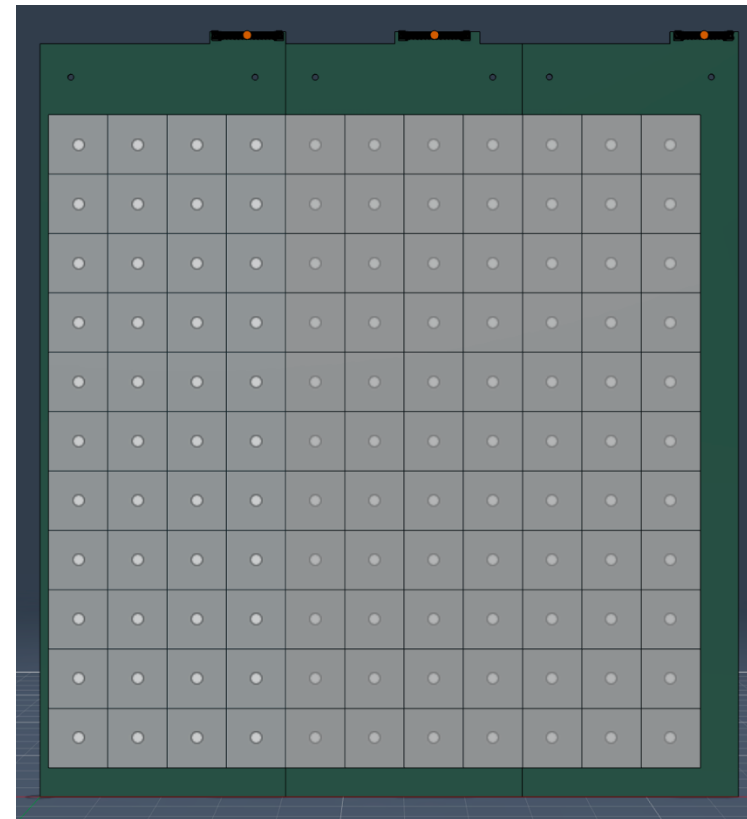
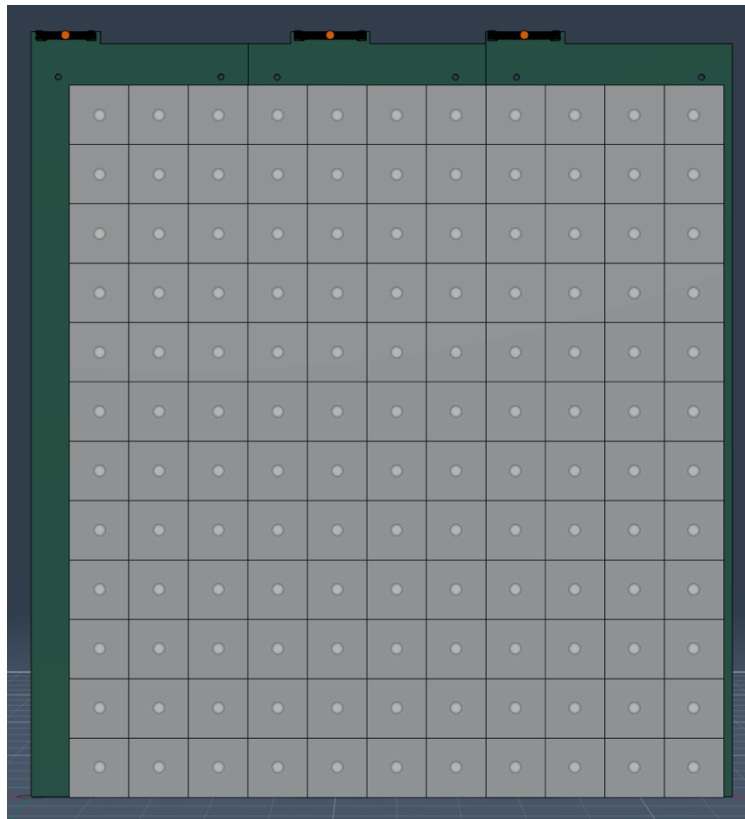


ZDC PCB Update

Sean Preins

6/1/26

- ZDC design requires 6 PBCs (3 for even layers, 3 for odd layers)
- Connectors need to be offset between even and odd layers
- There needs to be sufficient room above the iron for capacitors
- Two holes will be put into each PCB to aid with removal
- Including pads for LEDs in each channel



- Unlike prototype, we will attempt to use connectors with through-holes
- Will need two connectors per board, which unfortunately will make connector offset difficult
- Working on general layout now, then will compactify

