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# eRD109/PED update

PRESENTED BY

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ORNL



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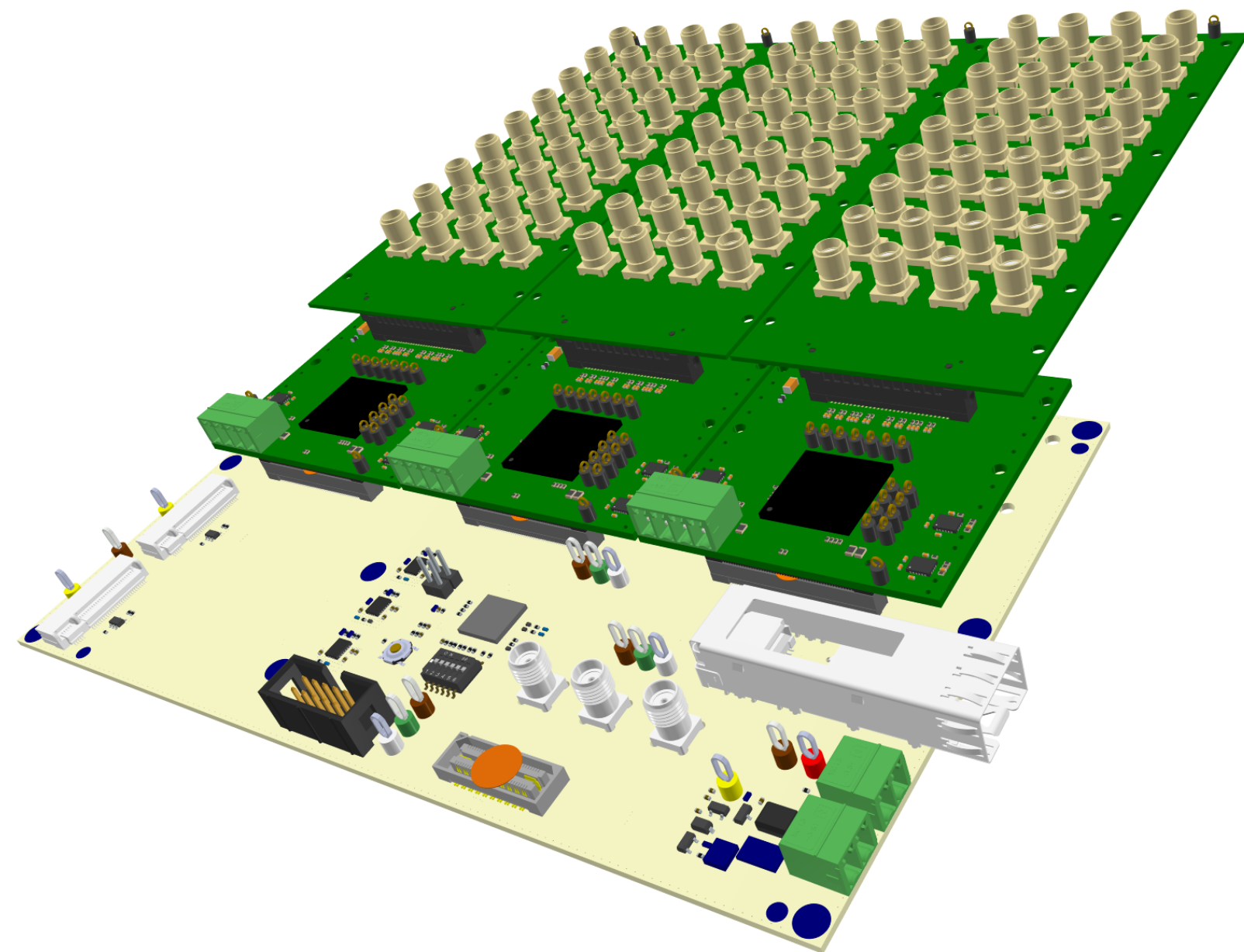
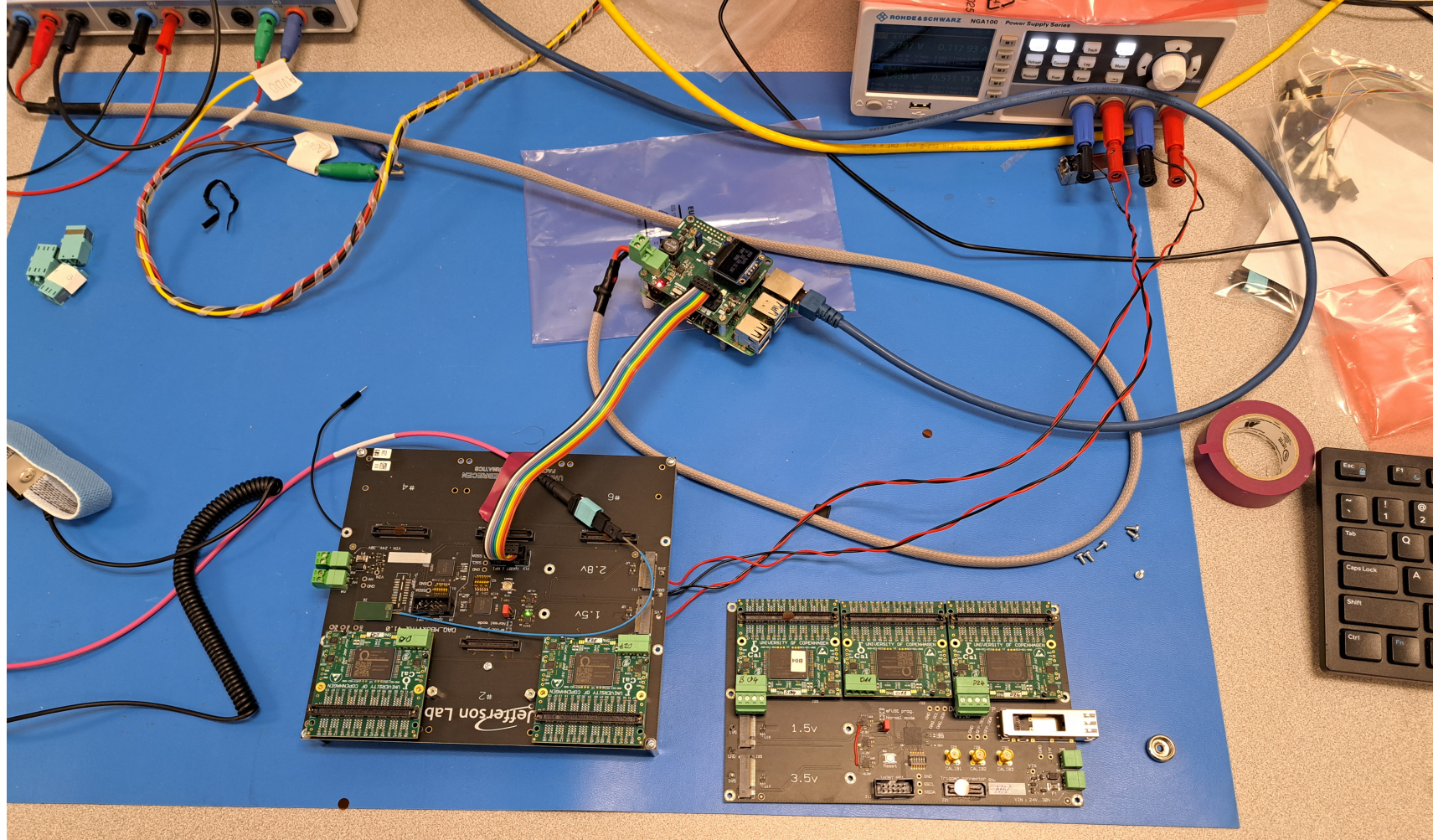
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# Ready to go

H2GCROC - LpGBT connection under testing:

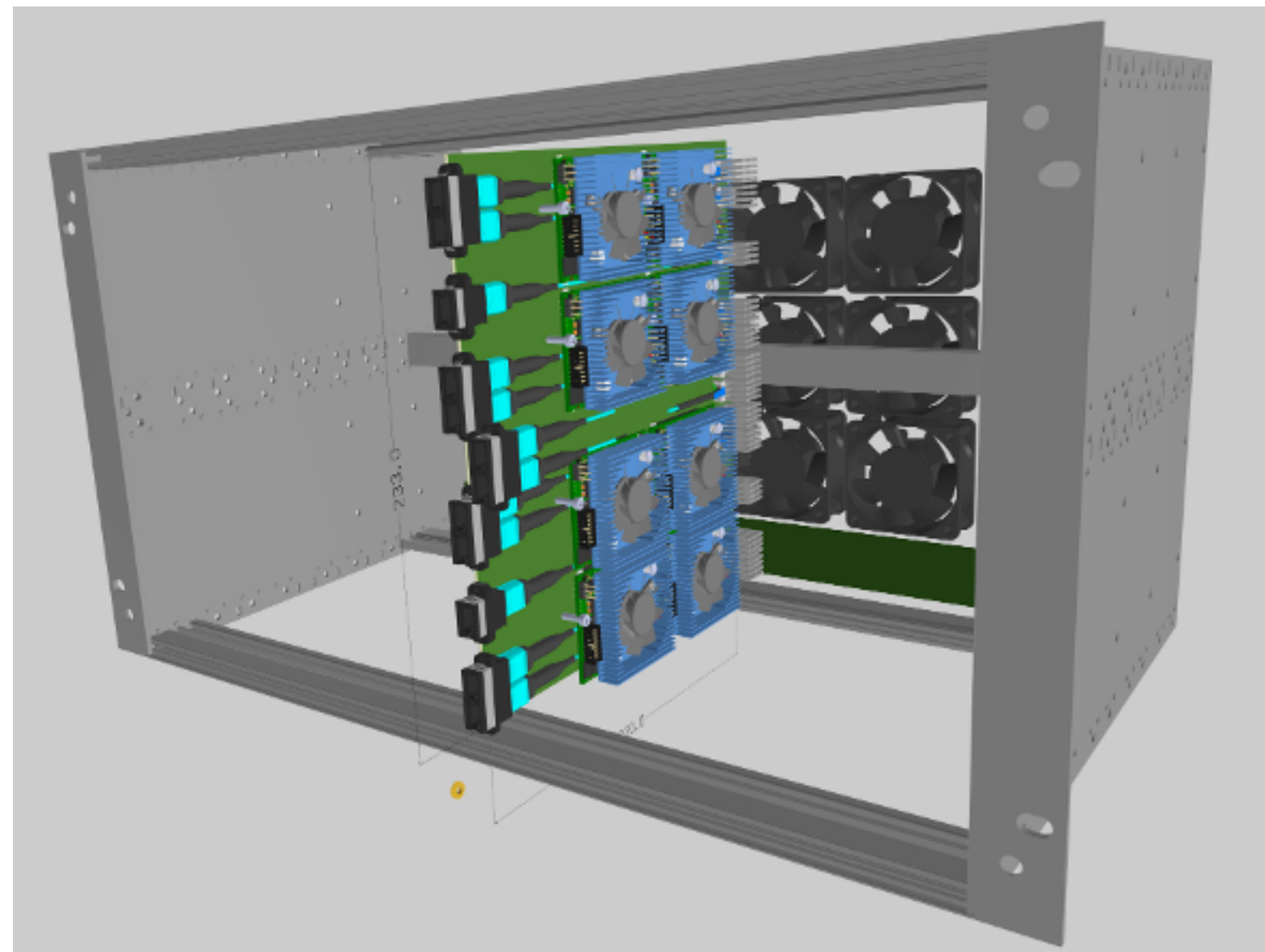
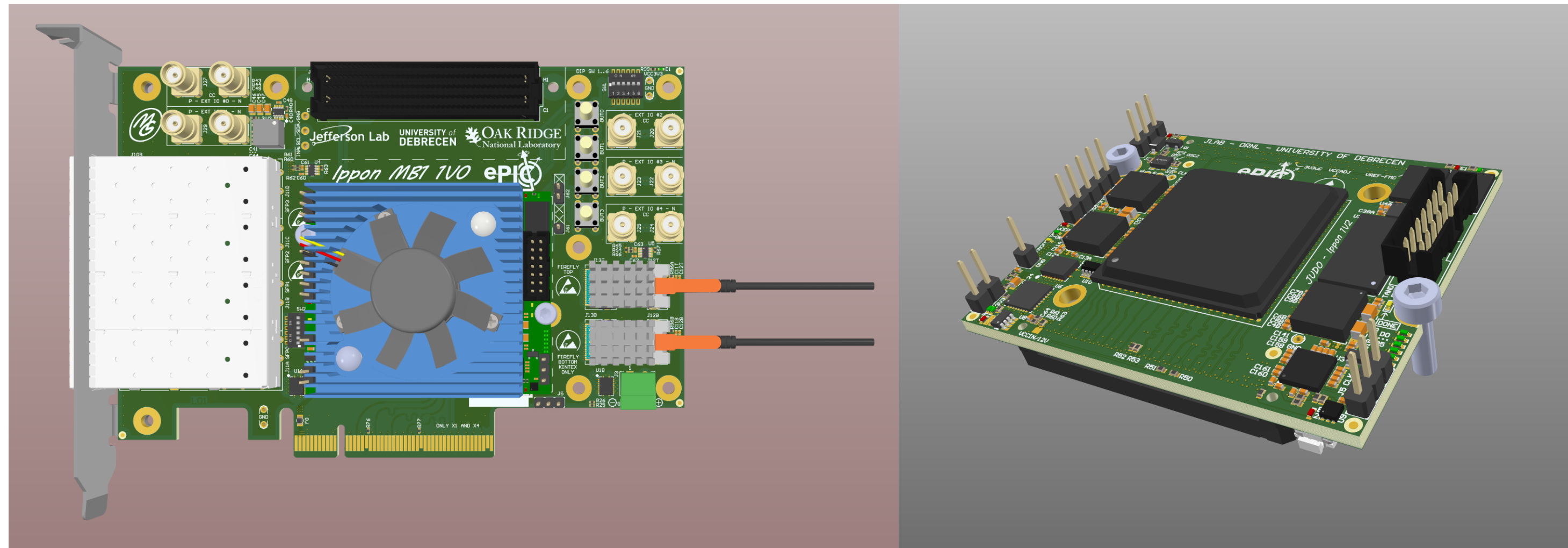
- We can send the fast command from LpGBT to H2GCROC, receiving the “idle” from at the FPGA
- I2C still under investigation, sometimes the LpGBT doesn't receive the acknowledgment from the H2GCROC
- Need the FELIX boards (now borrowing from Jo Schambach)



We received 2 CALOROC<sub>1A</sub>:

- The Mezzanine (similar then the H2GCROC, but for CALOROC) is ready for production
  - This is also compatible with CALOROC<sub>1B</sub>
- Interface board for UFL and MCX connectors is done too
- Readout should be ready by the time we get the chips

# gRDO



- The gRDO mezzanine is designed, ready for production
- The first mother board is ready for production:
  - Firefly added for SVT/other detectors
  - SFP+ is added too
  - FMC connector
  - PCIe
- First look also in the final arrangement in a crate:
  - 8 of these mezzanines can fit into a final gRDO board
  - Mezzanines from now can be reused in the final experiment