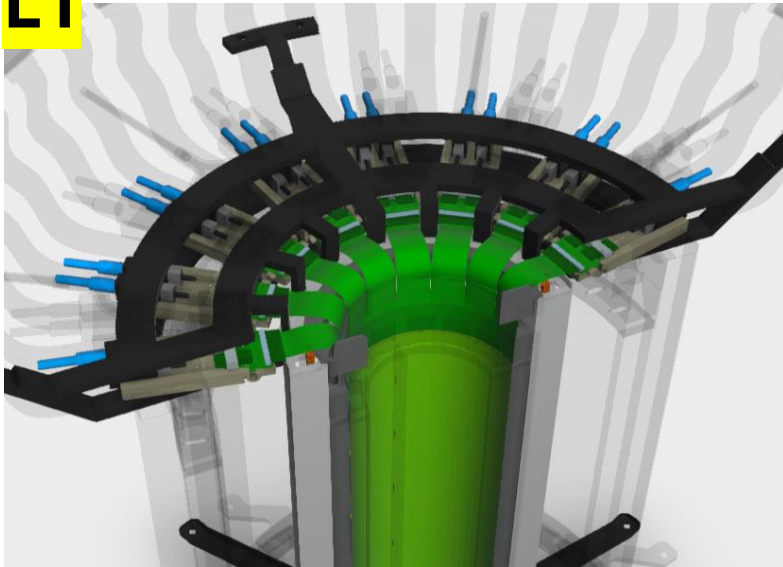


SIBs positioning recap

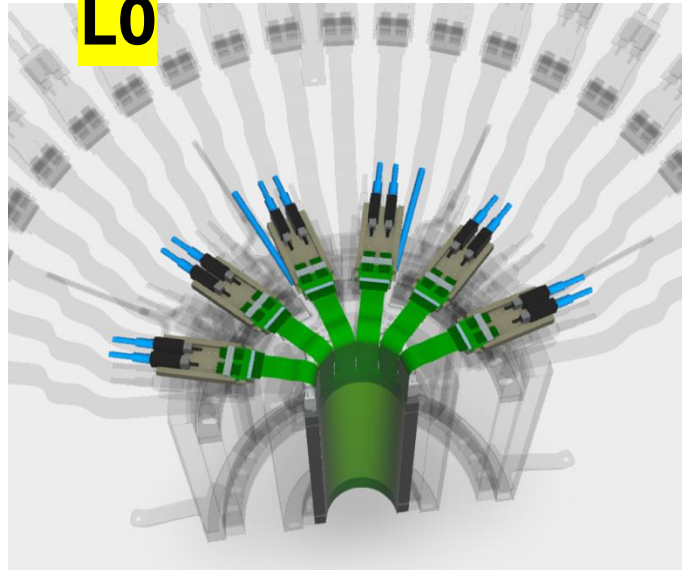
RT

SIB boards positions inside the envelope

L1



L0



FPCs length:

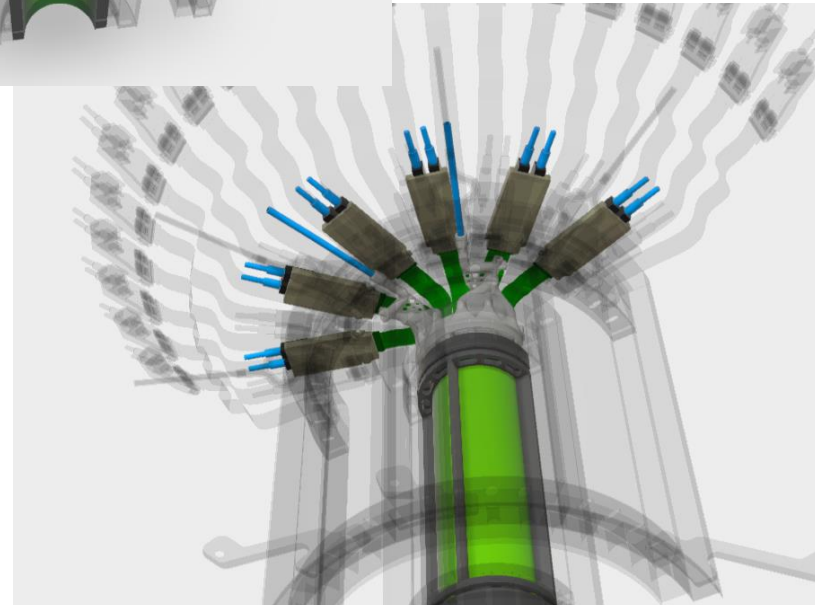
L0 147 mm



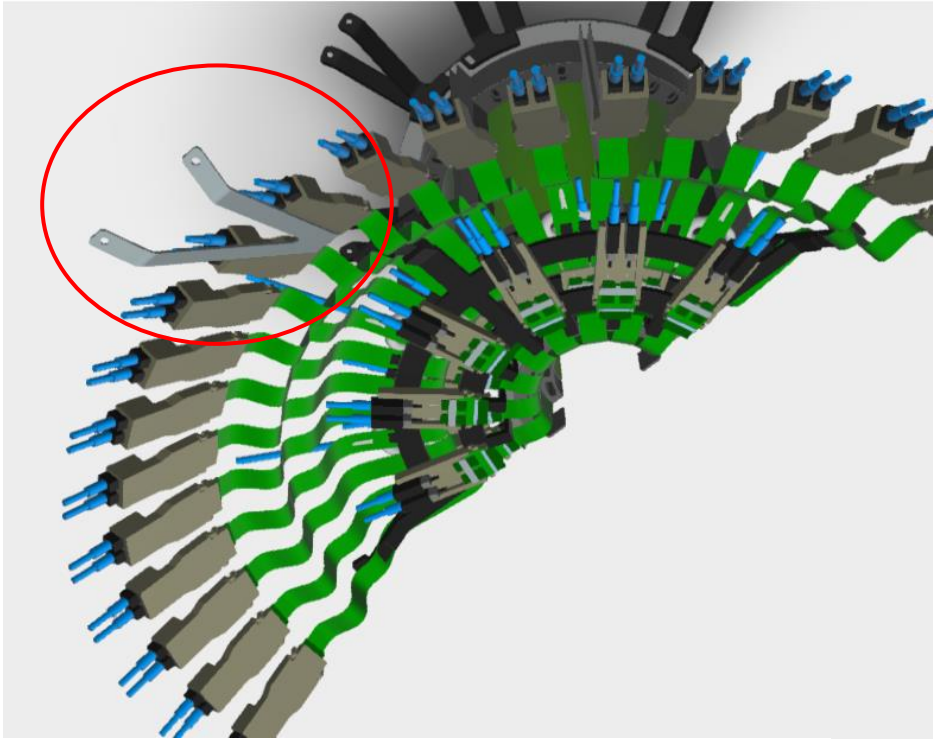
L1 113 mm



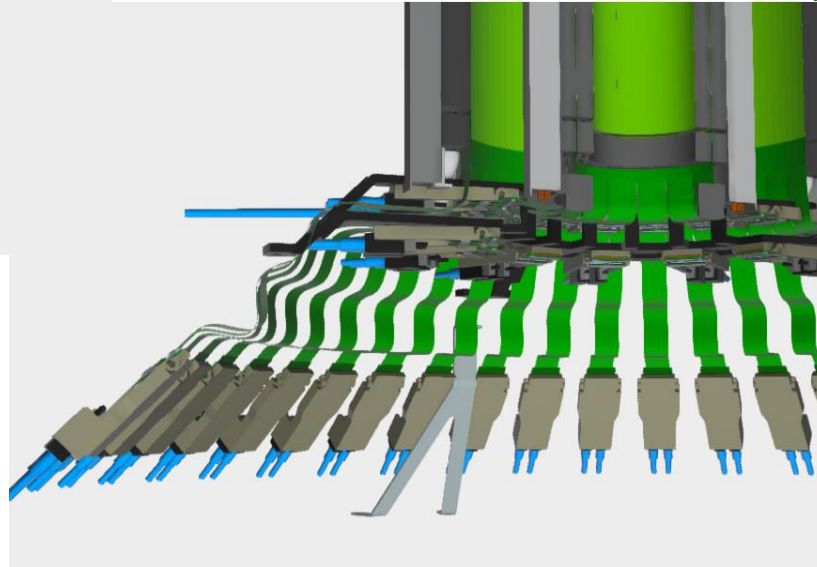
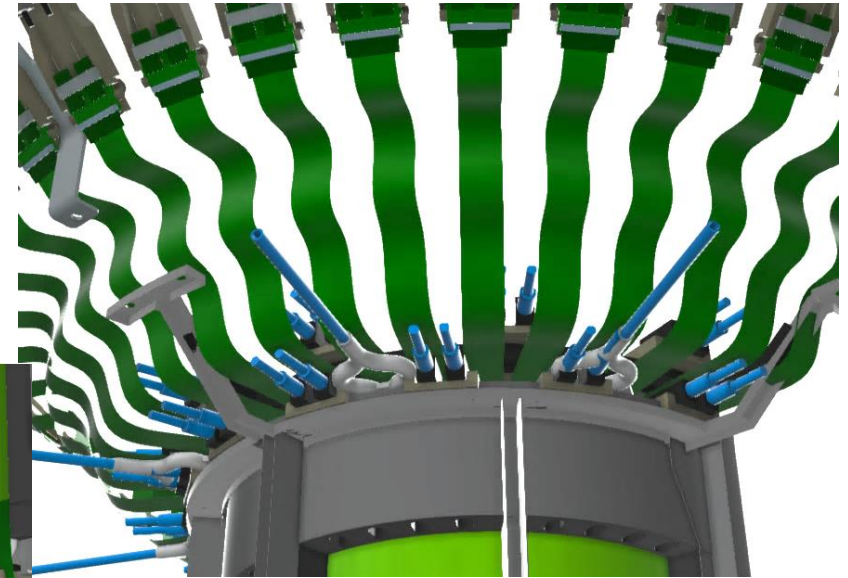
L2: see next slides



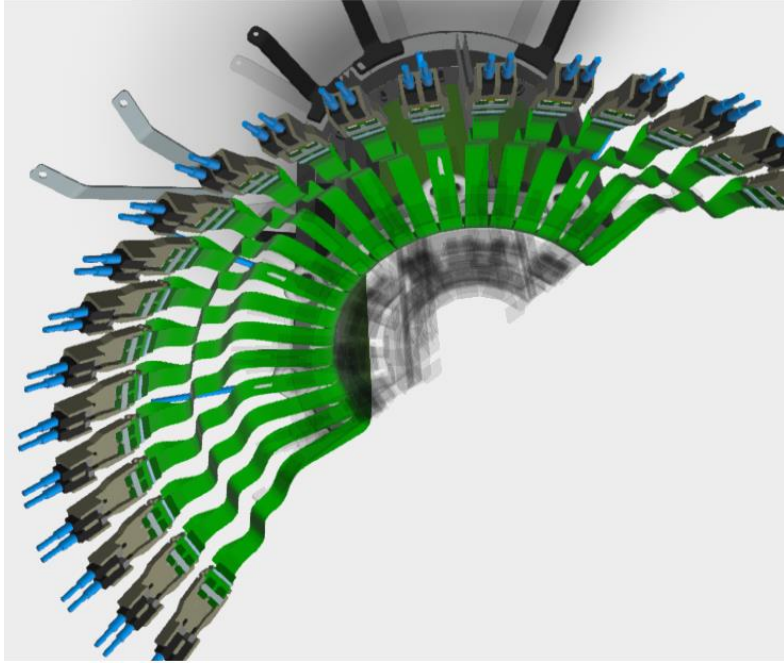
Positioning L2 SIBs – take 1



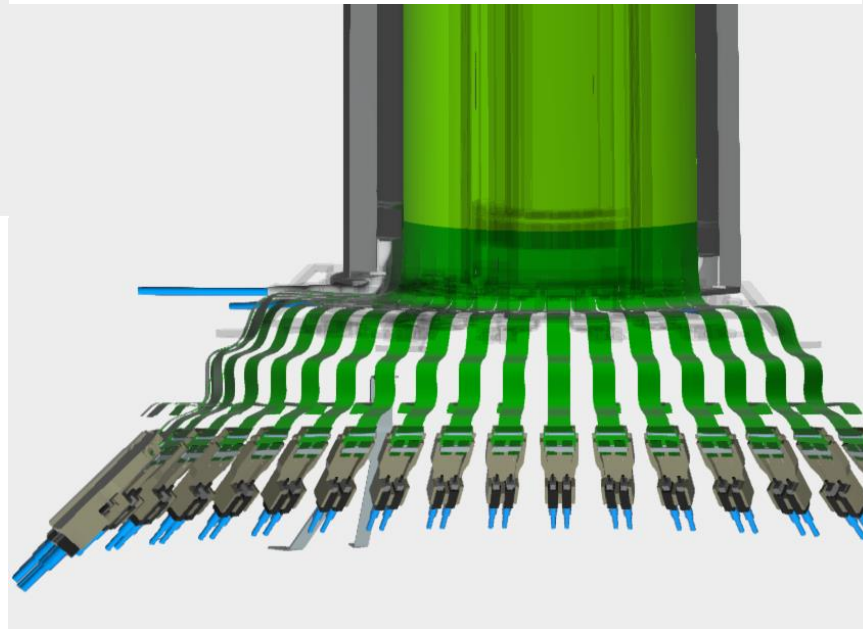
FPCs length: **299 mm**



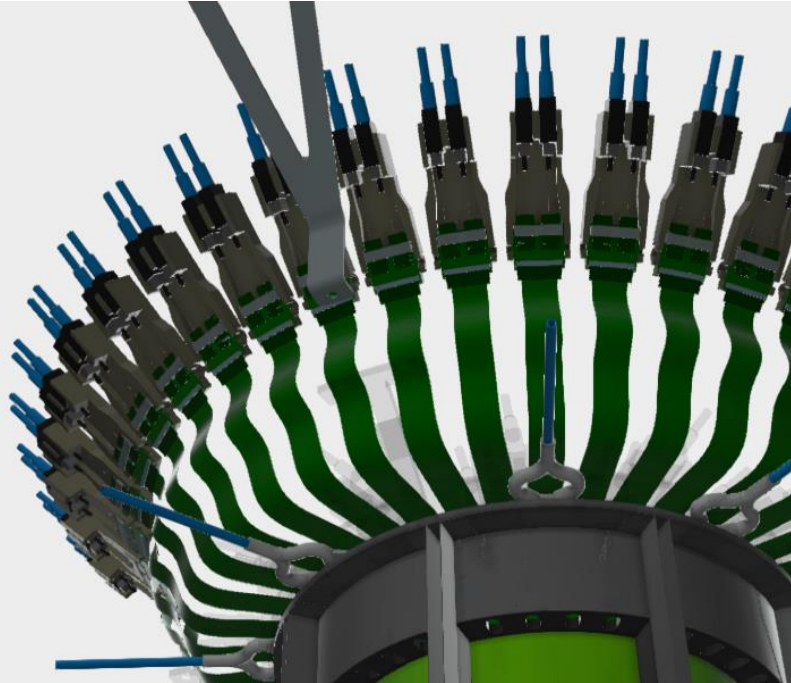
Positioning L2 SIBs – take 2



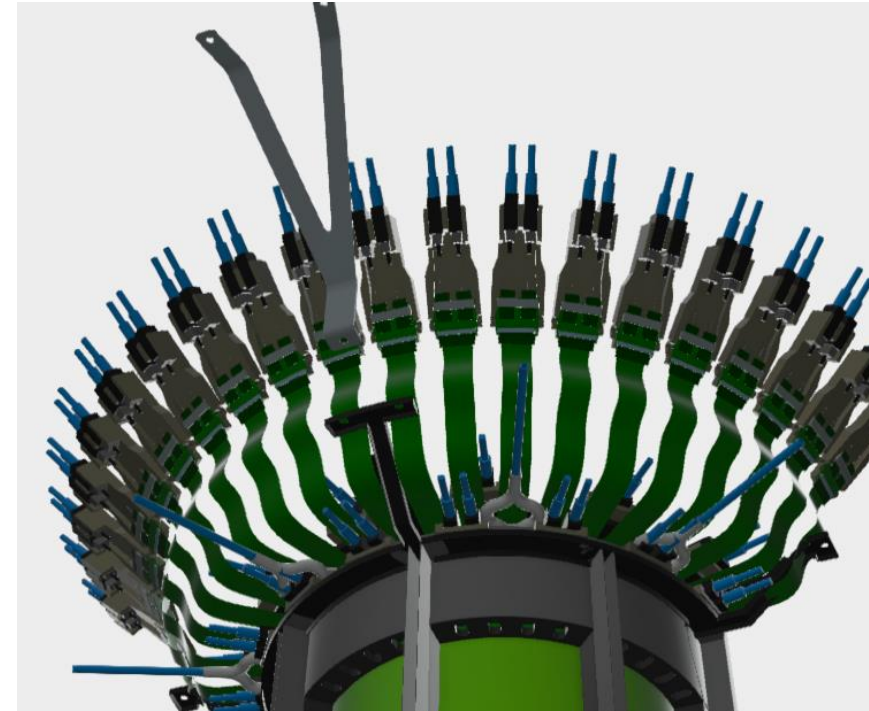
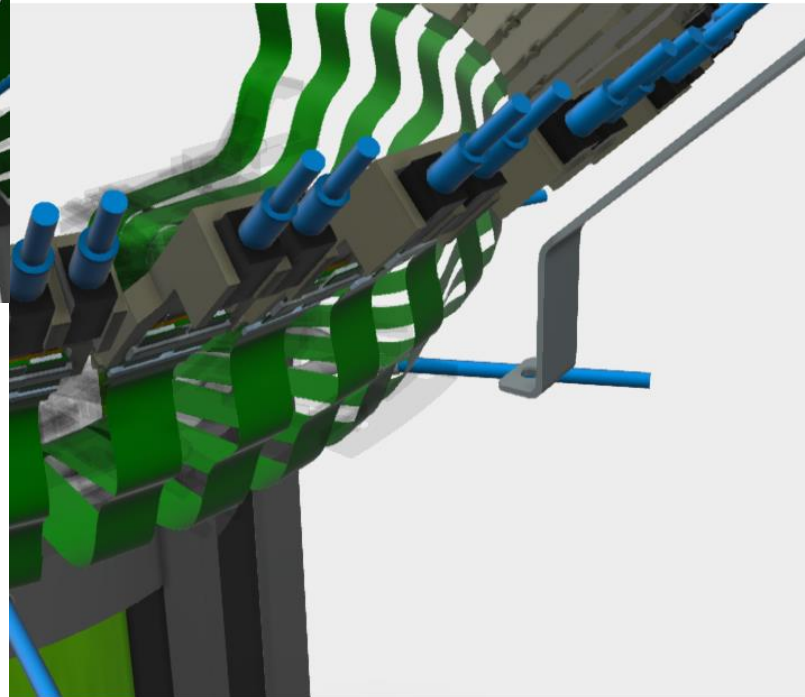
FPCs length: **289 mm**



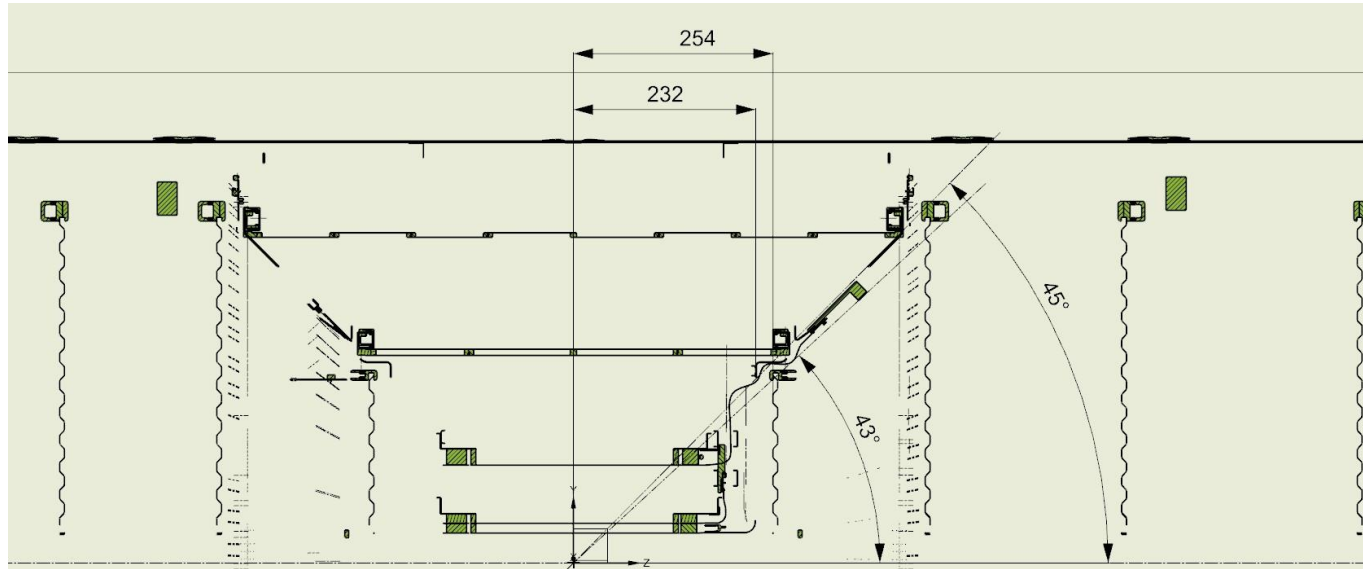
Positioning for L2 SIBs – take 3



FPCs length: **249 mm**

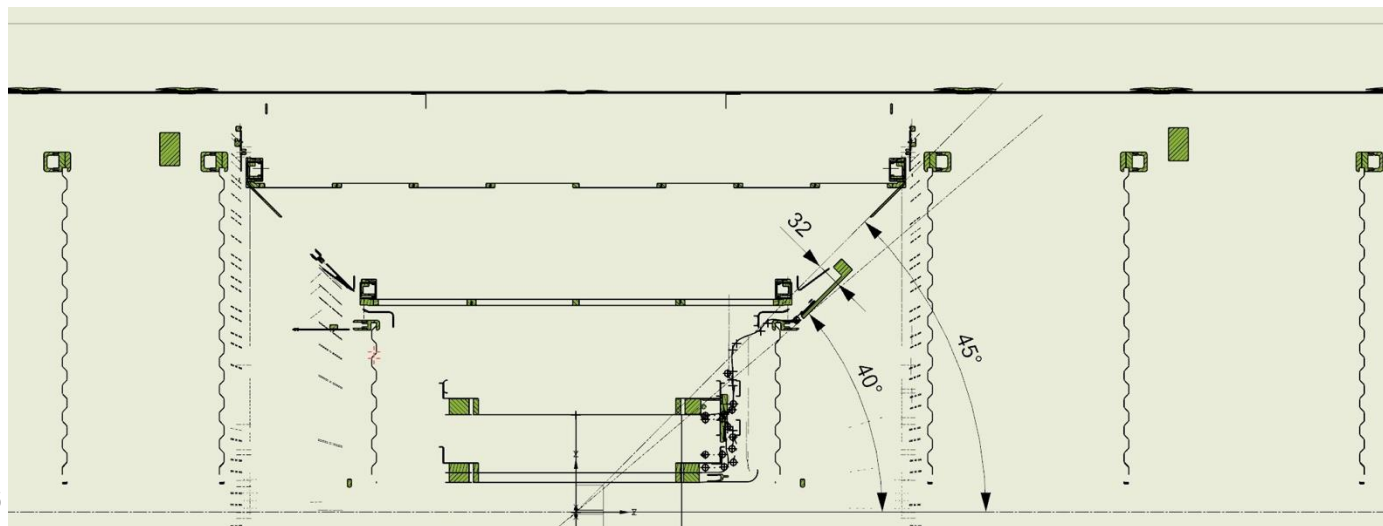


New positions for L2 SIBs – take 2-3 side by side



← #2

FPC length: 289 mm



← #3

FPC length: 249 mm

NB: support of SIB board to be revised in this case

Option “All outside the envelope”

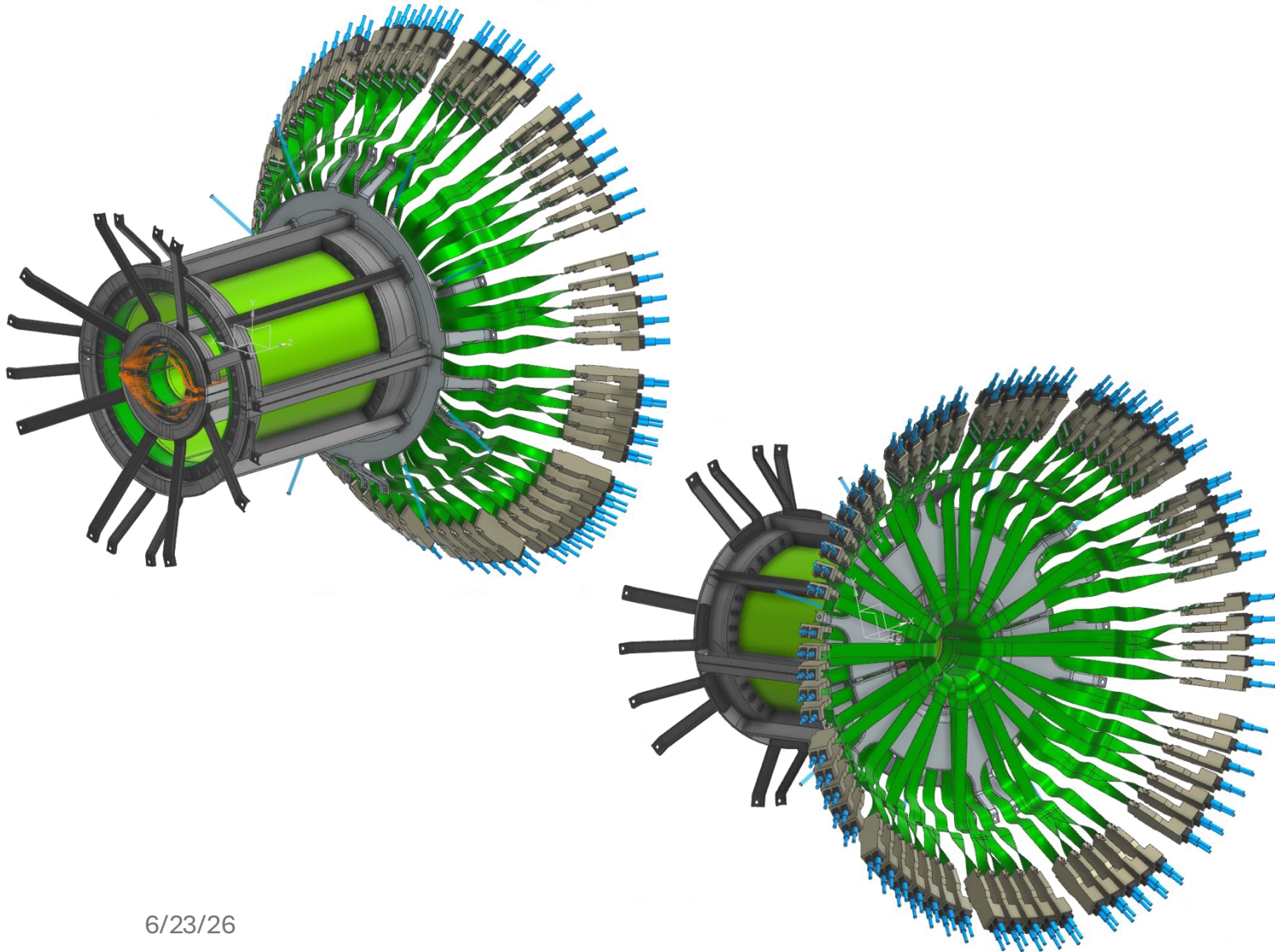
FPCs lengths:

L0 415 mm

L1 387 mm

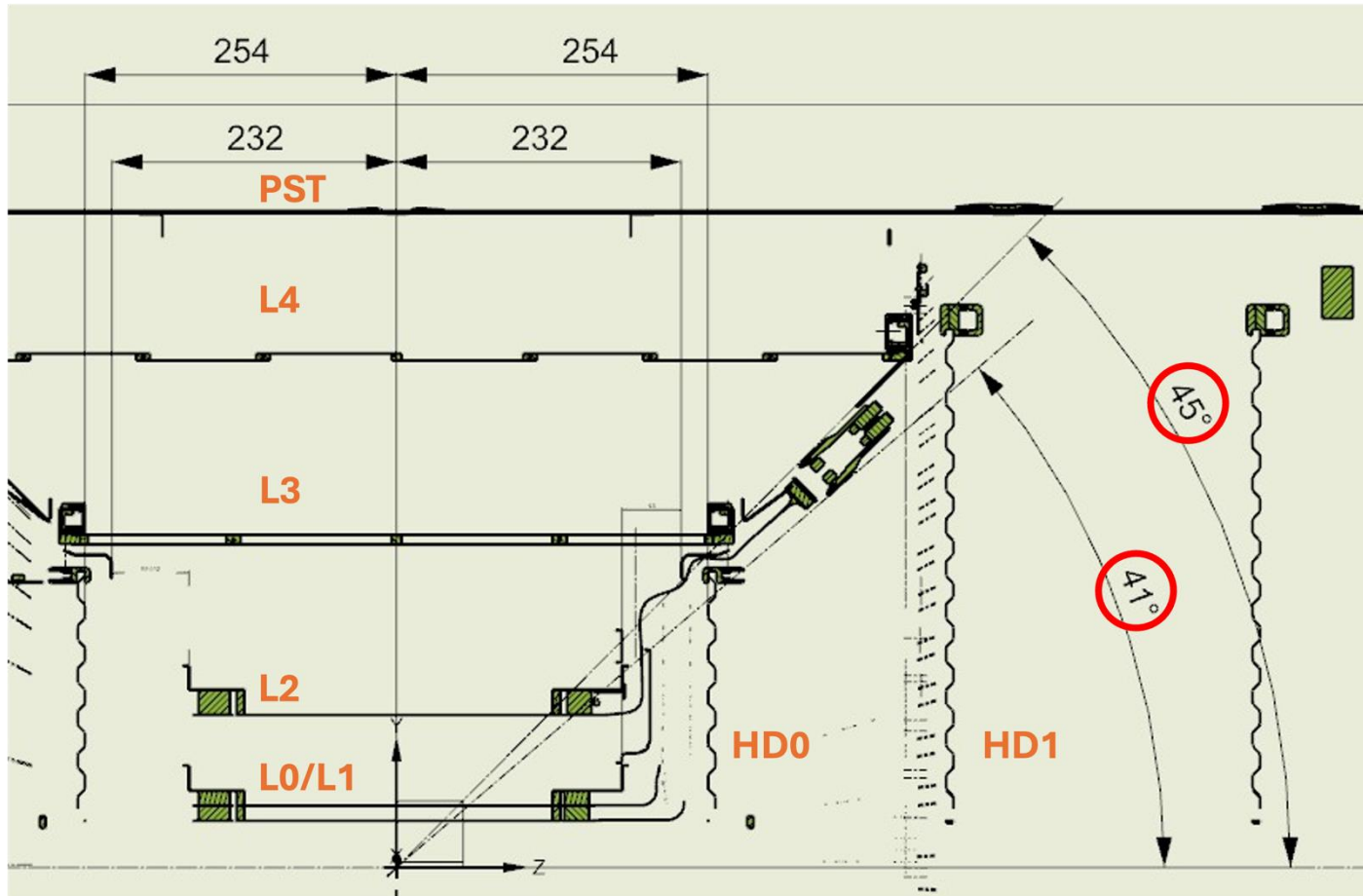
L2 310 mm

typical values, few mm variations happen within the same layer



SIBs pigtails currently at 33.5 mm (old heritage)
25 mm → keep current options
55 mm → maybe some repositioning needed for interference (see next slide)

Cross sections – check the shadow!



FPCs lengths:

L0 415 mm

L1 387 mm

L2 310 mm

typical values, few mm variations happen within the same layer

Mock-up for service integration tests

All parts 3D printed
Easily adaptable to design modifications

