

Session Program

8-9 Oct 2026

SVT Working Meeting

***ASICs: wafer probing, bench tests,
irradiation and test beams***

Lawrence Berkeley National Laboratory, Building 66-316
1 Cyclotron Road Berkeley, CA 94720

Friday 9 October

09:00

ASICs: wafer probing, bench tests, irradiation and test beams

Session | **Location:** LBNL, Building 66-317, 1 Cyclotron Road Berkeley, CA 94720

09:00–09:25 Wafer probing status & plans

Speakers

Gian Michele Innocenti, Gian Michele Innocenti

09:25–09:50 Sensor QA thoughts

Speaker

Tomo Inada and Hide Otono

09:50–10:10 Baby Mosaix bench tests

Speakers

Tucker Hwang, Tucker Hwang

10:10–10:30 Ancillary ASIC tests

Speaker

Shirsendu Nanda

10:30–11:00 Coffee break

11:00–11:20 Flexible Printed Circuits for Disks

Speaker

Zhengwei Xue

11:20–11:40 Beam test planning

Speaker

Zhenyu Ye

11:40–12:00 Discussion

Speaker

Zhenyu Ye

12:00