

● EICROCx Testing

- issue with bad ADC values resolved
 - power supplying problem (Alex can explain the details)
 - solution: power it separately with 2 PS: 1 for analog 1.2V, 1 for digital 1.2V
 - *don't use the Testboard's common 3.3V scheme!*
- 2 EICROC1 boards tested & available w wirebound ASIC
 - 1 will stay in BNL Physics [Alex, TL, Prithwish]
 - 1 will be shipped to Rice [Rachel]
 - once the 3rd has been tested with the ZCU in the lab we will modify it and make it ready for RBv1 & IpGBT readout
- common FW for both EICROC1 and EICROC0A almost finished
 - first checking EICROC1 (done) followed by EICROC0A (started)
 - common readout software in git

● BTOF SH progress

- main issue was the height of the SH, specifically inductor coils dimensions – *we needed a redesign from 15 mm height to 10 mm*
- Mike sketched a new Power Board using existing CERN's bPOL48 modules which use slimmed down PCB inductors
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