2nd EIC Yellow Report Workshop Pavia University (held online), 20-22 May 2020

Parallel session Inclusive, SIDID, Jet & HQ

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Tracking WG

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1

Ongoing activities and available results

Ongoing activities:

- working on the following main deliverables:
 - ✓ evaluate all-silicon vs hybrid (silicon & gaseous) trackers
 - ✓ compare realistic alternatives (TPC, MPGD options) for gaseous detectors, barrel and forward
- preliminary performance studies (mainly EicRoot-based simulations):
 - ✓ central region Si-vertex + TPC + Fast MPGD Layers advanced
 - ✓ Cylindrical Micromegas (MPGDs) just started
 - ✓ endcap region GEM (MPGDs) trackers just started
 - ✓ all-silicon (barrel) tracker + forward/backward silicon disks advanced
 - ✓ comparisons all-silicon vs BeAST (Si-vertex + TPC + MPGDs) concepts ongoing
- effort on Fun4All and ESCalate frameworks:
 - ✓ first implementations of all-silicon tracker in Fun4All and G4E ongoing
 - ✓ plan to implement realistic material and services for all the tracking detectors just started

Available results:

- relative momentum and pointing resolutions (in different configurations and options)
- angular resolutions at DIRC (Si-vertex + TPC + Fast MPGDs different options)

Outline for today's discussion

Hybrid/gaseous detector options:

- central region Si-vertex + TPC + Fast MPGD Layers:
 - ✓ 3 options studied: no MPGDs + 2 different configurations with MPGDs before/behind DIRC
 - ✓ angular resolution before and behind DIRC position, relative momentum resolution
- cylindrical micromegas:
 - ✓ alternative to TPC, 2 different layer arrangements studied
 - ✓ Angular resolutions at DIRC position, relative momentum resolution
- material budget considerations
- pros/cons summary table

Silicon detector trackers:

- all-silicon tracker option:
 - ✓ tapered all-silicon in Fun4All, first estimates of the angular resolutions
- all-silicon and Si+TPC tracker studies
- pros/cons all-Si vs hybrid trackers

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Si-vertex + TPC + MPDGs

Matt Posik, for eRD6



Detector setup:

- Si-vertex tracker: 4 layers of 20 $\mu m \times 20 \mu m$
- TPC: No distortion corrections, field cage and end Ο cap materials included
 - Transvers Dispersion: 40 $\mu m/\sqrt{D}$
 - Transverse Resolution: 90 µm
 - Longitudinal Dispersion: 1 $\mu m/\sqrt{D}$
 - Longitudinal Resolution: 500 μm ٠
- MPGDs in μ TPC mode: 100 μ *m* × 100 μ *m* (ϕ × *Z*)
- 3 configurations investigated
 - No MPGDs .
 - One MPGD layer in front of DIRC ٠
 - 2 MPGD layers sandwiching DIRC ٠



No MPGD



2 MPGDs "sandwiched" DIRC



Si-vertex + TPC + MPDGs

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Angular resolution $\Delta \theta$ before and after the DIRC:

- B = 1.5 T, 0
- Solid Markers: At DIRC (~82 cm) Ο
- Open Markers: Behind DIRC (~88 cm) Ο
- Significant improvement seen in angular resolution behind the DIRC with MPGD layers sandwiching it Ο
 - Angular resolution $\Delta \theta \sim 0.25$ mrad before DIRC 0



Si-vertex + TPC + MPDGs

Relative momentum resolution:

- One MPGD layer in front of DIRC significantly improves momentum resolution
- A second MPGD after DIRC slightly degrades performances because of multiple scattering in DIRC bar
 - However it is not really an issue as this data point is not needed for the momentum



Cylindrical Micromegas

- Barrel MPGD tracker as TPC alternative:
 - Curved MPGD tiles with low material budget
 - Micromegas technology is being used in CLAS12
 - Possibly readout 2D coordinates on a single layer
- Simulation and performance study are under the ePhenix context
 - ePhenix TPC is replaced with the tracker
 - R is from 20 to 80cm, 2 tracker configs are studied



X/X0 ~ 0.3% per layer Tracking WG 2nd

/er6 equidistant layers2nd EIC-YR Workshop / Pavia University / 20-22.5.2020

Qinhua Huang, CEA Saclay



6 layers arranged as 3x2

Cylindrical Micromegas

- Compare momentum/angular resolutions at DIRC (r=81.5cm) of different configs
 - Each point contains 10k π^- shot from (0,0,0) and with a constant θ =43°
 - Track reconstruction includes SVTX measurements: $\sigma(R/\varphi/Z)=5\mu m$
 - For TPC: σ(φ)=200μm, σ(Ζ)=500μm
 - For MPGD: σ(φ)=150μm, σ(Ζ)=150μm
- Vigorous R&D ongoing at CEA Saclay to verify a potential improvement of the performance with micro-TPC mode



Rey Cruz-Torres, Winston DeGraw - UCB

All-silicon angular resolutions





Tapered All-Si Tracker in Fun4All

Functionality added by Chris Pinkenburg to project momenta onto cylinders or planes

Kalman Filter: PHG4TrackFastSim

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Generation (~5M events):
vertex: (0,0,0)
momentum: (0,50 GeV/c)
|η|: (0,4)
φ: (0,2π)
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Additional parameters: generated particle (π^+,π^-,μ^-,e^-) B field: 1.5, 3.0 T (solenoidal)

Only the silicon is implemented in the simulation No support structure/services implemented

B = 1.5 T, π^{-}

Rey Cruz-Torres, Winston DeGraw - UCB



Beast TPC + Si barrels and disks ("hybrid")

Si barrels and disks ("all silicon")

All-silicon layout: Two eRD18 vertex layers, seven eRD16 "tapered" equidistant disks in a BeAST configuration, and an ALICE-like outer barrel, in a 3T solenoidal field

In addition:

Material cones/cylinders surrounding the disks were implemented to make a start on the effects associated with support structures, read-out infrastructure, etc. Ernst Sichtermann et al, eRD16

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Momentum resolution as a function of pseudo-rapidity

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Ernst Sichtermann et al, eRD16

- Various all-silicon layouts tested
- Parameters used:
 - Particle: e-
 - Momentum range: 0 to 50 GeV/c
 - Pseudorapidity range: $0 \le \eta \le 2.5$
 - Pixel size: 20x20 µm²
 - Magnetic field: uniform 1.5 T
 - Layer thickness in "TPC replacement": 0.8 %X₀

Key layouts and their aliases

2+2 layers, long	2 layers, long, small radius
2 layers, short, small radius, large disks	5 layers, short, optimised disks

- Large disk coverage is important to keep resolution at higher η
- All-silicon layout can outperform Si+TPC at p≥5 GeV/c
- Pointing resolutions do not change much between layouts, apart when layers are missed

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- Large improvement in relative momentum resolution
- Little difference in pointing resolutions
 - Transverse pointing resolution gets worse at 3 T at the lowest momenta, due to spiralling

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