

2nd EIC Yellow Report Workshop
Pavia University (online), 20-22 May 2020

Parallel session
Calorimeter & Particle ID & Tracking
Tracking summary

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for the Tracking WG

Ongoing activities and available results

Ongoing activities:

- Working on the following main deliverables:
 - Evaluate all-silicon vs hybrid (silicon & gaseous) trackers
 - Compare realistic alternatives (TPC, MPGD options) for gaseous detectors, barrel and forward
- Preliminary performance studies (mainly EicRoot-based simulations):
 - Central region Si-vertex + TPC + Fast MPGD Layers **advanced**
 - Cylindrical Micromegas (MPGDs) **just started**
 - Endcap region GEM (MPGDs) trackers **just started**
 - All-silicon (barrel) tracker + forward/backward silicon disks **advanced**
 - Comparisons all-silicon vs BeAST (Si-vertex + TPC + MPGDs) concepts **ongoing**
- Effort on Fun4All and ESCalate frameworks:
 - first implementations of all-silicon tracker in Fun4All and G4E **ongoing**
 - plan to implement realistic material and services for all the tracking detectors **just started**

Available results:

- Relative momentum and pointing resolutions (in different configurations and options)
- Angular resolutions at DIRC (Si-vertex + TPC + Fast MPGDs different options)

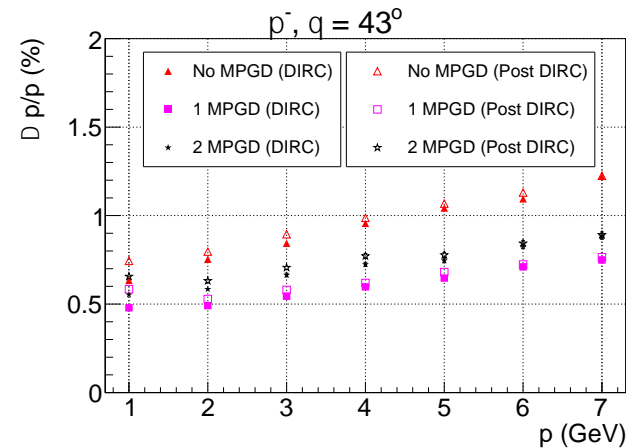
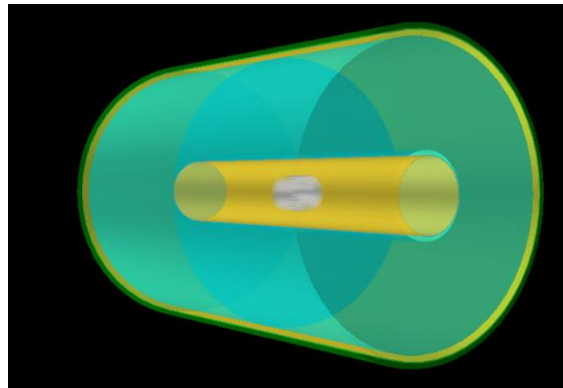
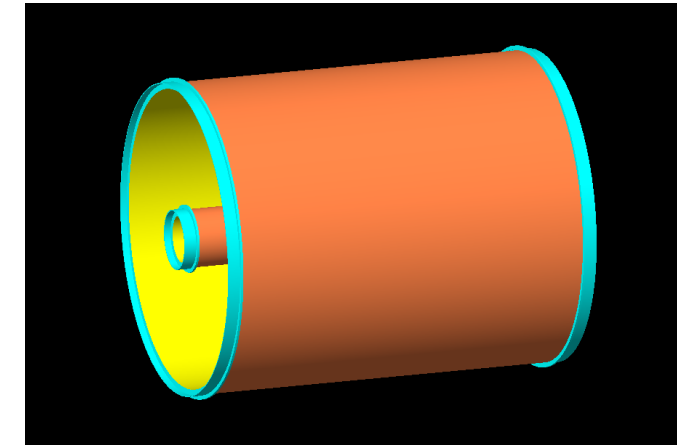
Si vertex + Gaseous Tracking

Matt Posik, for eRD6

Si-vertex + TPC + Fast MPGD layers

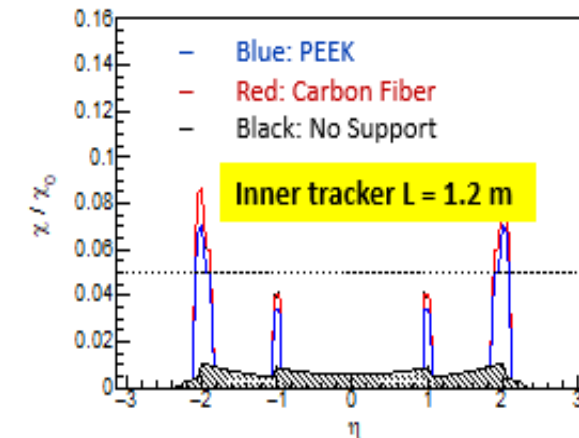
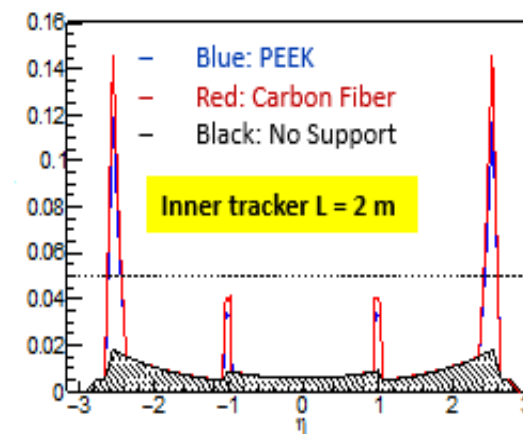
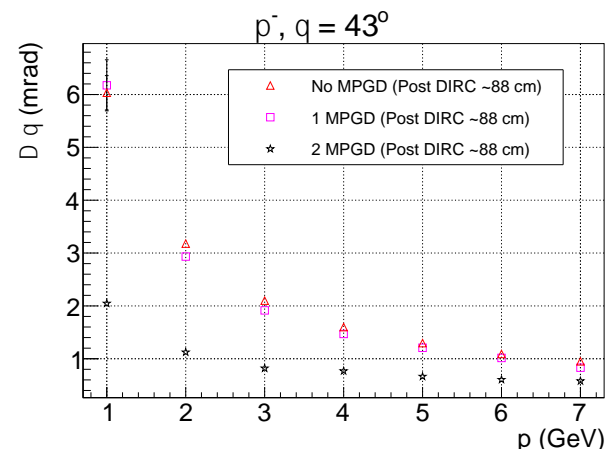
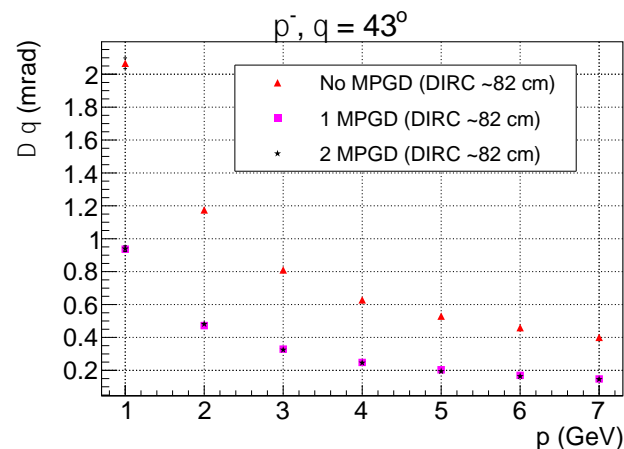
- ❑ Si-vertex tracker: 4 layers of $20\ \mu\text{m} \times 20\ \mu\text{m}$
- ❑ TPC: No distortion corrections, field cage and end cap materials included
- ❑ 3 configurations investigated
 - No MPGDs
 - One (or two) outer MPGD layers in front (or “sandwiching”) of DIRC
 - One Inner MPGD layers: fast and calibration for TPC detector

Mock prototype (support ring)



Material budget studies

- ❑ additional **inner μ RWell**: Fast signal & calibration for TPC
- ❑ Detector configuration; Fast layers in barrel region
 - Outer μ RWell layers: $L = 2\ \text{m}$; radius = 80.0 cm
 - Inner μ RWell layer: $L = 1.2\ \text{or}\ 2\ \text{m}$; radius = 12.5 cm
 - Impact of length & support structure on radiation length



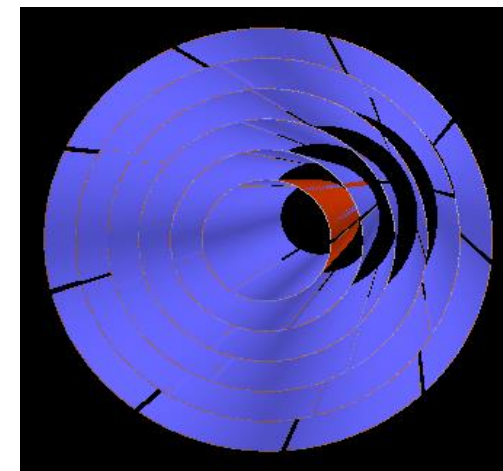
Si vertex + Gaseous Tracking

Si-vertex + Cylindrical MPGDs

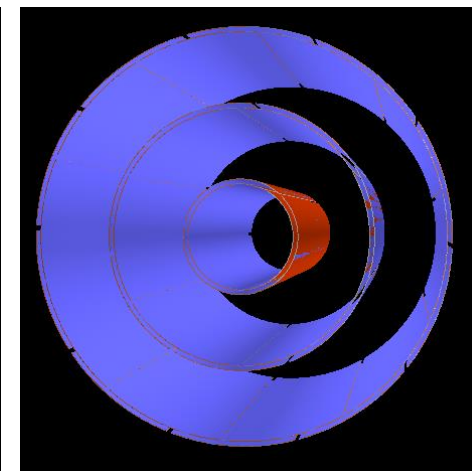
- ❑ Barrel MPGD tracker as TPC alternative:
 - Curved MPGD tiles with low material budget
 - Micromegas technology is being used in CLAS12
 - Possibly readout 2D coordinates on a single layer
- ❑ Simulation and performance study are under the ePhenix context
 - ePhenix TPC is replaced with the tracker
 - R is from 20 to 80cm, 2 configs are studied
- ❑ Compare material budget for TPC and 2D MPGD
 - Both TPC & 2D MPGD tracker compatible with $X/X_0 < 5\%$ requirement for central tracker



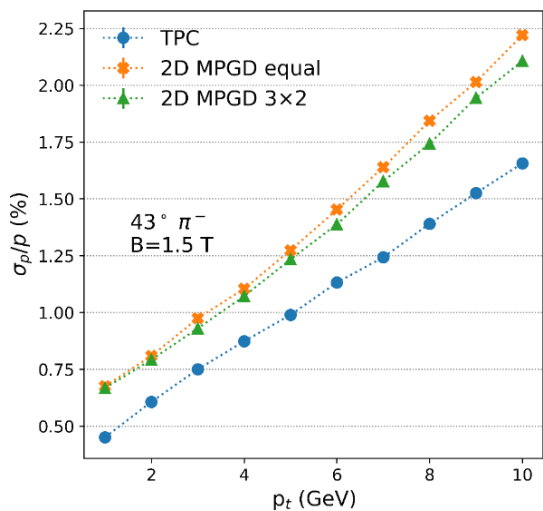
$X/X_0 \sim 0.3\%$ per layer



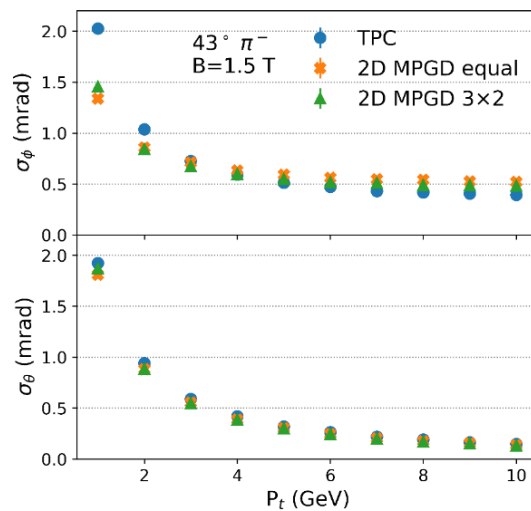
6 equidistant layers



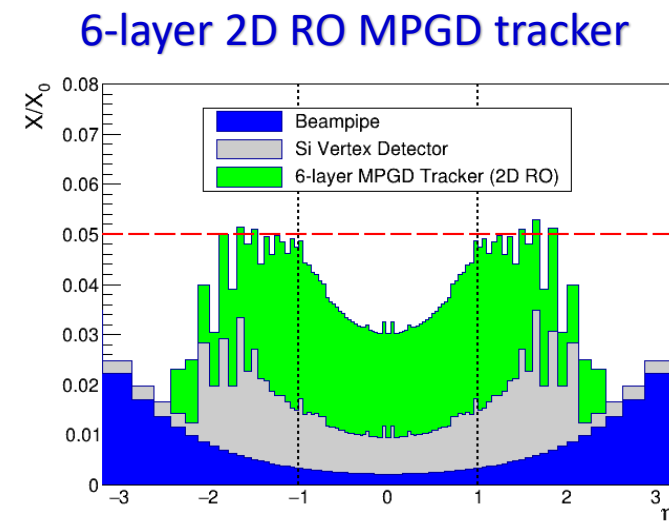
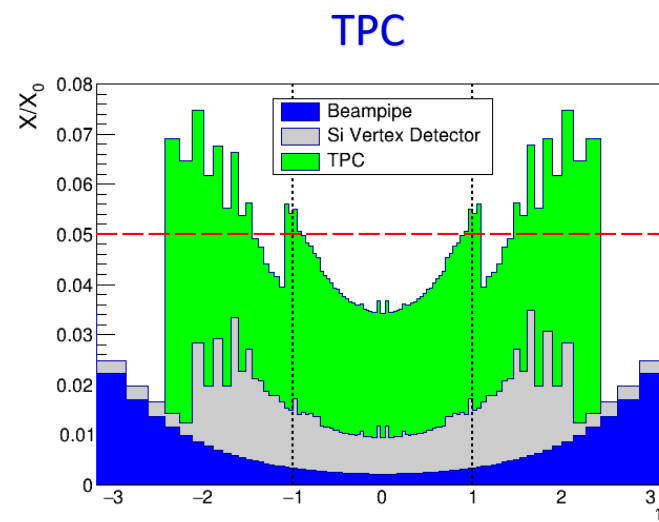
6 layers arranged as 3x2



Tracking WG



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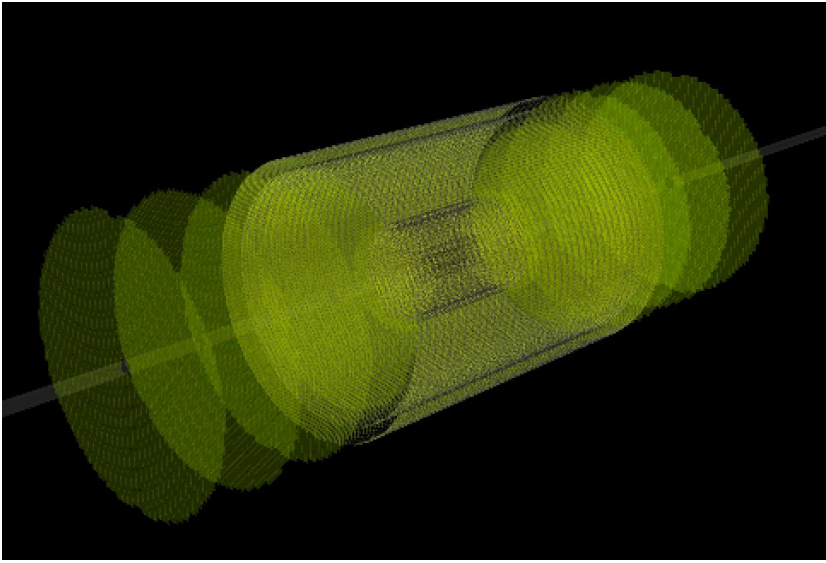
Gaseous Detectors: technology input for complementarity

	TPC + Fast MPGD Layer	Cylindrical MPGD (Micromegas, μ RWELL)	Drift Chambers / Straw Tubes	Planar MPGDs (GEM, Micromegas, μ RWELL)	Small TGCs	MPGD-TRDs
Barrel region	<u>Pros:</u> - momentum res.; - additional dE/dx; - cost - Low material in barrel	<u>Pros:</u> - Space point & angular res. - Time resolution (< 10 ns) - Low material in End cap - Cost & robustness	<u>Pros:</u> - momentum res.; - additional dE/dx; - cost - Low material in barrel	<u>Pros:</u> - Alternative to cylindrical MPGDs arrangement in polygons - Easier fabrication	N/A	N/A Radiator size
	<u>Cons:</u> - End cap material - calibration space charge distortion	<u>Cons:</u> - Momentum res. - Fabrication challenges - Material budget in barrel	<u>Cons:</u> - End cap material - calibration - Stability issues	<u>Cons:</u> - Momentum res. - Detector space barrel - Material budget in barrel		
Hadron End Cap	N/A Only planar option		<u>Pros:</u> - momentum res.; - additional dE/dx; - cost	<u>Pros:</u> - Momentum & angular res. - Low material (< 0.4% X/X0 per layer) - Cost & robustness	<u>Pros:</u> - Momentum & angular res. - Cost & robustness	<u>Pros:</u> - Additional tracking - Angular res. for RICH - Additional e/π PID
			<u>Cons:</u> - Material budget - calibration - Stability issues	<u>Cons:</u> - ?	<u>Cons:</u> - Material budget	<u>Cons:</u> - Radiator size
Electron End Cap	N/A Only planar option		N/A	<u>Pros:</u> - Momentum & angular res. - Low material (<0.4%) - Cost & robustness	N/A Mainly because of material budget	<u>Pros:</u> - Additional tracking - Complement main e PID in electron end cap
				<u>Cons:</u> - ?		<u>Cons:</u> - Radiator size?

All Silicon Tracking concepts

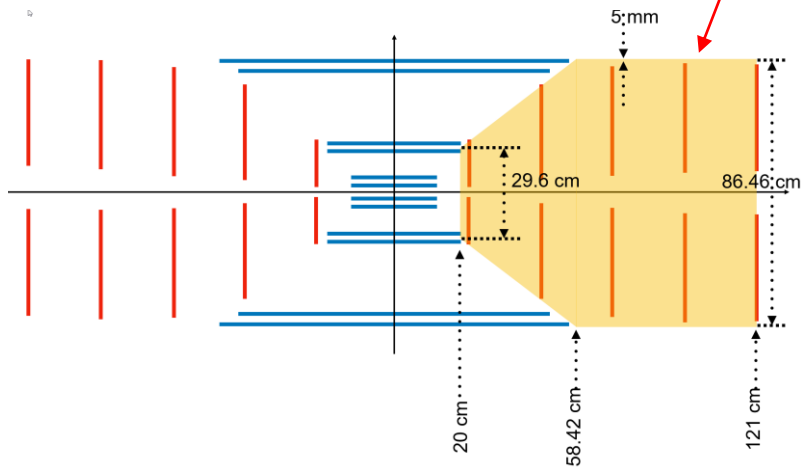
Ernst Sichtermann et al, eRD16

All silicon inner detector design

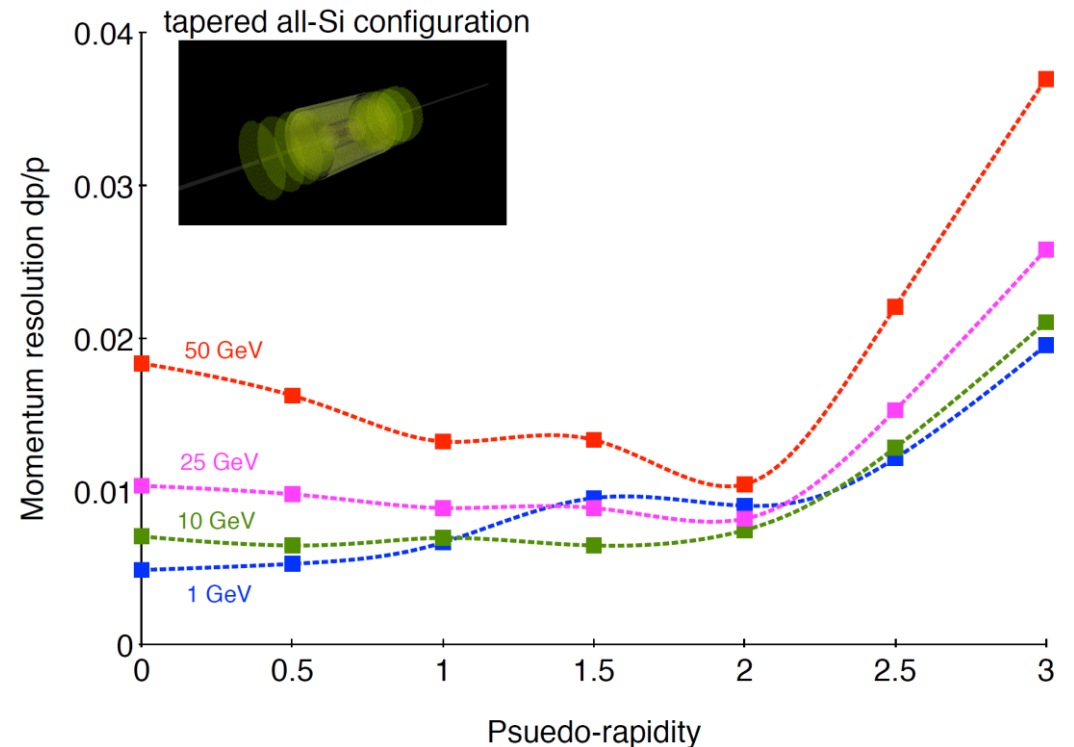


EIC-root

tapered all-Si configuration, $r \sim 43\text{cm}$



- ❑ Multiple configurations simulated, tapered all Si configuration shown.
- ❑ An all-silicon tracker, two eRD18 vertex layers, seven eRD16, “tapered”, equidistant disks in a BeAST configuration, and an ALICE-like outer barrel, in a 3T solenoidal field
- ❑ Identical barrel configurations, identical in length (z) to BeAST.
- ❑ Material cones/cylinders (5mm Al) surrounding the disks were implemented to make a start on the effects associated with support structures, read-out infrastructure, etc.; studies started/in progress.

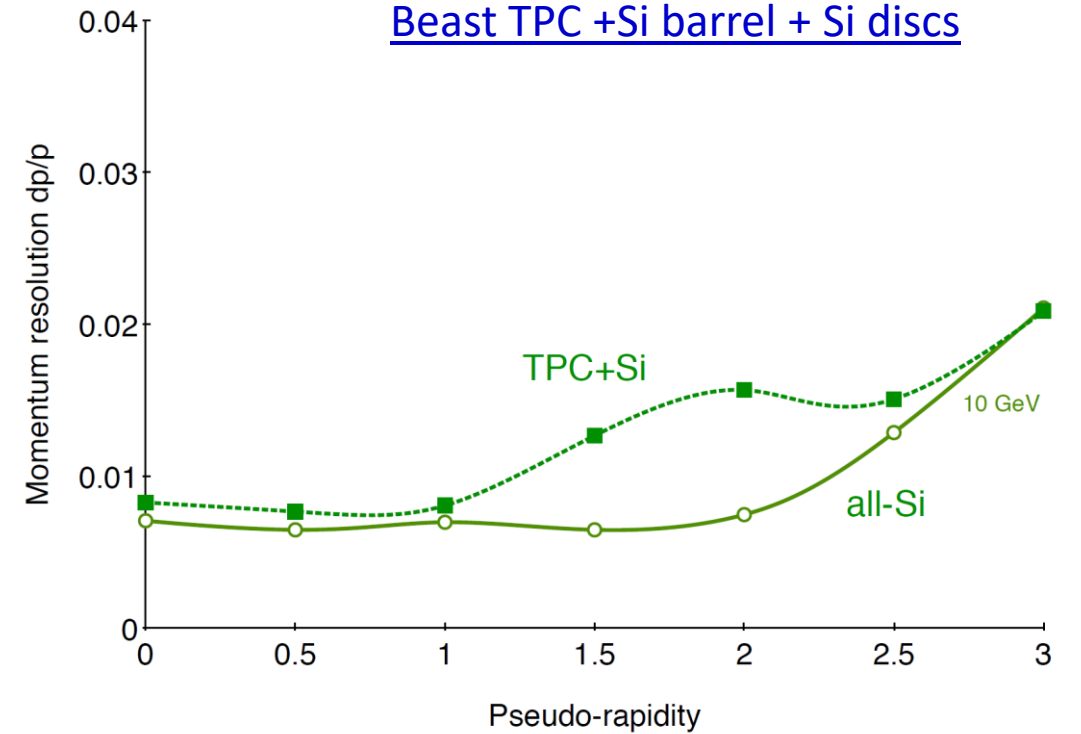
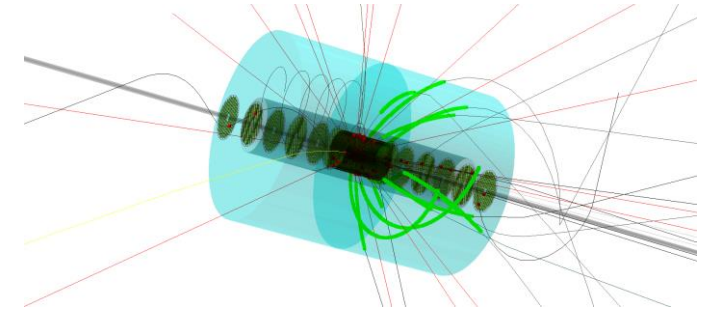
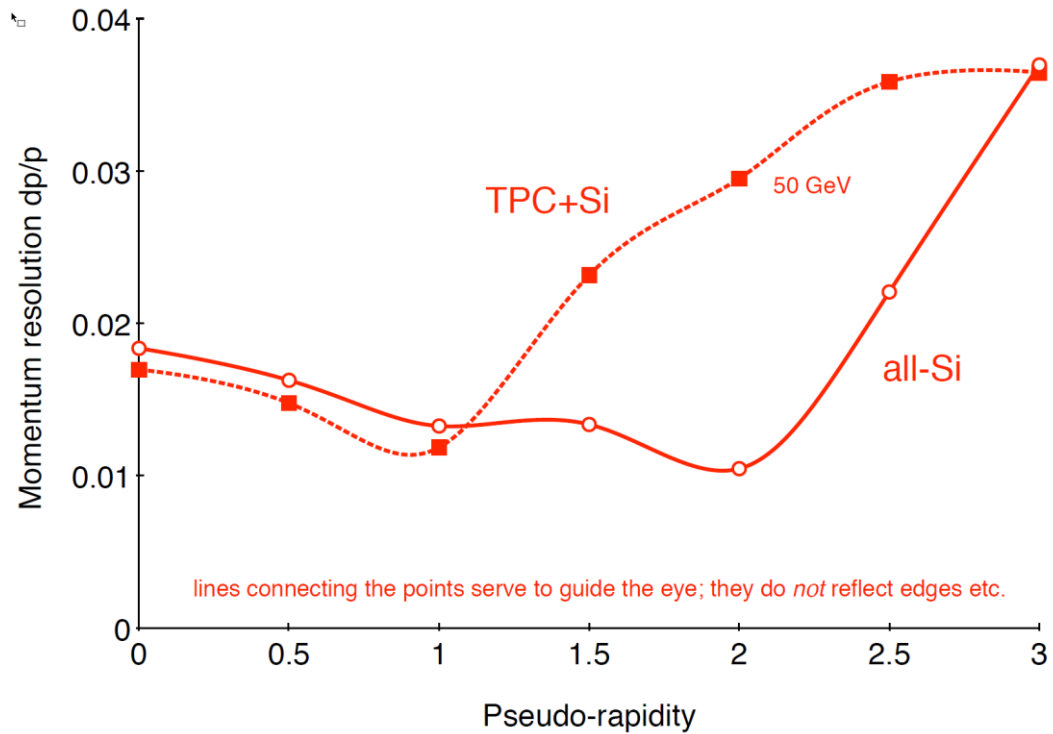


All Silicon Tracking concepts

Ernst Sichtermann et al, eRD16

Comparisons of (Beast TPC + Si barrel + Si discs) with all silicon design (TPC replaced with 5 Si barrel layers)

EIC-root

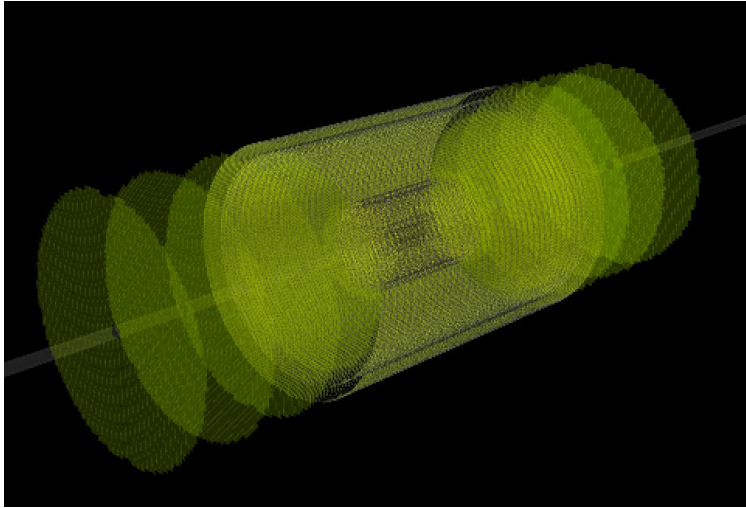


All Silicon Tracking concepts

Rey Cruz-Torres, Winston DeGraw - UCB

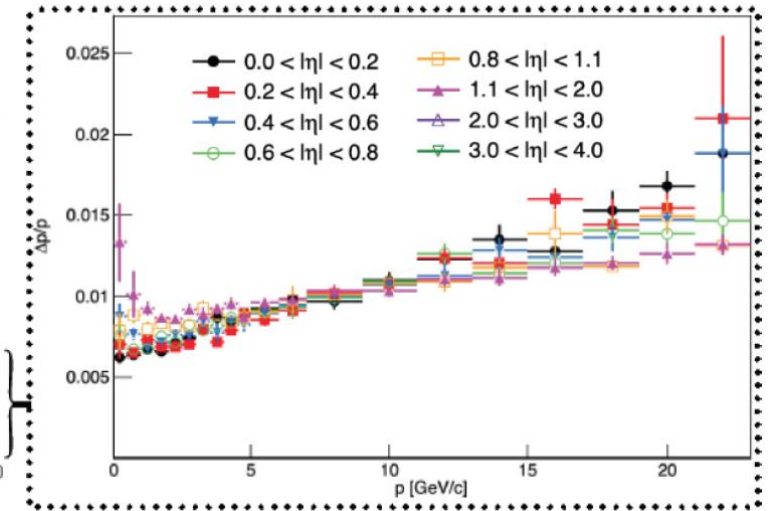
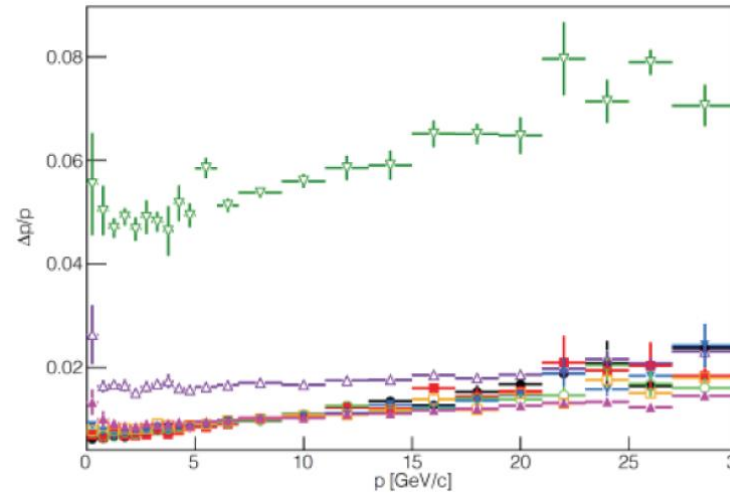
All silicon inner detector design

The “tapered all-Silicon” model from ERD16 implemented in Fun4All

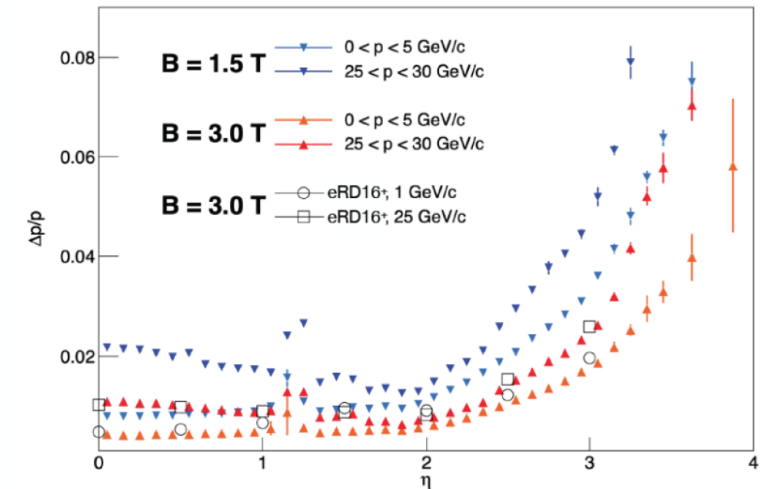
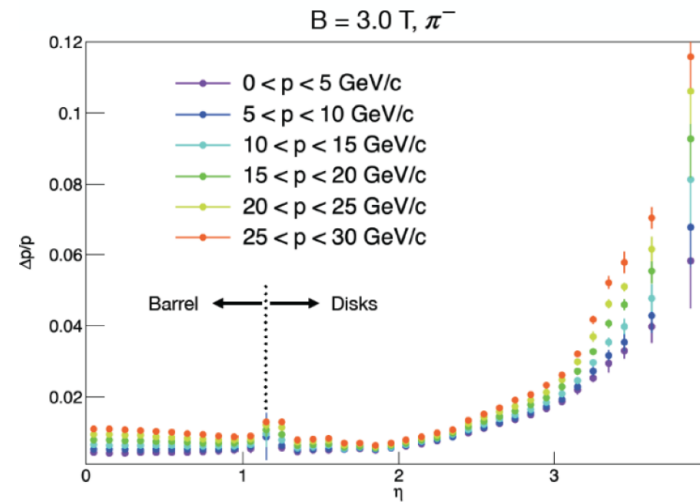


Fun4All

Performance very good and consistent with EICroot modeling



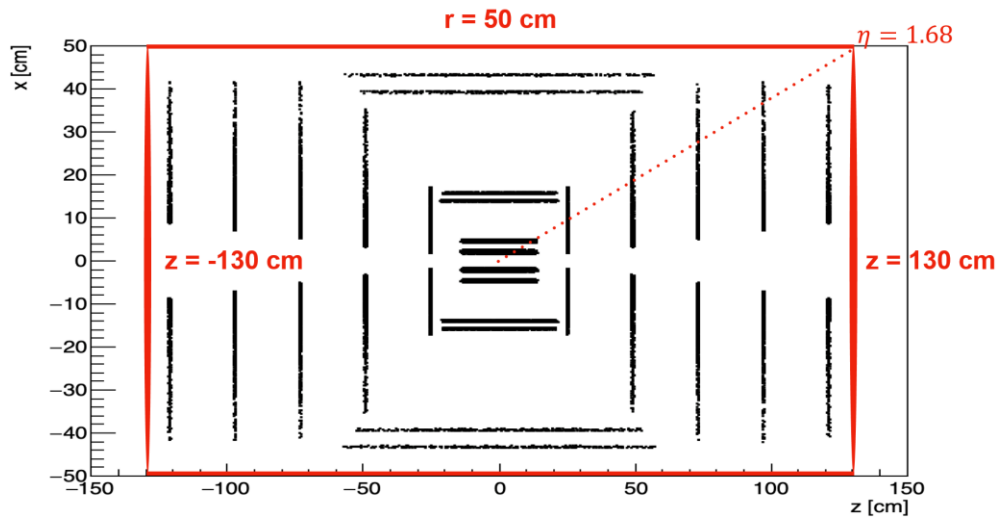
π^-



All Silicon Tracking concepts

All silicon inner detector design – angular resolution at PID locations

Again – using the “tapered all-silicon” model with Functionality added by Chris Pinkenburg to project momenta onto cylinders or planes

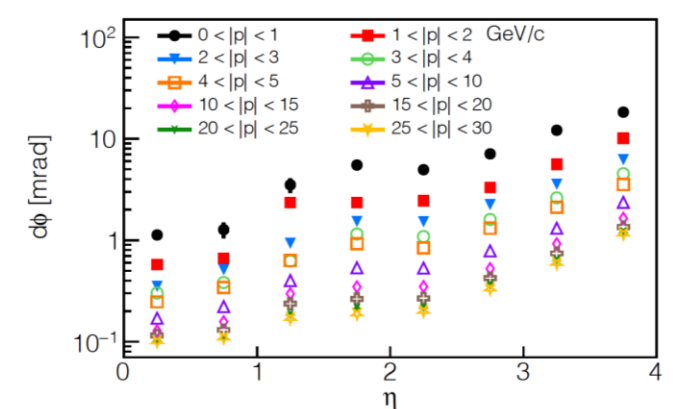
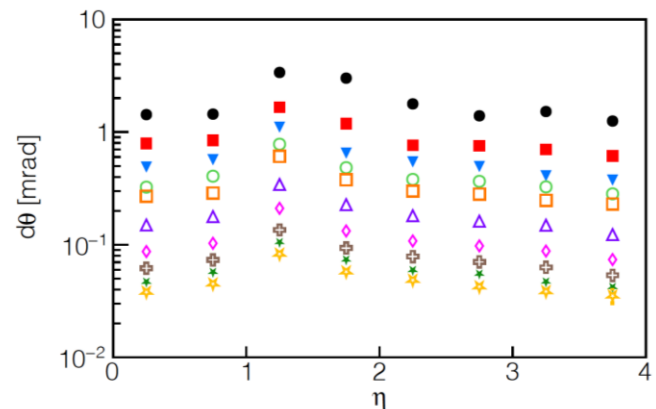
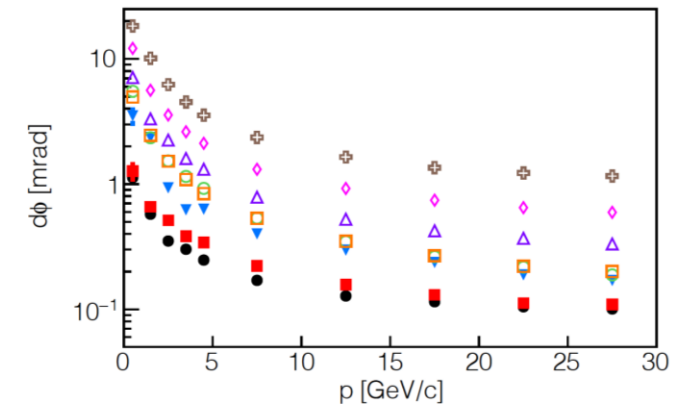
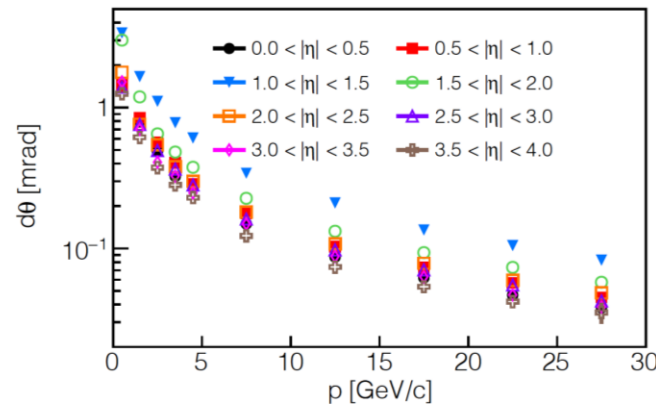


Fun4All

Initial results

Do not include PID detector material

e^- , $B = 3.0\text{ T}$



All Silicon Tracking concepts

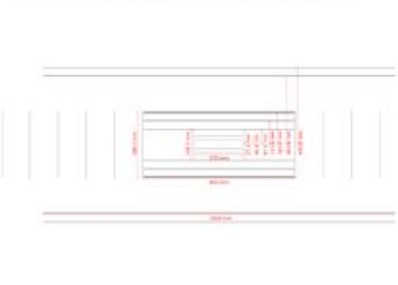
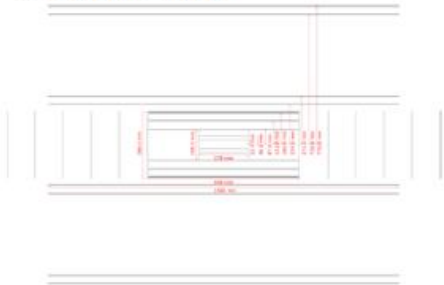
All silicon inner detector design and comparison with Beast TPC

Many silicon geometries simulated – see tracking notes for full list

Key layouts and their aliases

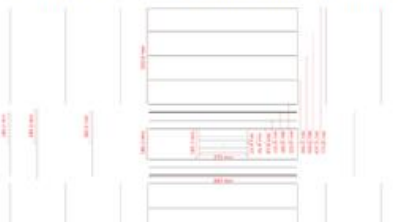
2+2 layers, long

2 layers, long, small radius

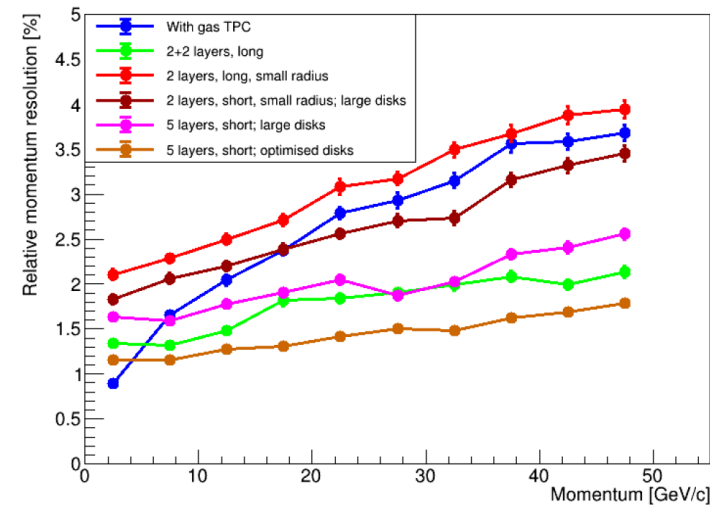


2 layers, short, small radius, large disks

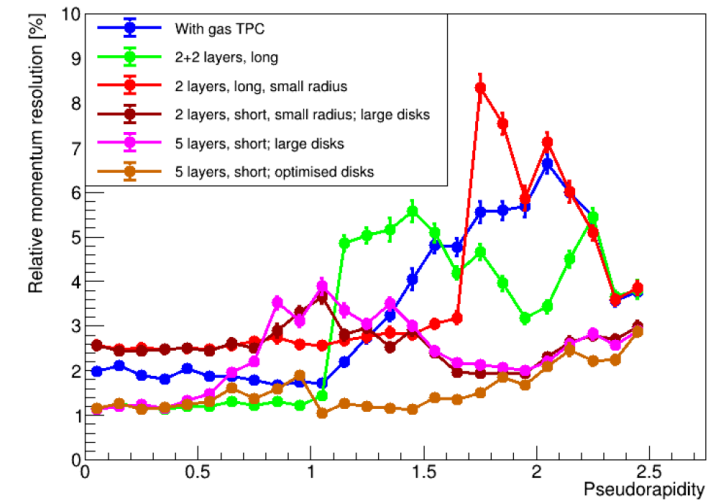
5 layers, short, optimised disks



Relative momentum resolution vs p



Relative momentum resolution vs η



EICroot

- ❑ Large disk coverage is important to keep resolution at higher η
- ❑ All-silicon layout can outperform Si+TPCat $p \geq 5$ GeV/c
- ❑ Pointing resolutions do not change much between layouts, apart when layers are missed

Tracking WG: technology input for complementarity

Tracking Si central detector (vertex + barrel + discs)

Technology: for the vertex, barrel and inner disc detectors, the **only identified technology that meets the requirements are MAPS**. No currently existing MAPS sensor appears to fully meet all of the EIC requirements (current simulations are based on ALPIDE sensors with a smaller pixel size 20 x 20 μm^2). In order to produce a new sensor design that meets the EIC requirements a consortium of EIC groups are joining an **ongoing sensor development effort at CERN**. There are **contingency plans for modification of existing sensor designs to meet EIC requirements should this CERN effort be unsuccessful**.

There is general consensus that this is a promising path to pursue to deliver an EIC sensor in the given timeframe. **Momentum and pointing resolution performance studies are in progress**. EIC requirements seem **satisfied**.

ITS3 silicon design parameters

Parameter	Wafer-scale sensor (this proposal)
Technology node	65 nm
Silicon thickness	20-40 μm
Pixel size	O(10 x 10 μm)
Chip dimensions	scalable up to 28 x 10 cm
Front-end pulse duration	~ 200 ns
Time resolution	< 100 ns (option: <10ns)
Max particle fluence	100 MHz/cm ²
Max particle readout rate	100 MHz/cm ²
Power Consumption	< 20 mW/cm ² (pixel matrix)
Detection efficiency	> 99%
Fake hit rate	< 10 ⁻⁷ event/pixel
NIEL radiation tolerance	10 ¹⁴ 1 MeV n _{eq} /cm ²
TID radiation tolerance	10 MRad

	Stave X/X0
ITS3 like vertexing	~0.1%
ITS3 like barrel (up to 1.5m length)	0.55 %
ITS3 like disc (up to 60 cm diameter)	0.24%

Si + gaseous detector vs. all silicon

	Si + gaseous	All Si
Attributes for consideration	<ul style="list-style-type: none"> dE/dx in gas for PID Well understood technology - less R&D needed. Costs less (likely) Less material in tracking region Worse single point resolution but more position samples 	<ul style="list-style-type: none"> Readout faster than TPC Better momentum resolution than TPC at higher momentum (>~5GeV/c) Can be made more compact Less material in endcap regions Fewer calibration/correction issues Very high single point resolution