



sPHENIX INTT

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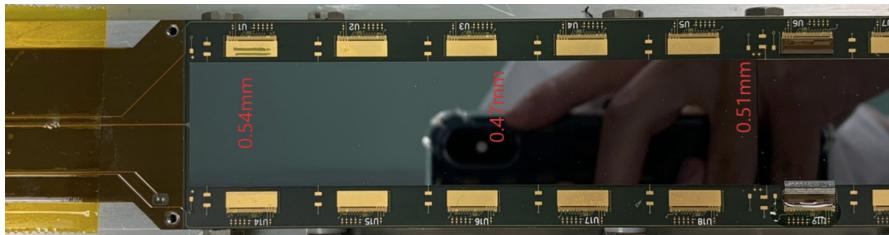


Dummy sensor placing error



Test area : type A and B (dummy sensor (320 μ m), rejected HDI were used)

Assembly in 2020/05/20



Placing error

Type A -> X : 14 μ m, Y : 27 μ m

Type B -> X : 24 μ m, Y : 18 μ m

Measurements done by OGP

Name	Midpoint (x,y) mm
Dummy sensor A	(12.561, -167.203)
HDI	(12.575, -167.176)

Type A

Name	Midpoint (x,y) mm
Dummy sensor B	(12.577, -51.004)
HDI	(12.553, -50.986)

Type B

Reach the requirement !!!

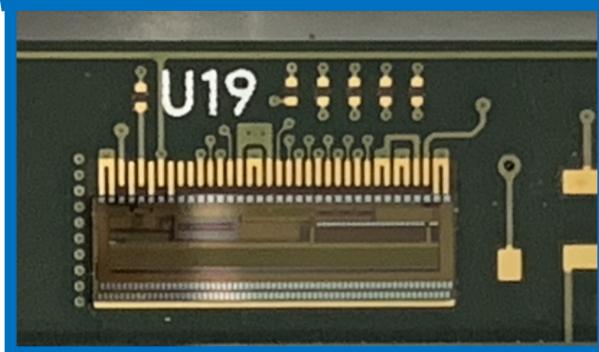
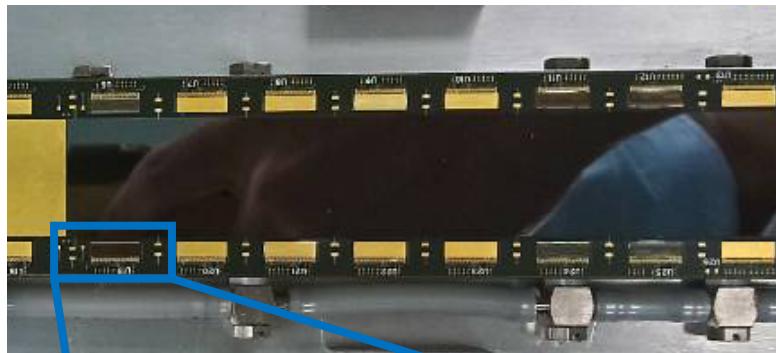


Assembly test with rejected HDI

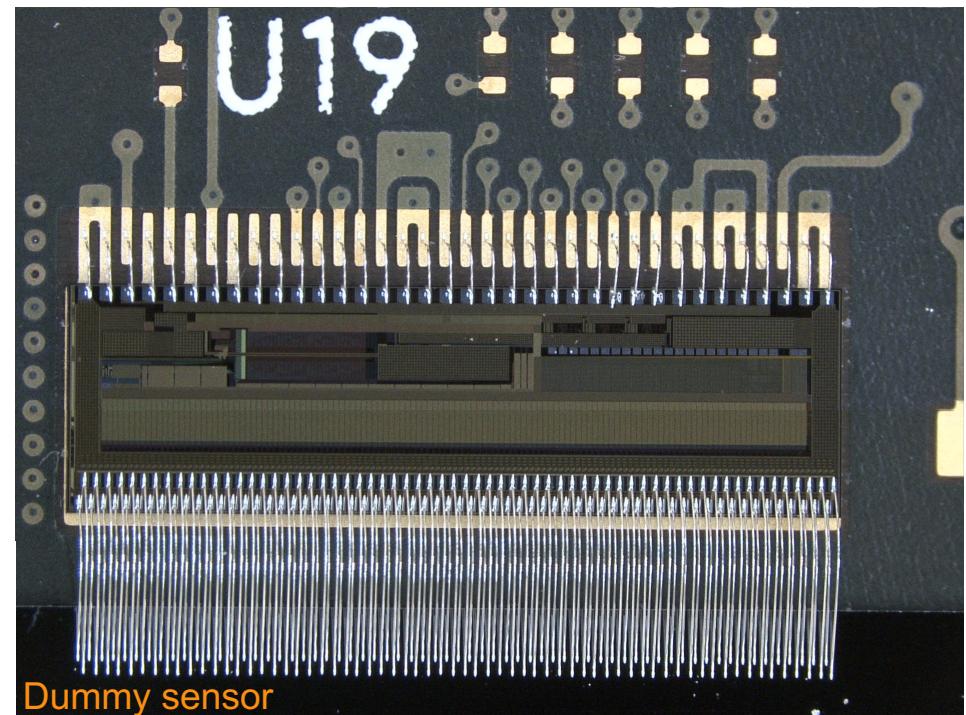


Test area : type A (dummy sensor (320 μ m), rejected HDI were used), Test Chips : 6 (new bad chips)

assembled in 2020/05/19



Bonding in 2020/05/20





First real module assembly in Taiwan

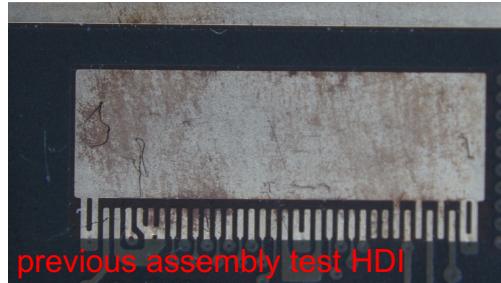


Area : type A and B (**good** sensors, **pre production** HDI were used), 26 **good** chips

2020/05/21

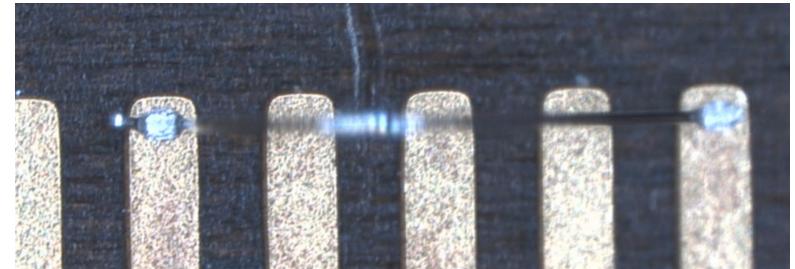
Before module assembly

oxidation rust appears

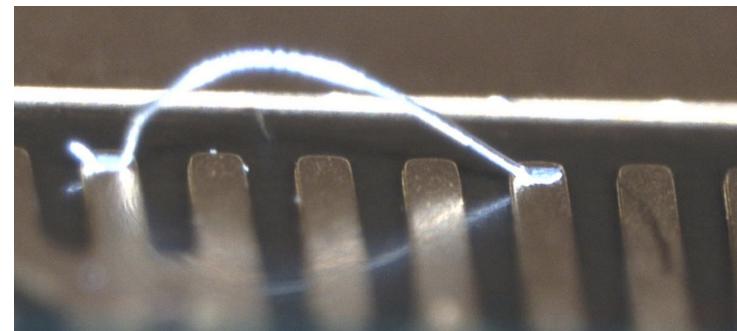
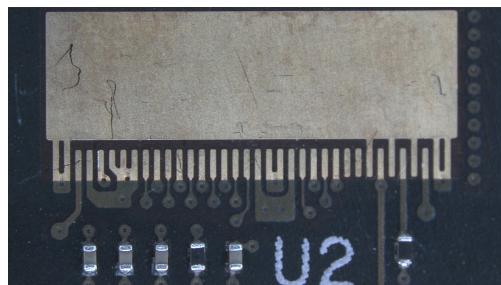


Bonding test

Too make sure the surface is smooth enough



Cleaned by eraser



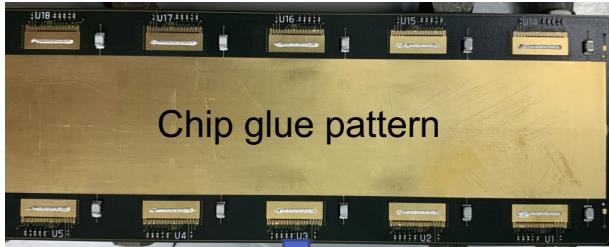


First real module assembly in Taiwan

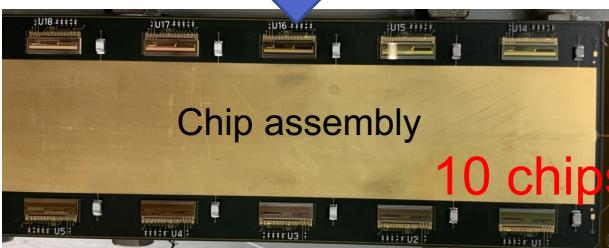


Area : type A and B (**good** sensors, **pre production** HDI were used), 26 **good** chips

First : Type B area, 10 chips assembly and bonding in NTU

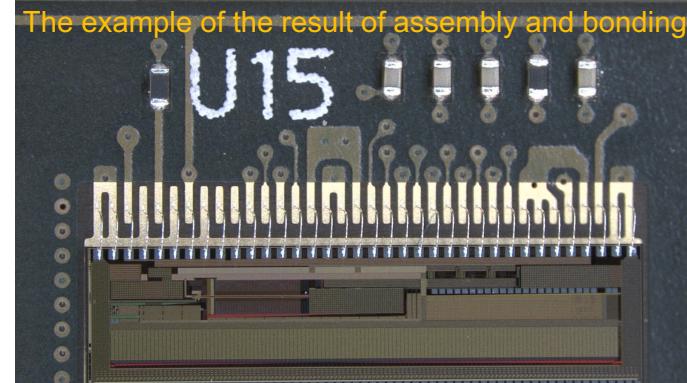


Chip glue pattern



Chip assembly

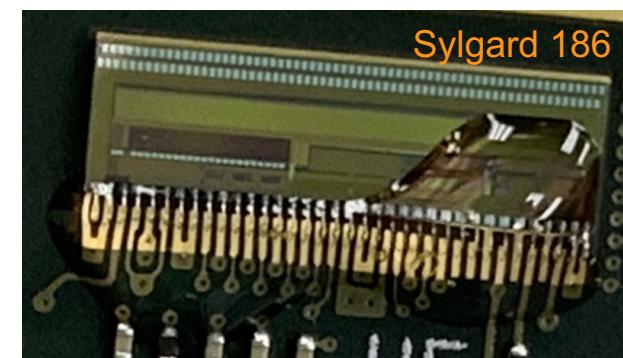
The example of the result of assembly and bonding



10 chips are looked to be good !!!



To cure the glue fast,
Bake HDI for 45 $^{\circ}\text{C}$ 1.5 hrs, then 25 $^{\circ}\text{C}$ 0.5 hr



Bonding wire is encapsulated to avoid the oxidation rust happen

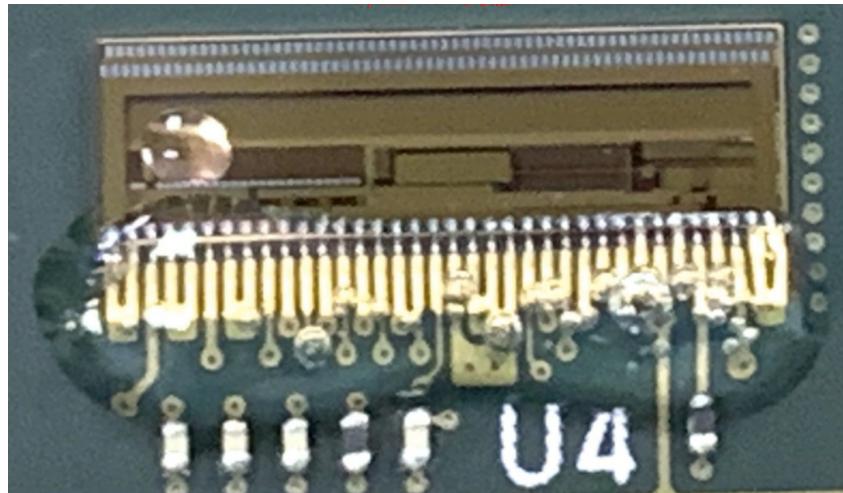


Assembly problem

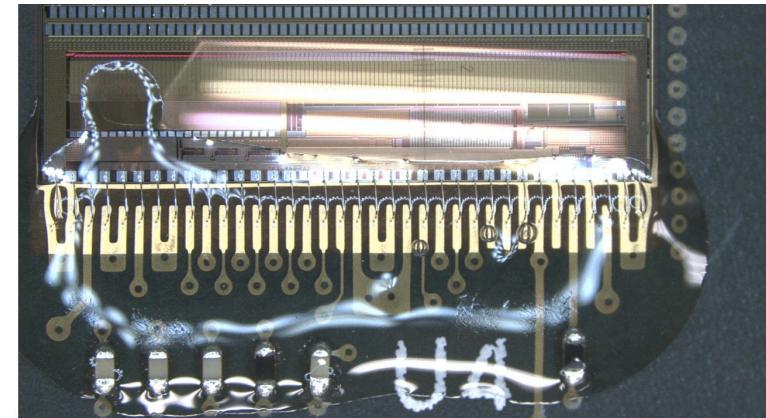


The sylgard 186 spreads during 3 days curing

2020/05/22



2020/05/25



Some of channels of chips in type B area can not work.....

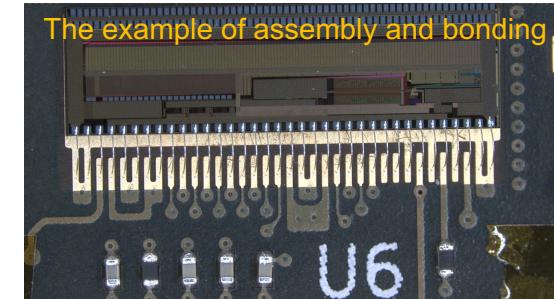
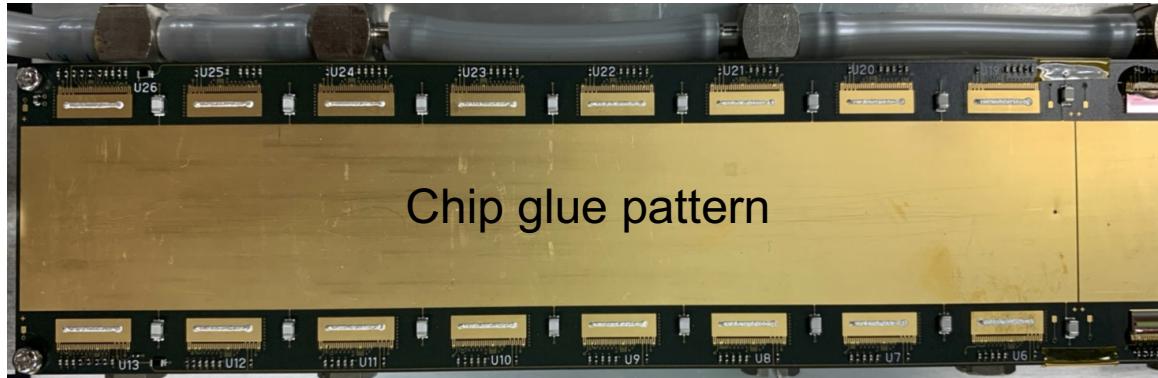


First real module assembly in Taiwan

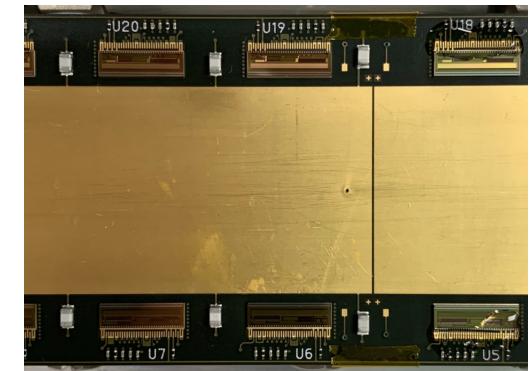
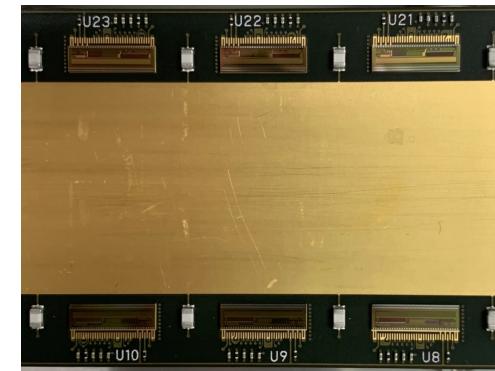
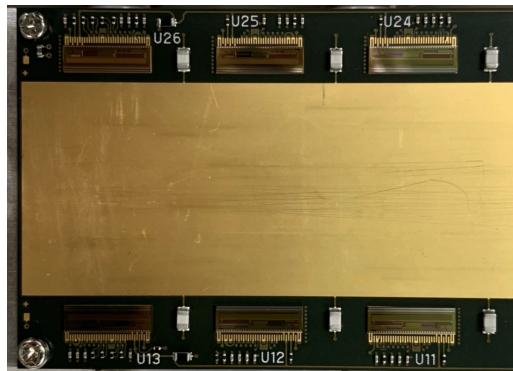


Area : type A and B (**good** sensors, **pre production** HDI were used), 26 **good** chips

Second : Type A area, 16 chips assembly and bonding in NTU



16 chips are looked to be good !!!





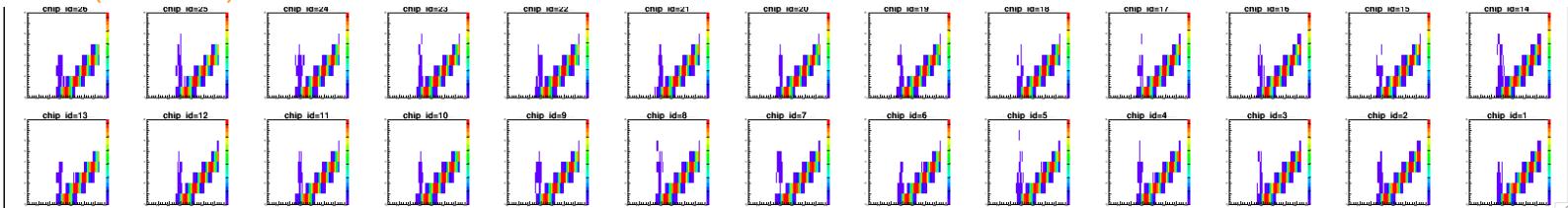
First real module assembly in Taiwan -> HDI-chips calibration test



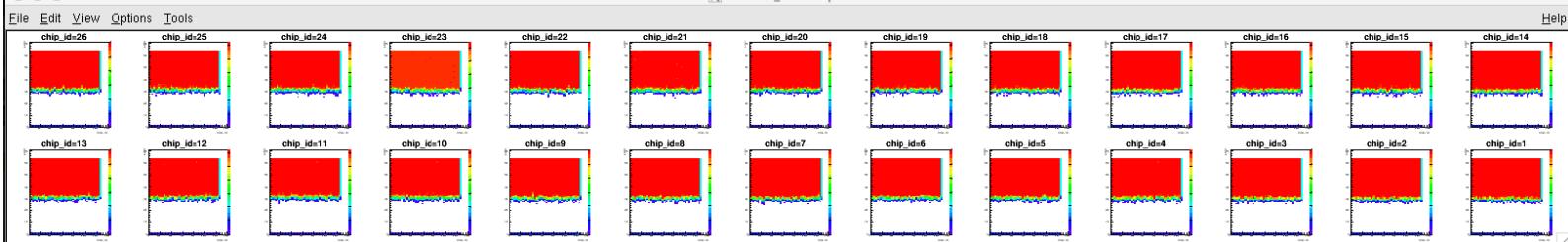
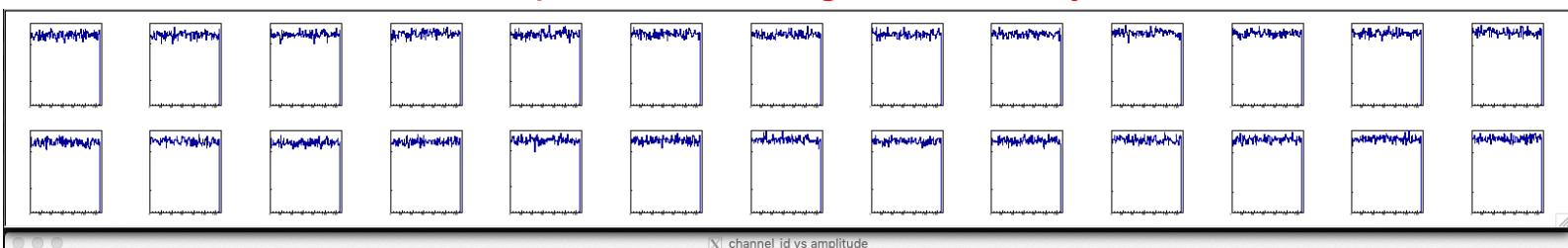
Calibration test in NCU



Draw("COLZ0")

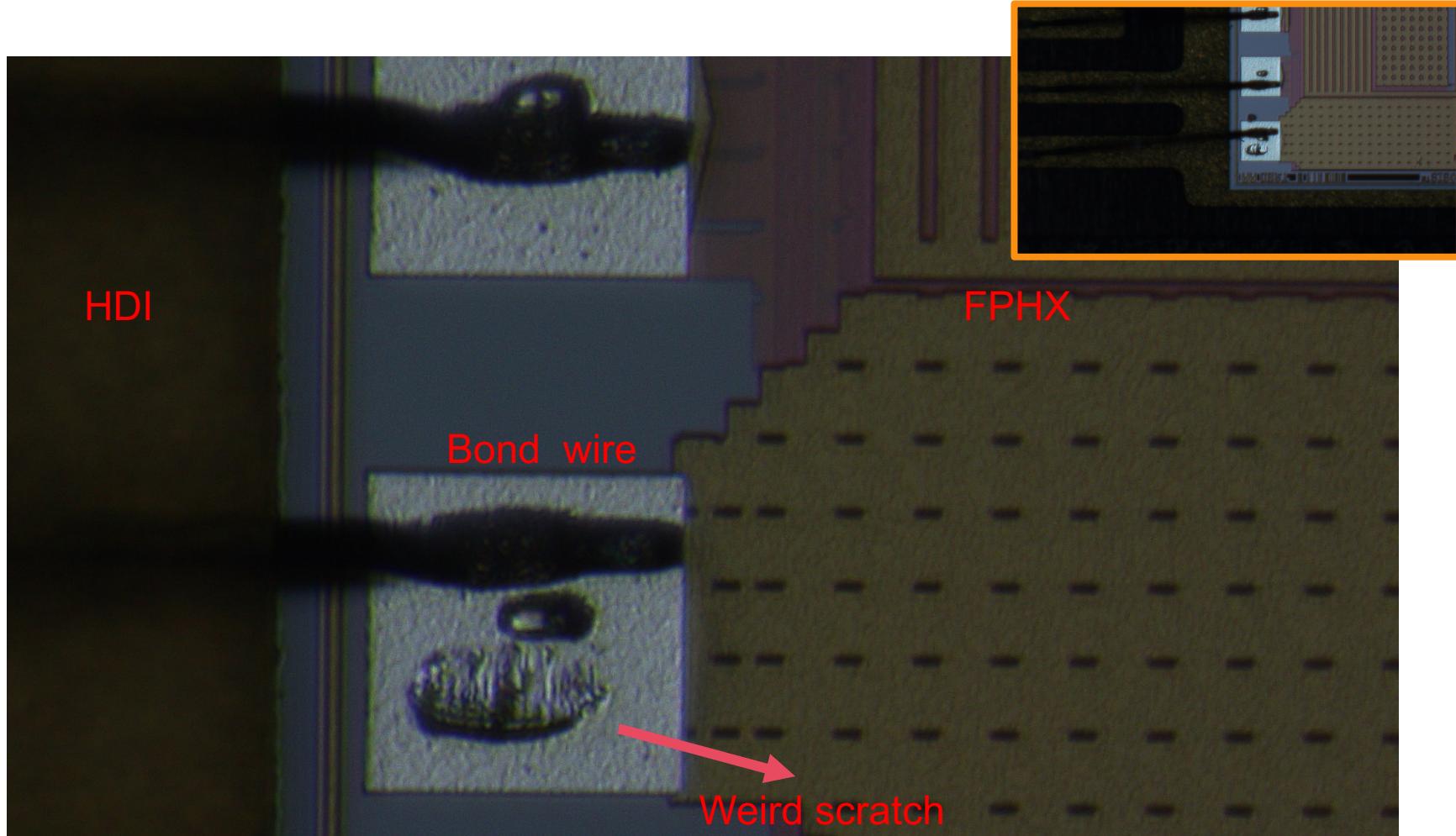


All chips are working functionally !!!



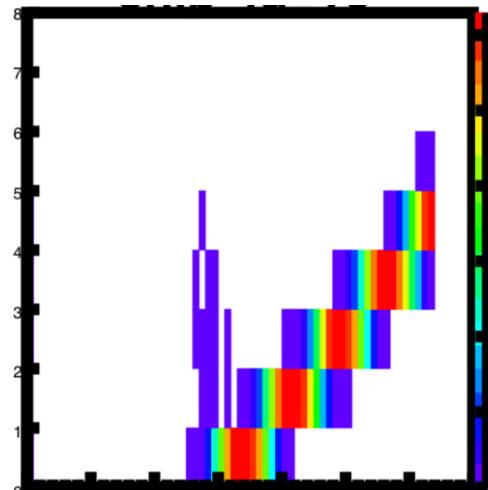


A weird damage on FPHX



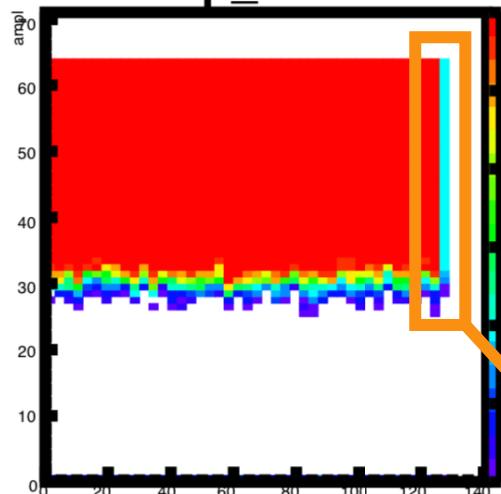


Testbench problems



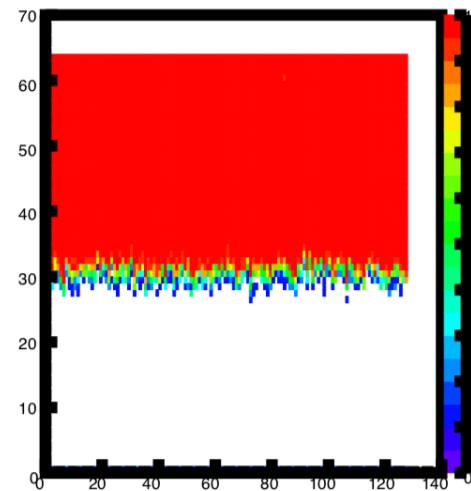
		Read	Write	Set	Reset	Default
DAC0	20					
DAC1	25					
DAC2	30					
DAC3	35					
DAC4	40					
DAC5	45					
DAC6	50					
DAC7	55					

chip_id=8



Change the bin width

Unfunctional channel 128?





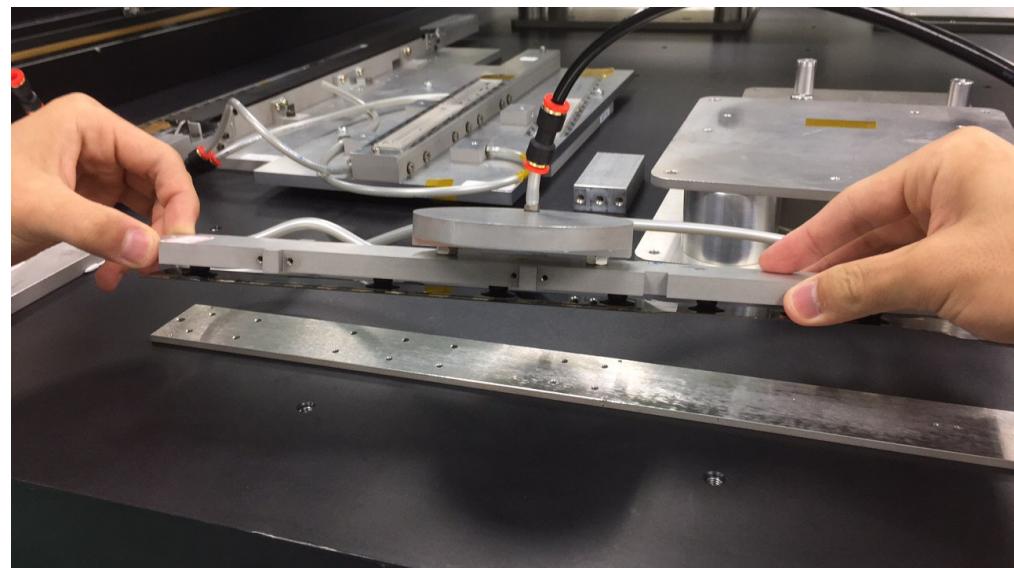
Module pick up tool



Ladder assembly tray



Module pick up tool



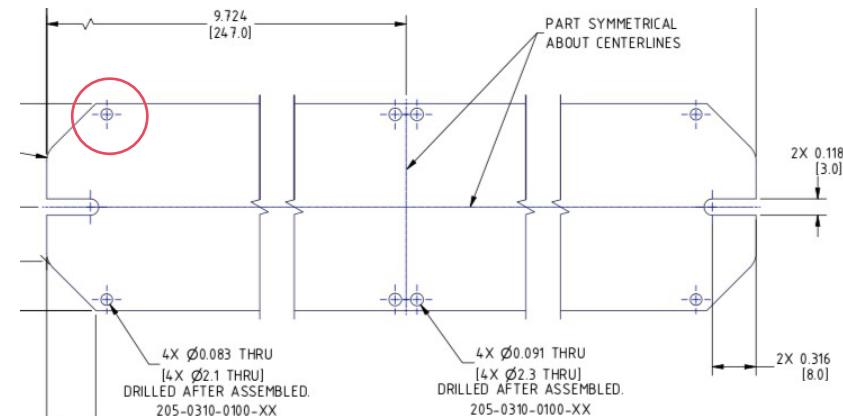
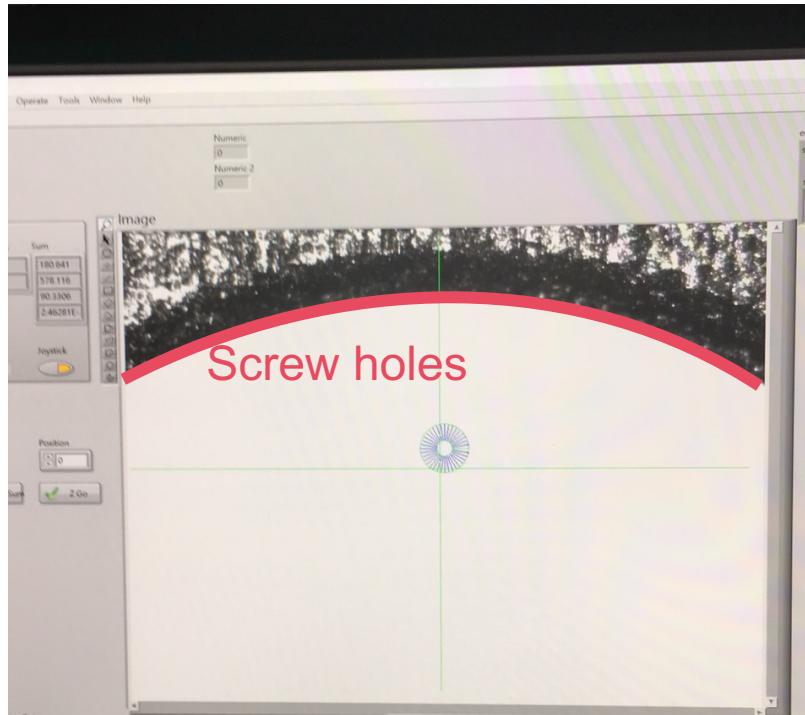


Problem of ladder assembly



- No alignment holes on stave

We try to use the screw holes as alignment holes, it is too big



Is it possible to add the alignment marks on stave ?



To do list

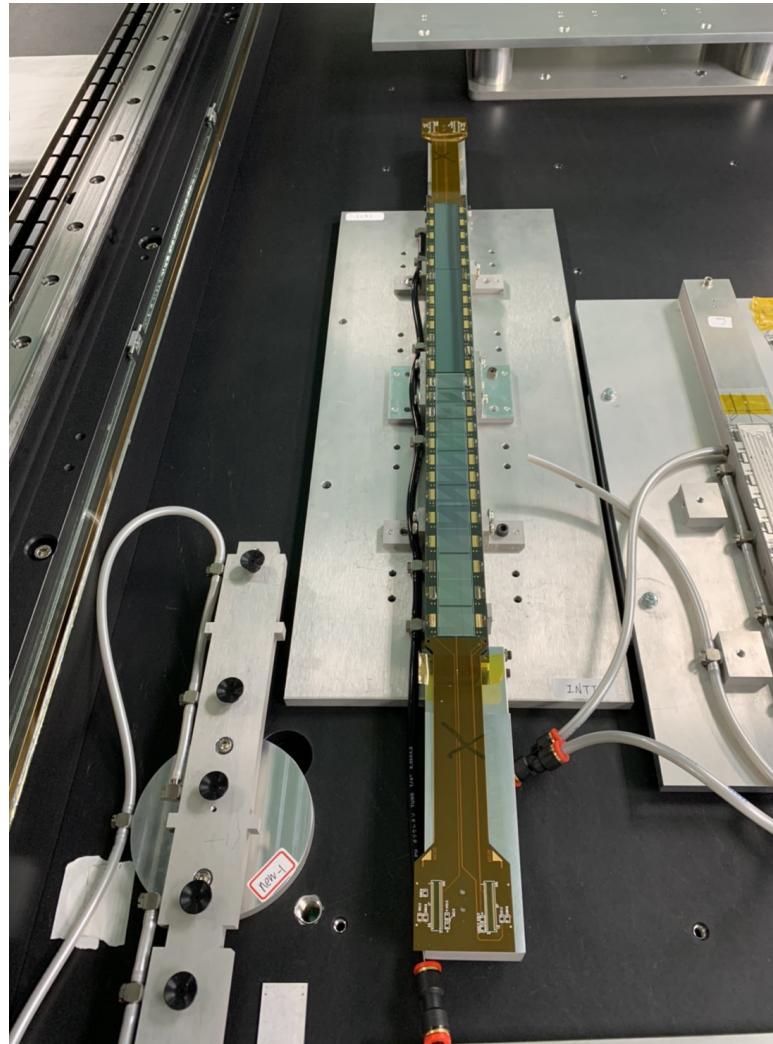


- Assemble and bond good sensors on HDI.
- Measure the placing error by OGP
- Produce first two real modules ASAP

Back up



Dummy ladder assembly



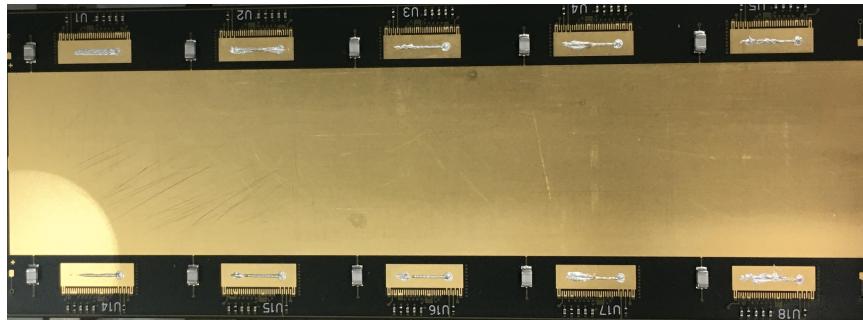


First chips assembly test

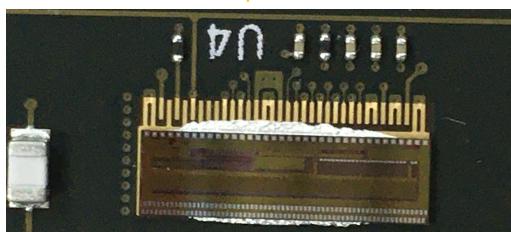


2020/4/22

Test area : type B
Test Chips : 10



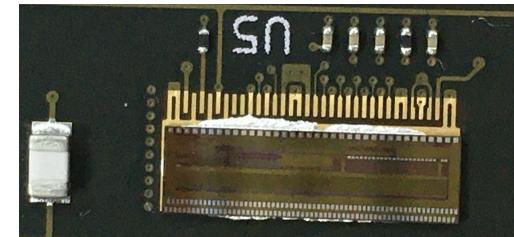
Apply glue



Place all chips

The leakage of glue is too much

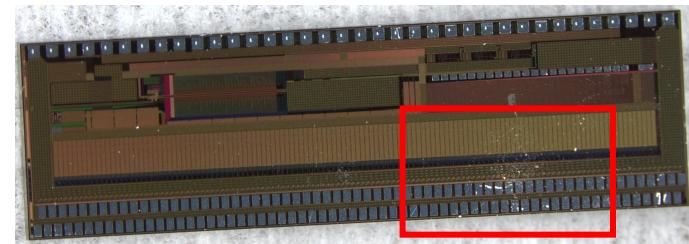
Place all chips



Remove all chips
Clean the HDI



Remove all chips
Clean the HDI



The HDI looks ok, 10 chips is considered to be bad
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2020/6/3

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