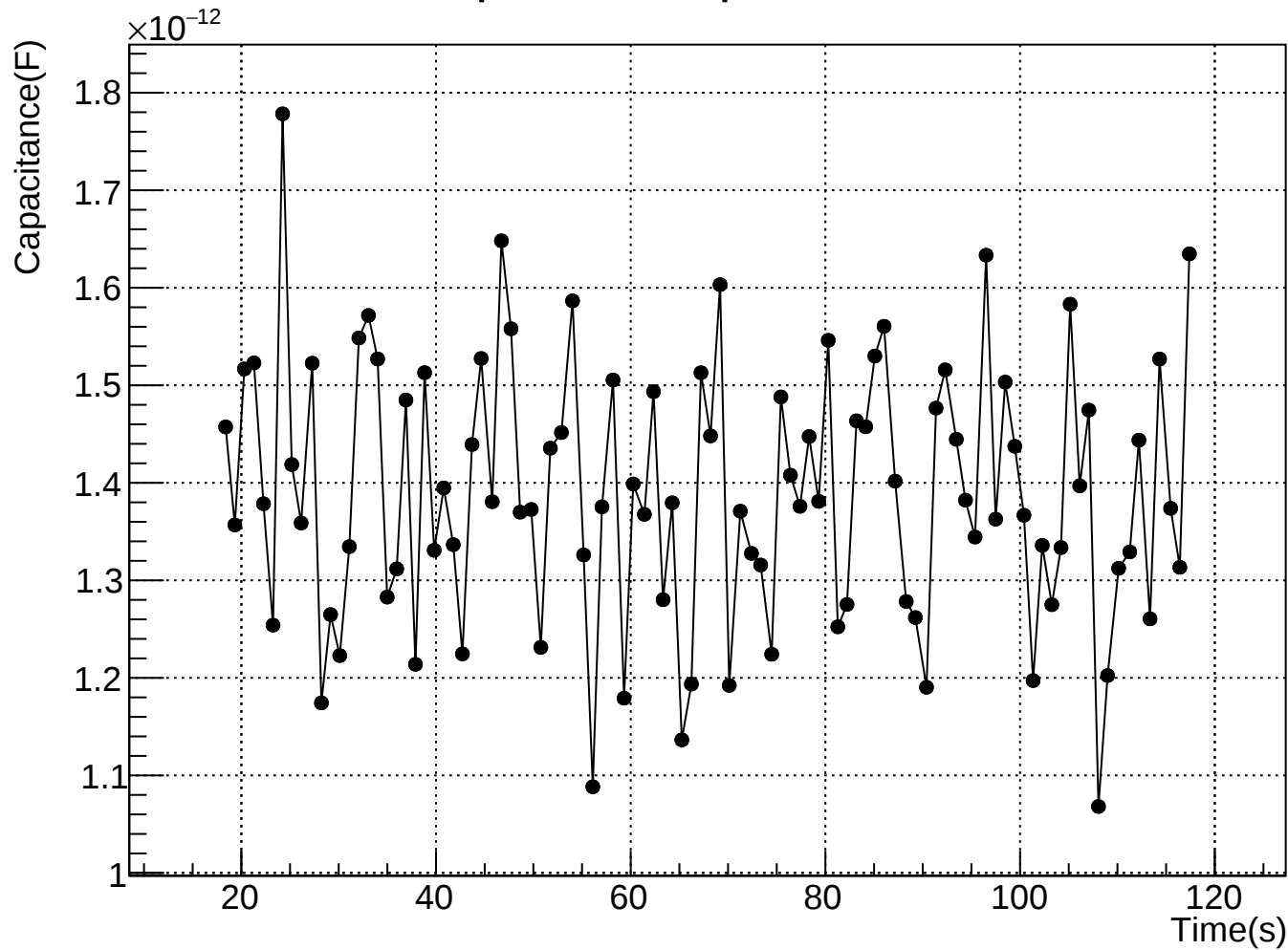
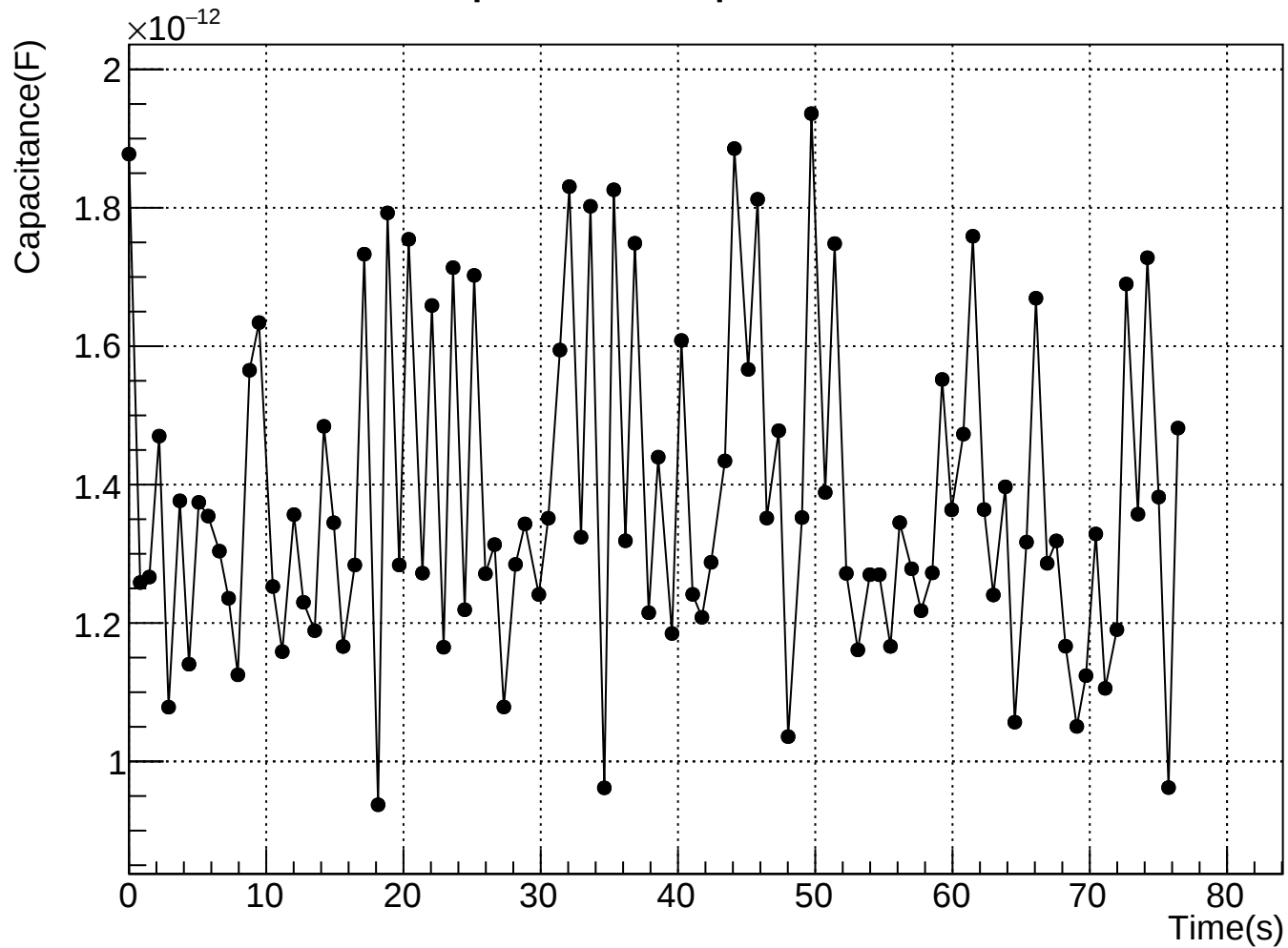


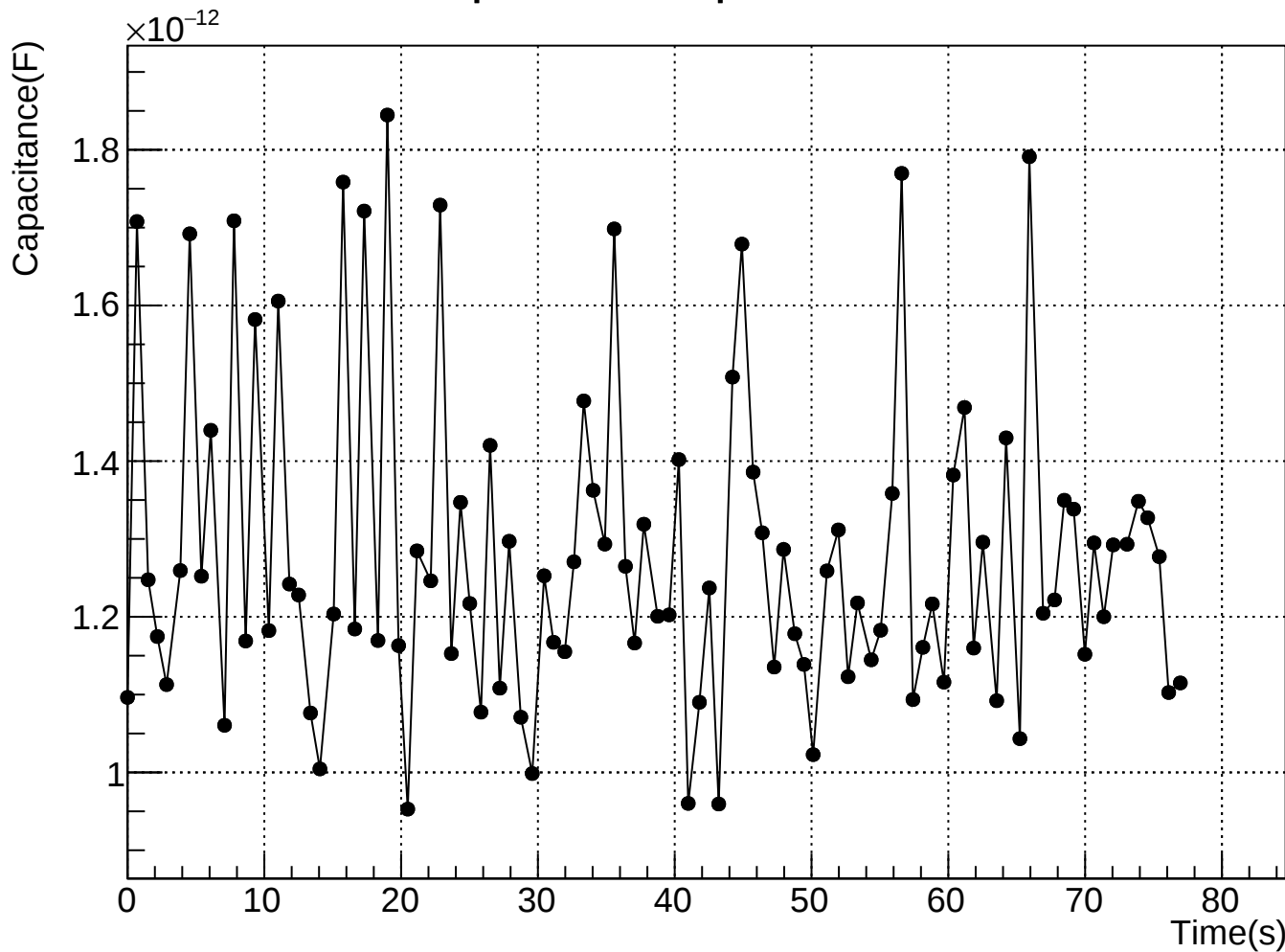
Sample101 Chip5 Channel001



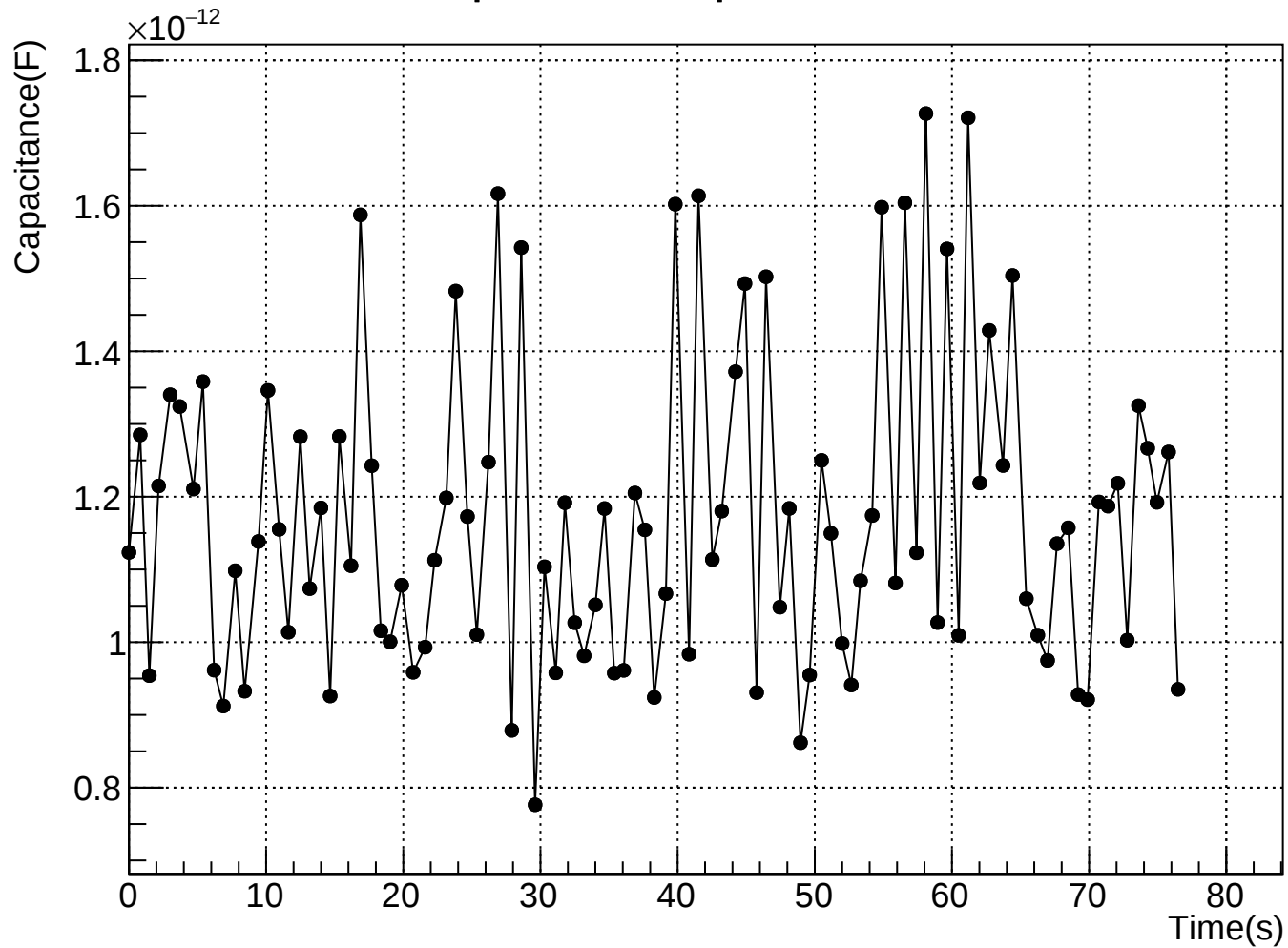
Sample101 Chip5 Channel002



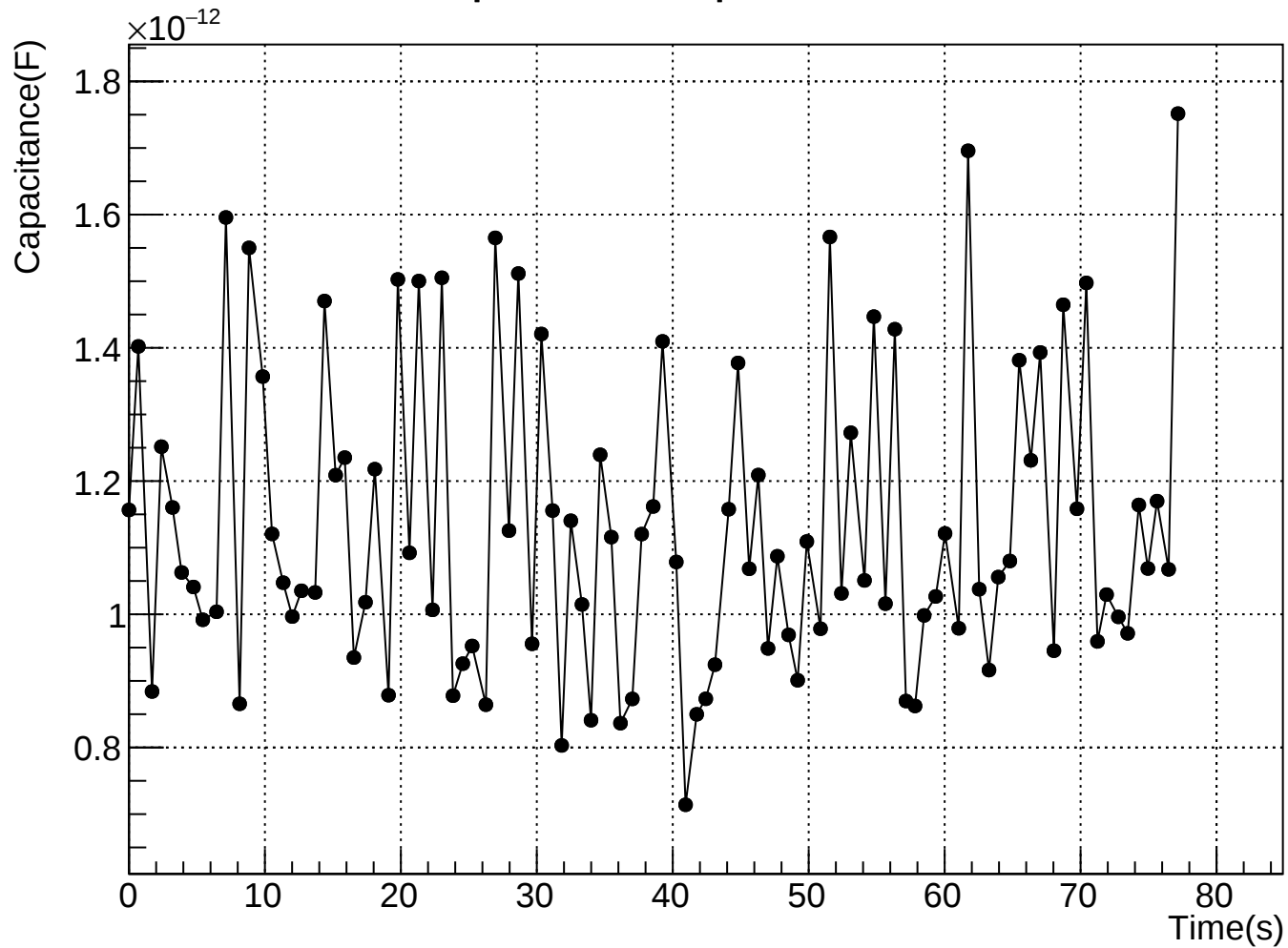
Sample101 Chip5 Channel003



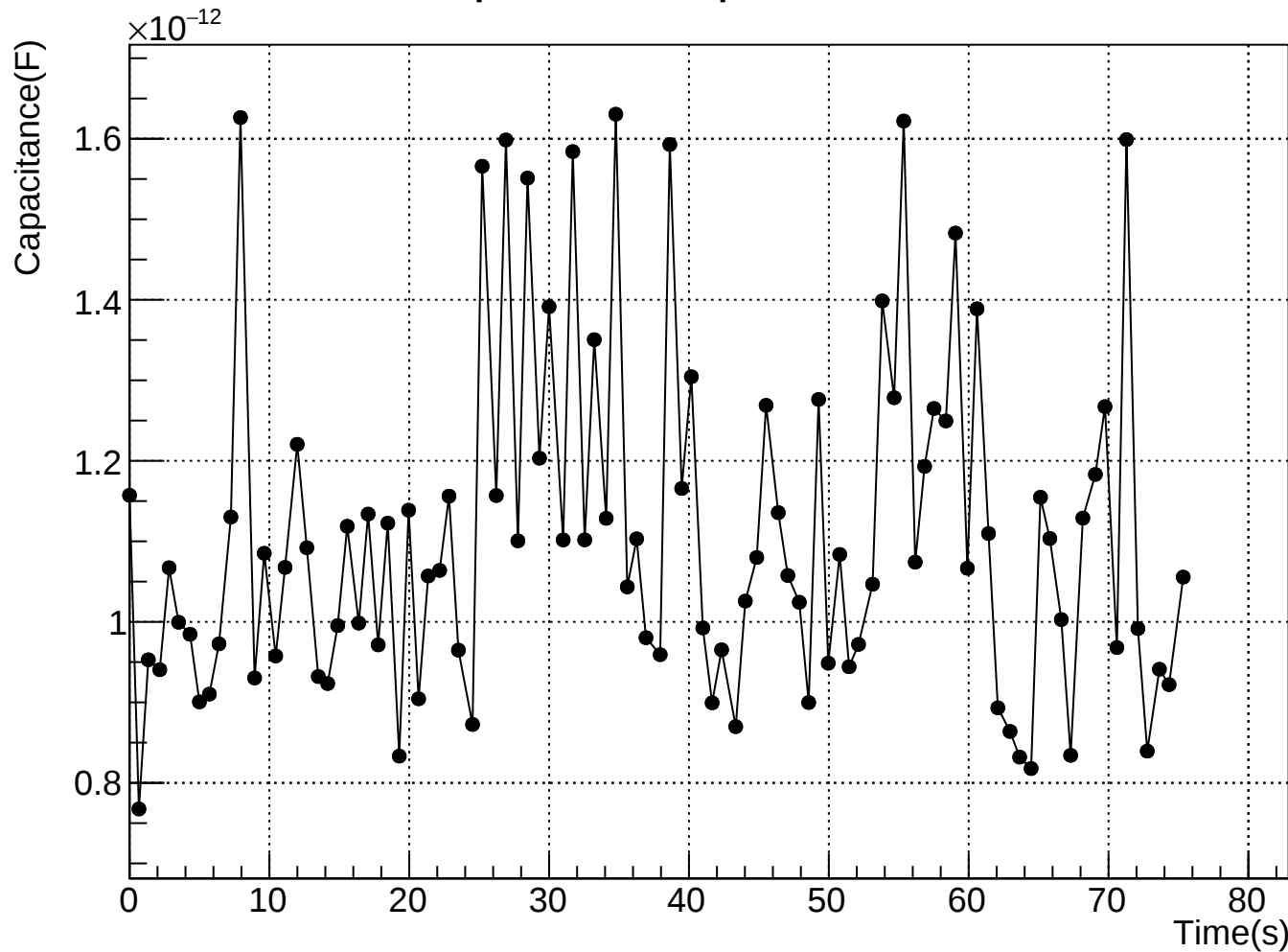
Sample101 Chip5 Channel004



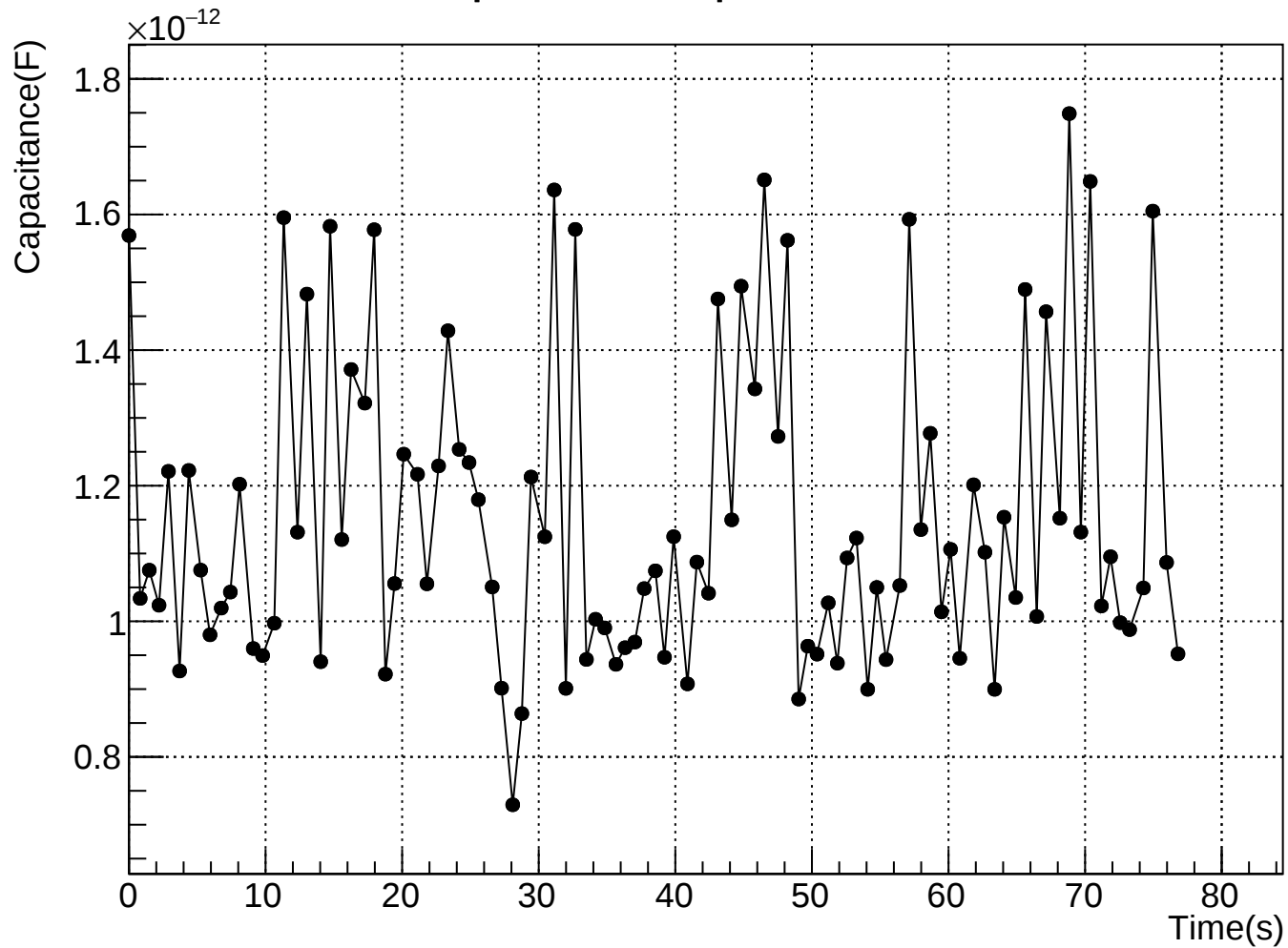
Sample101 Chip5 Channel005



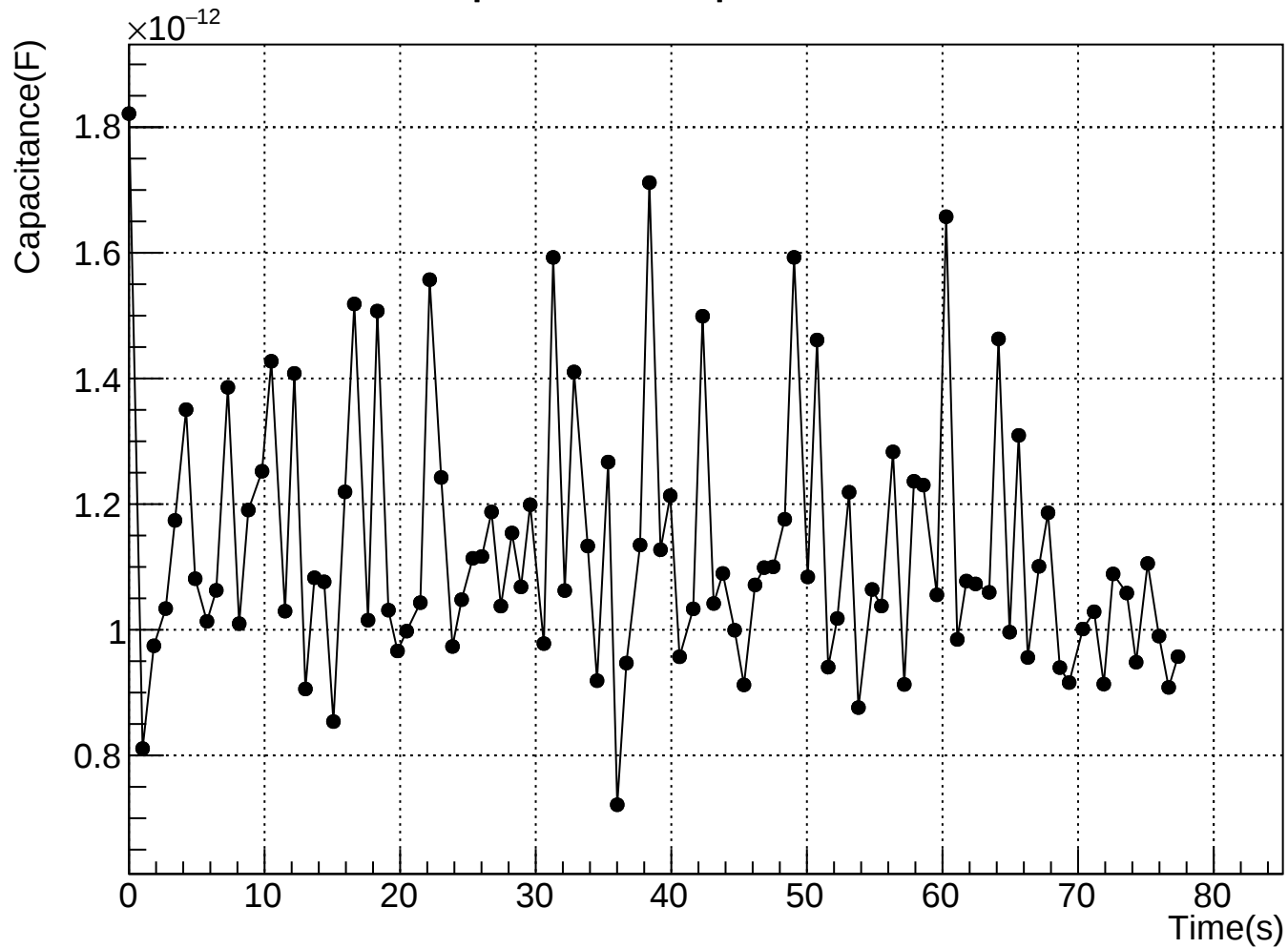
Sample101 Chip5 Channel006



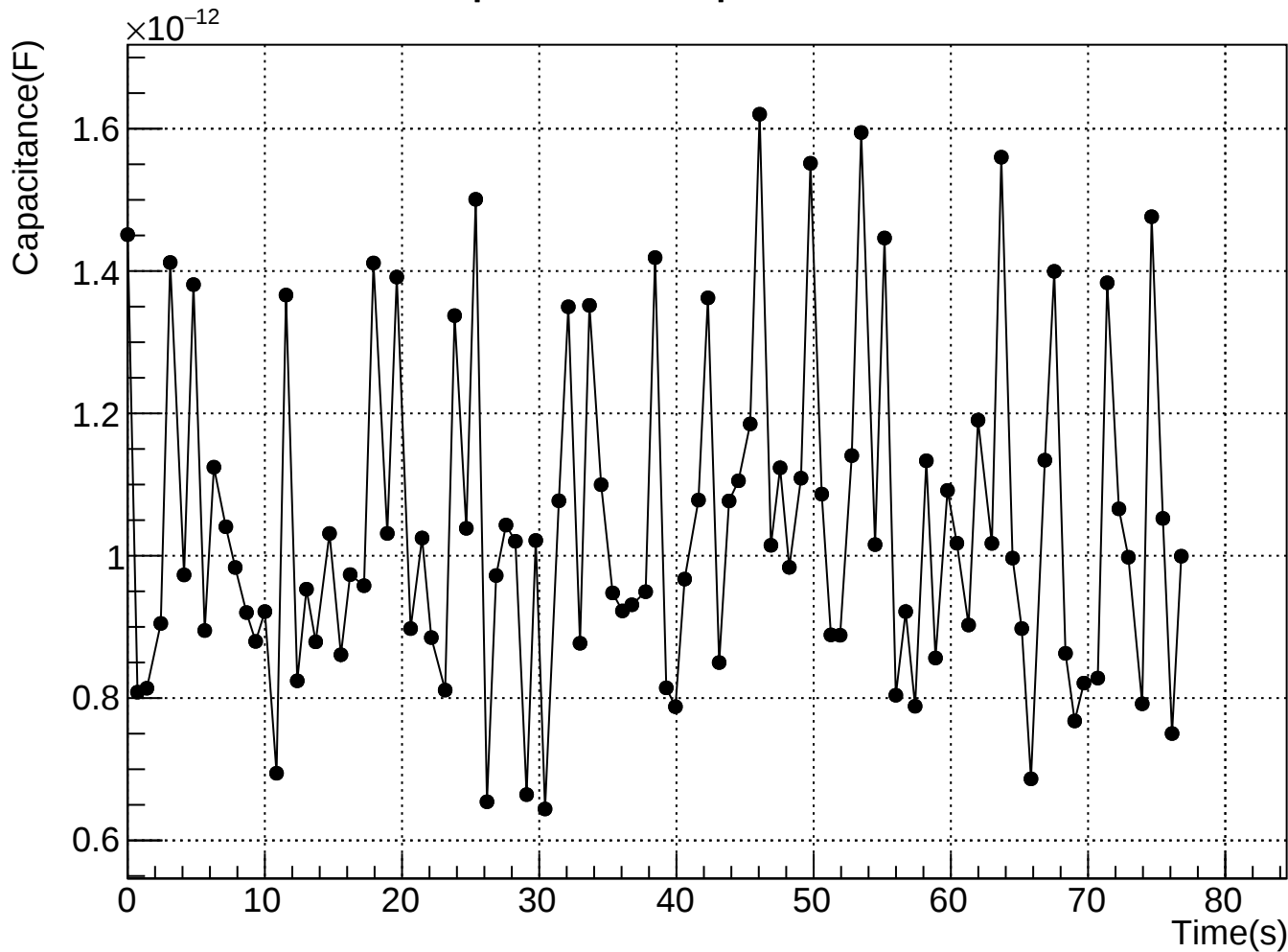
Sample101 Chip5 Channel007



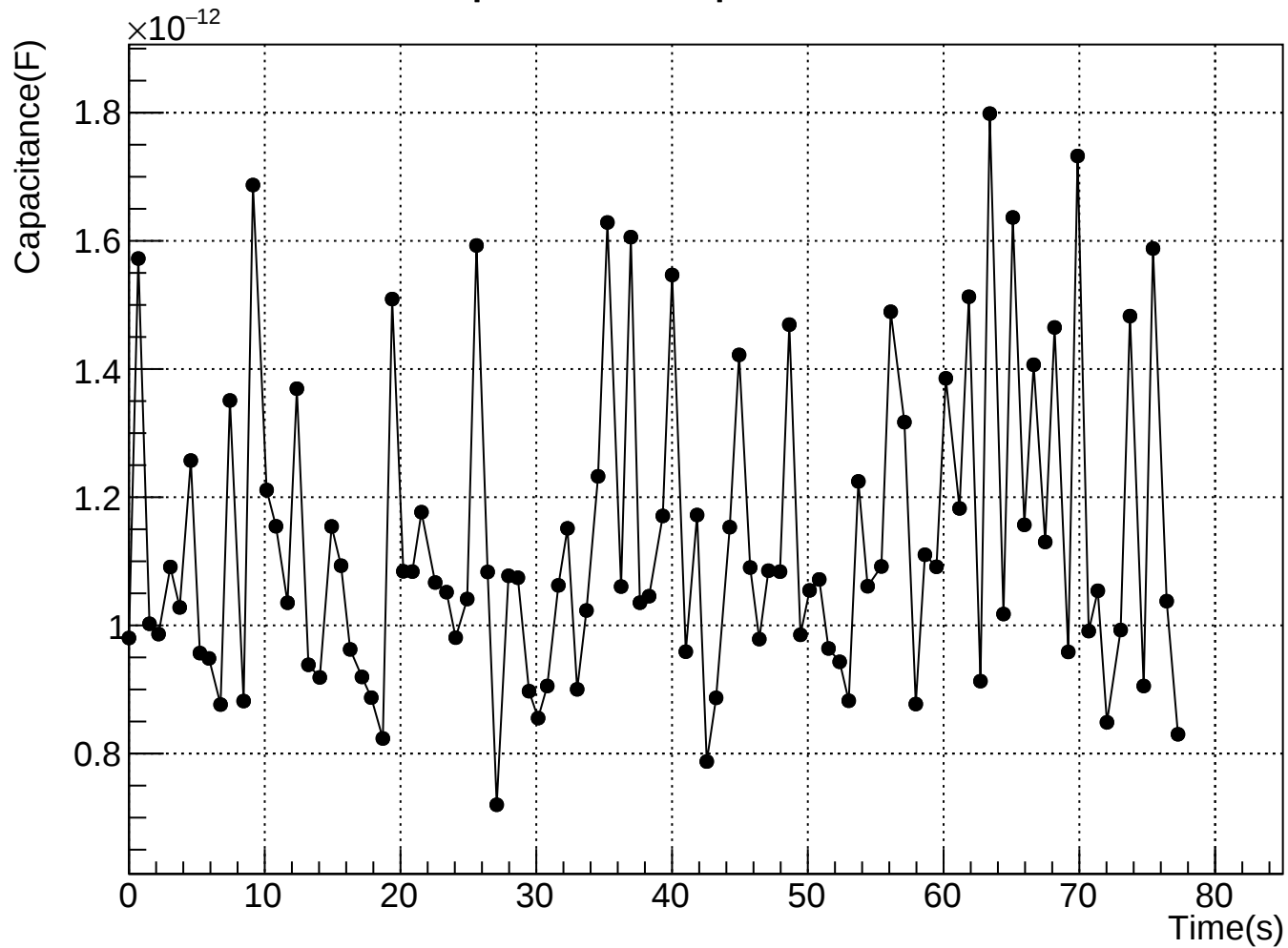
Sample101 Chip5 Channel008



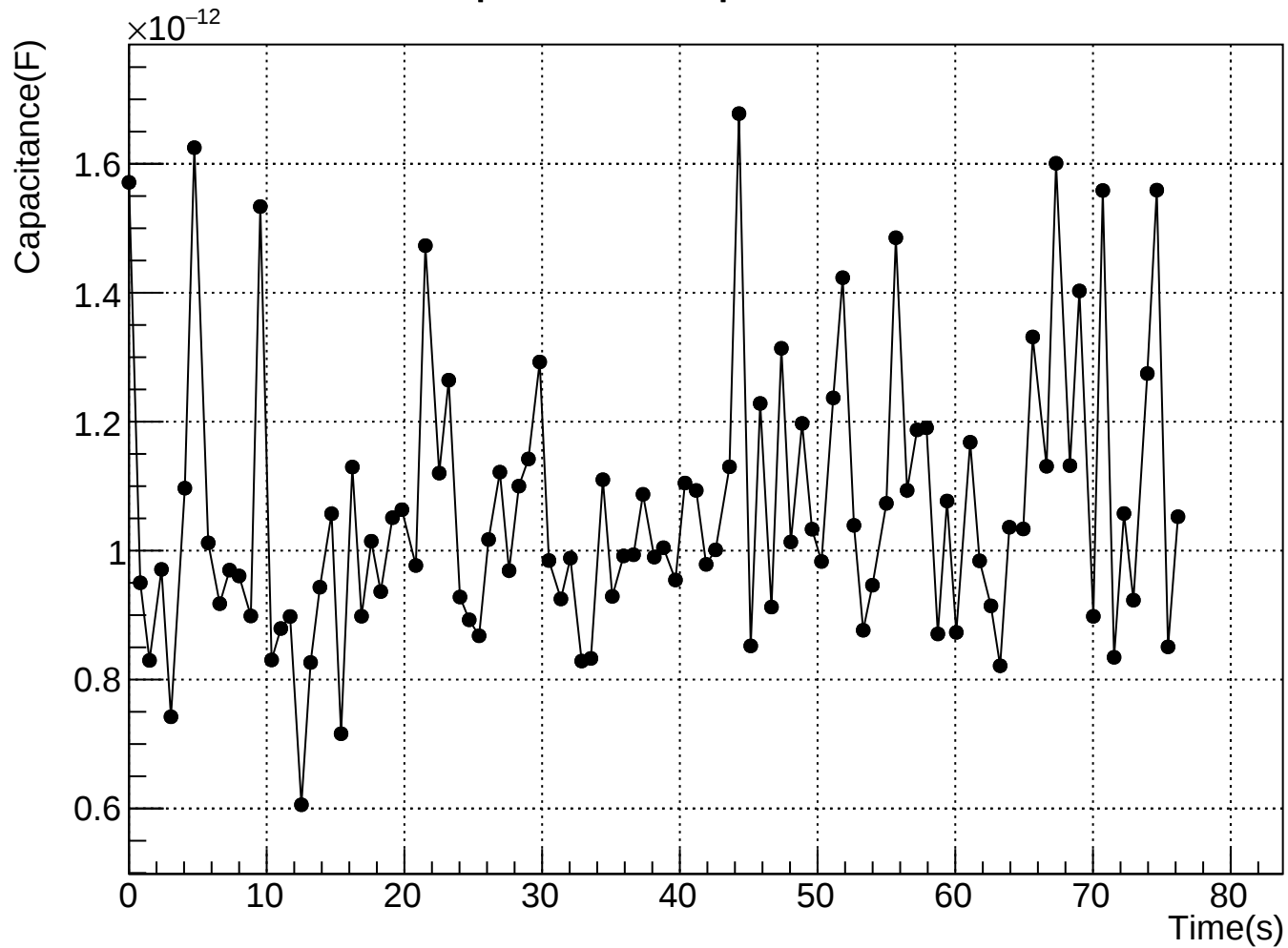
Sample101 Chip5 Channel009



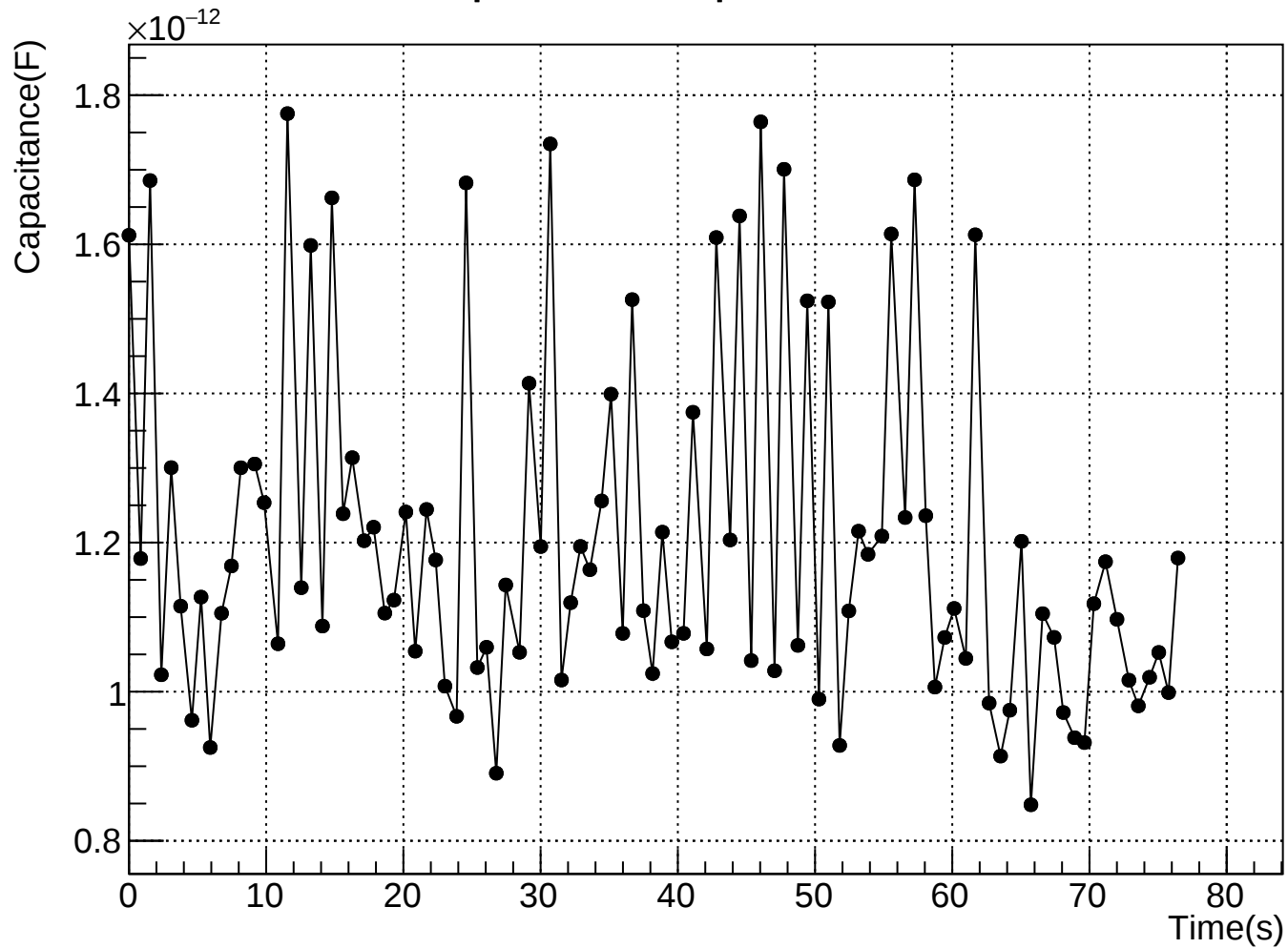
Sample101 Chip5 Channel010



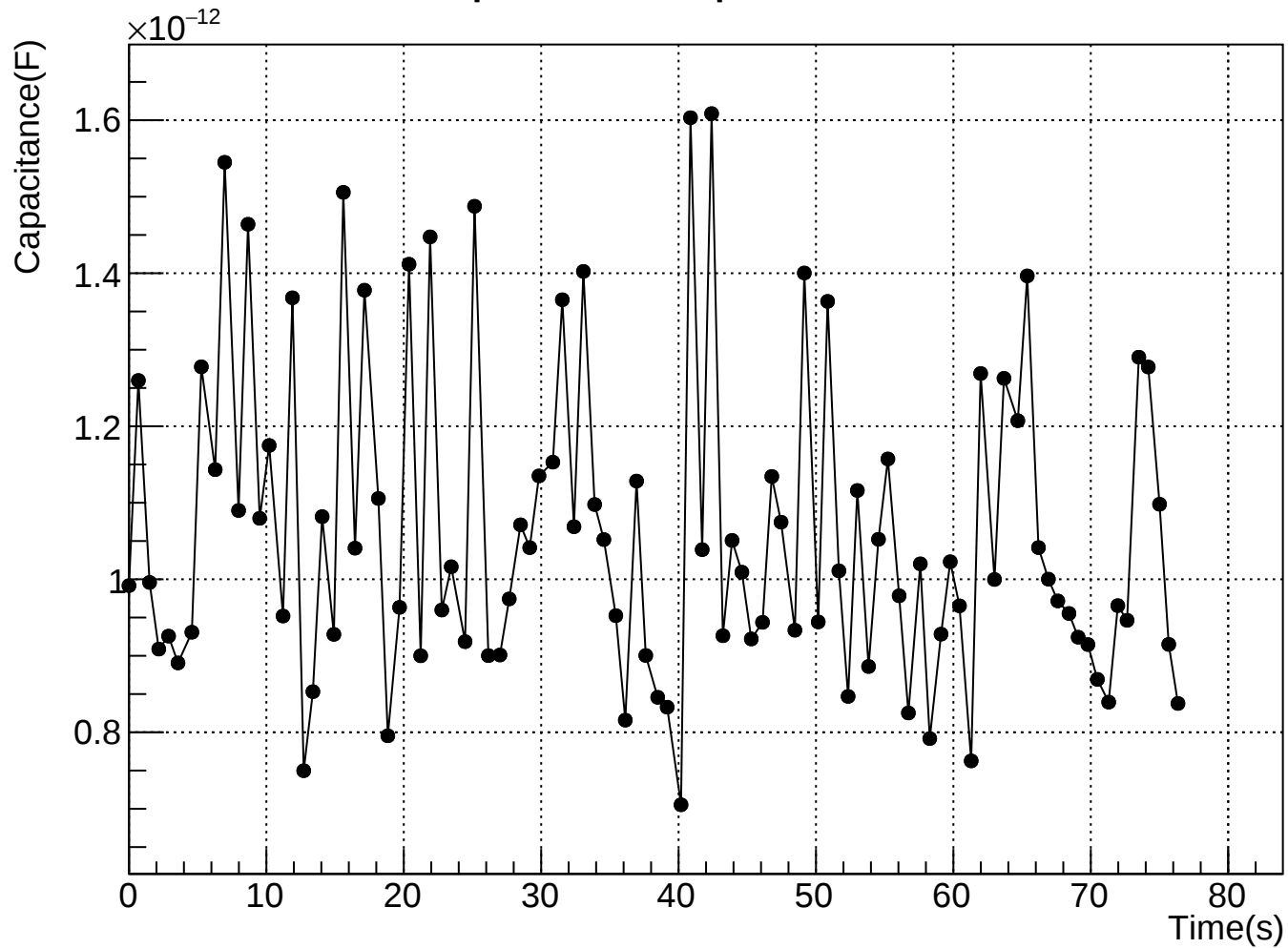
Sample101 Chip5 Channel011



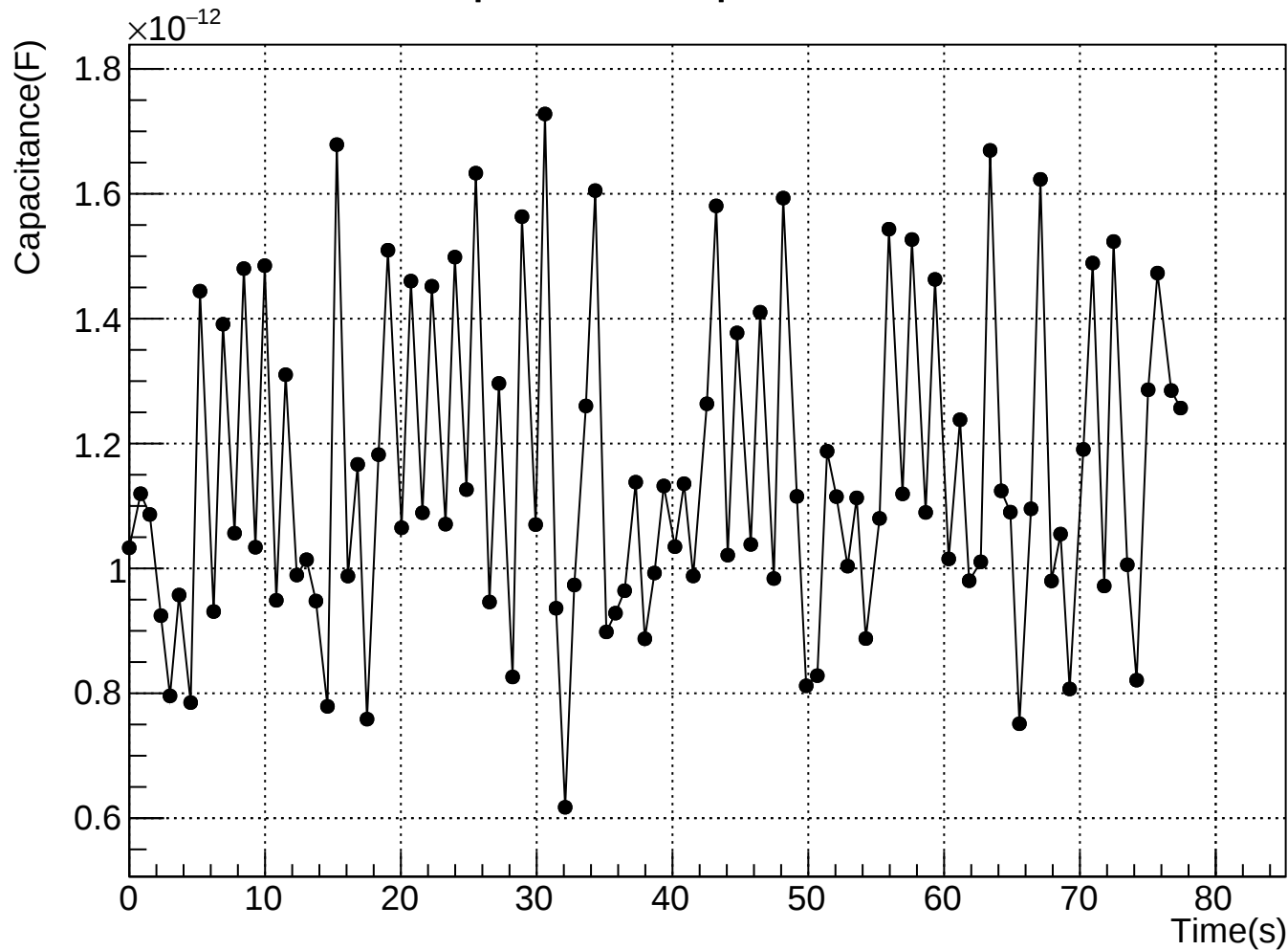
Sample101 Chip5 Channel012



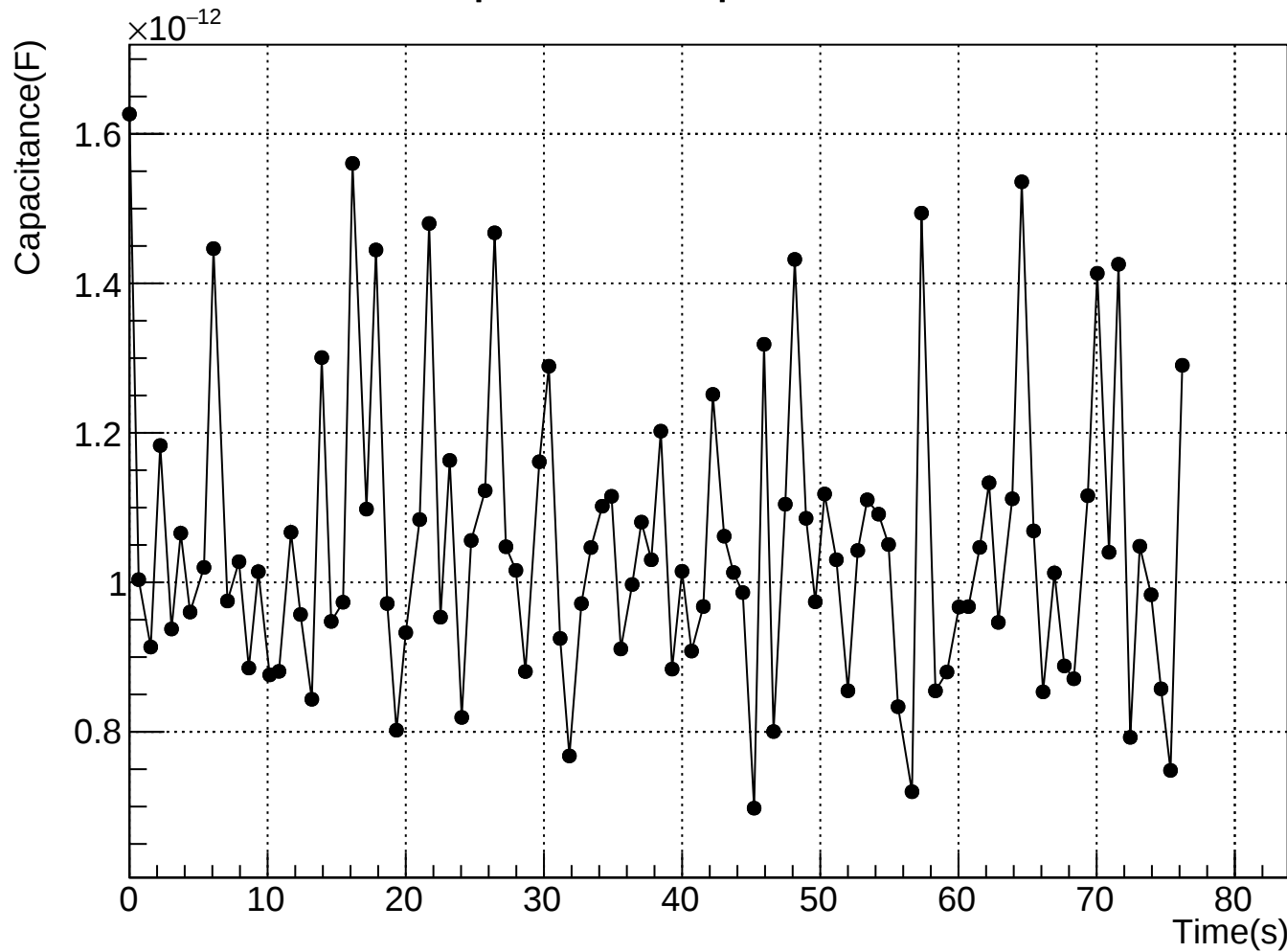
Sample101 Chip5 Channel013



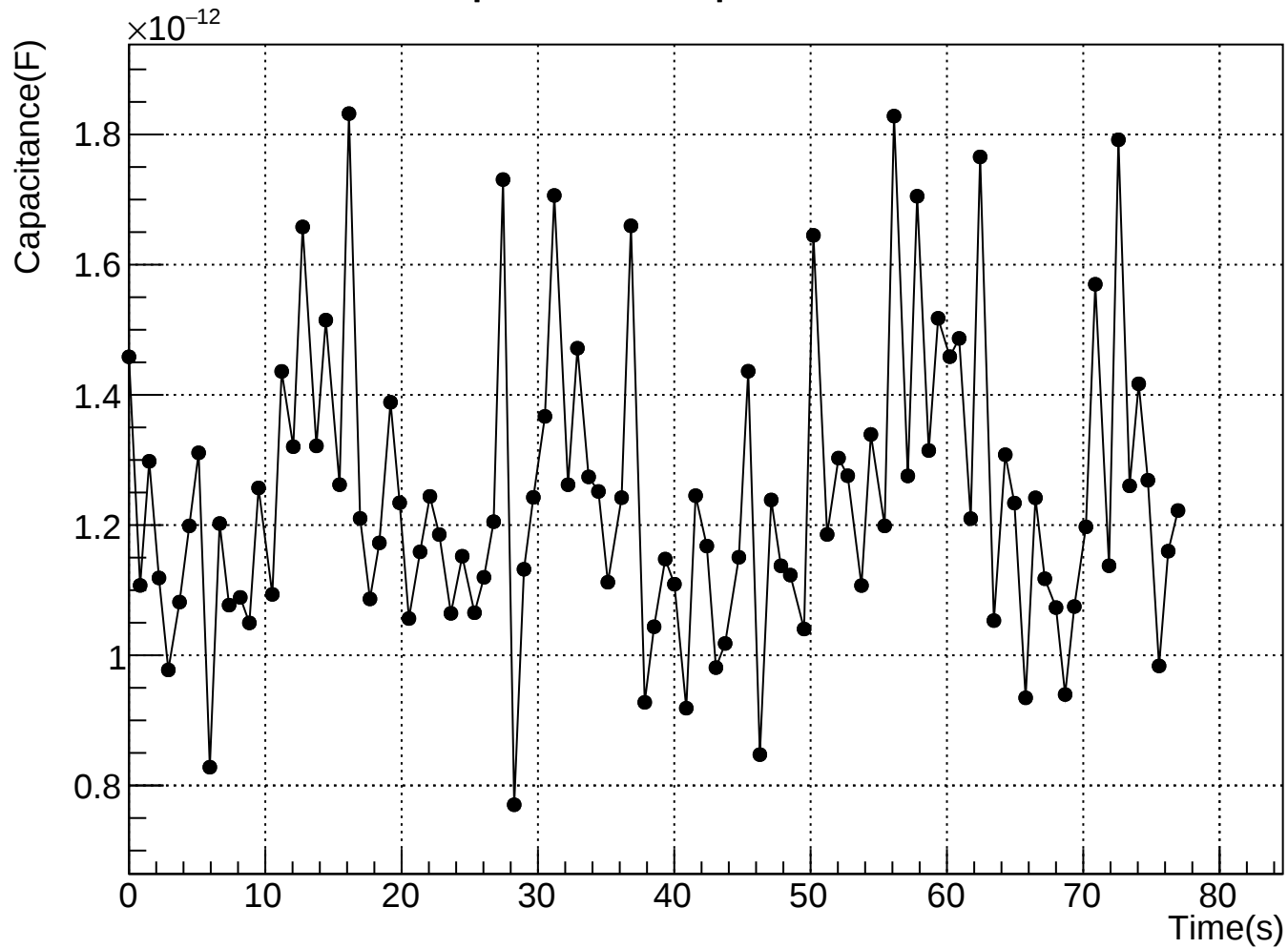
Sample101 Chip5 Channel014



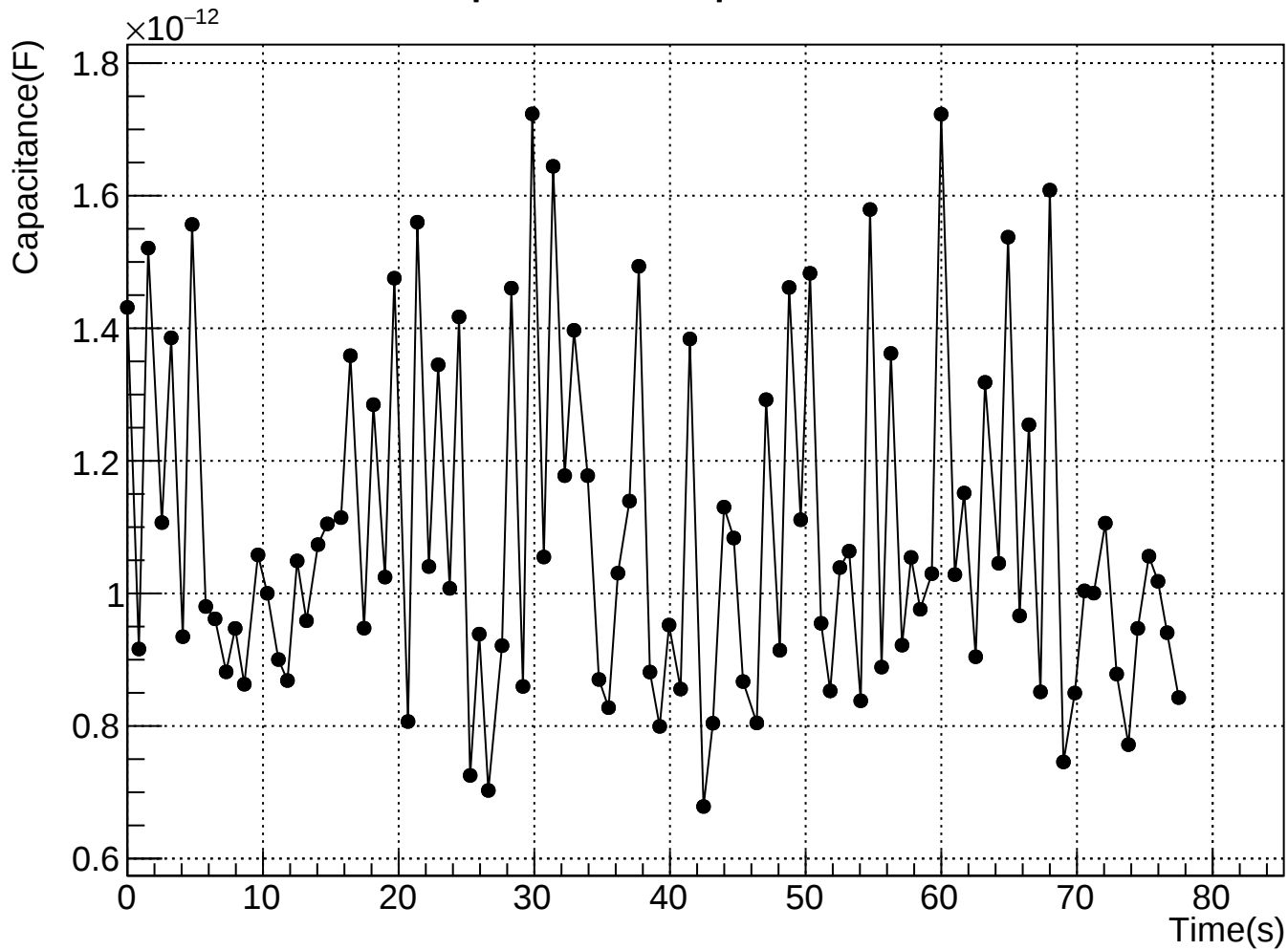
Sample101 Chip5 Channel015



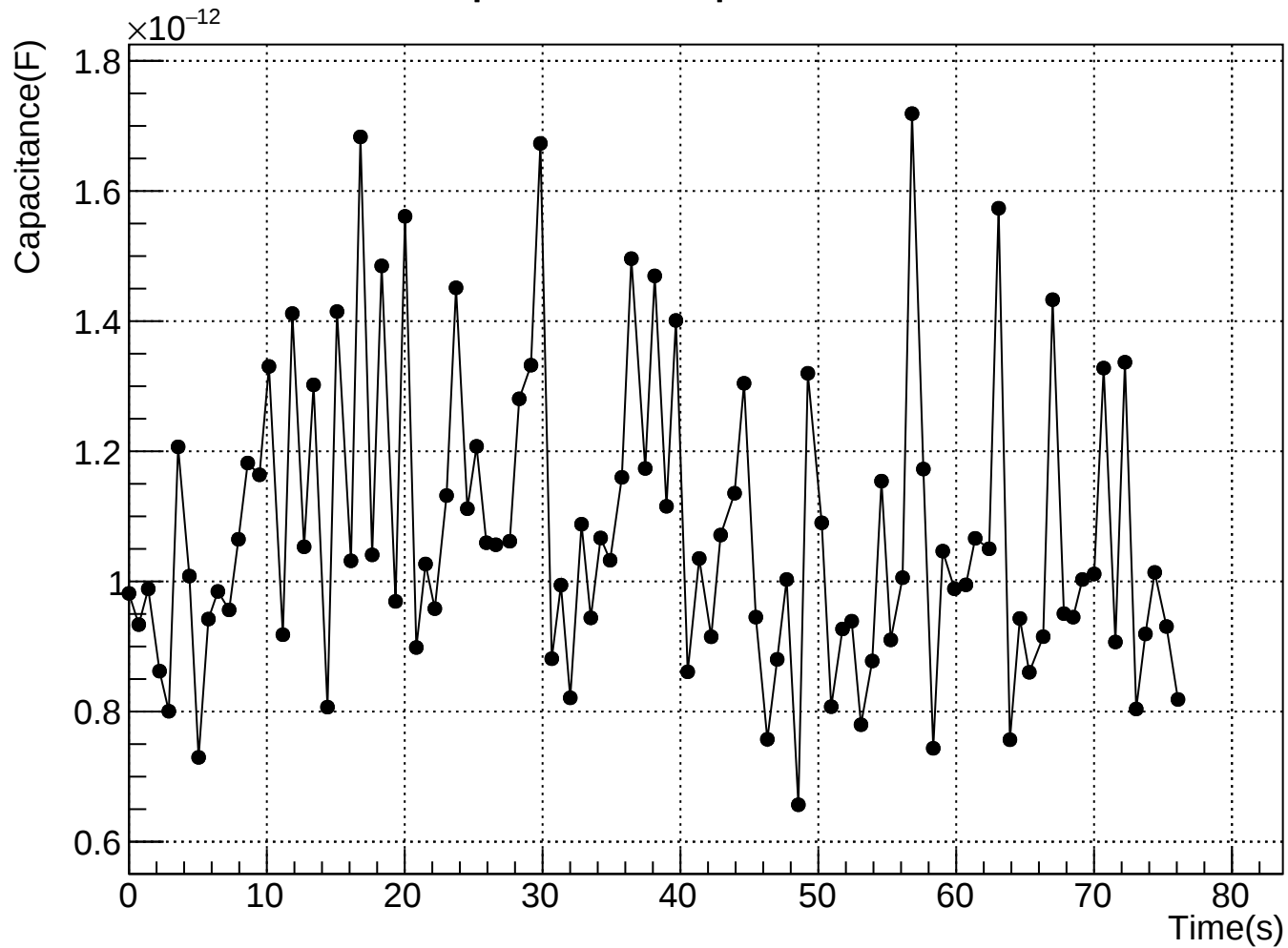
Sample101 Chip5 Channel016



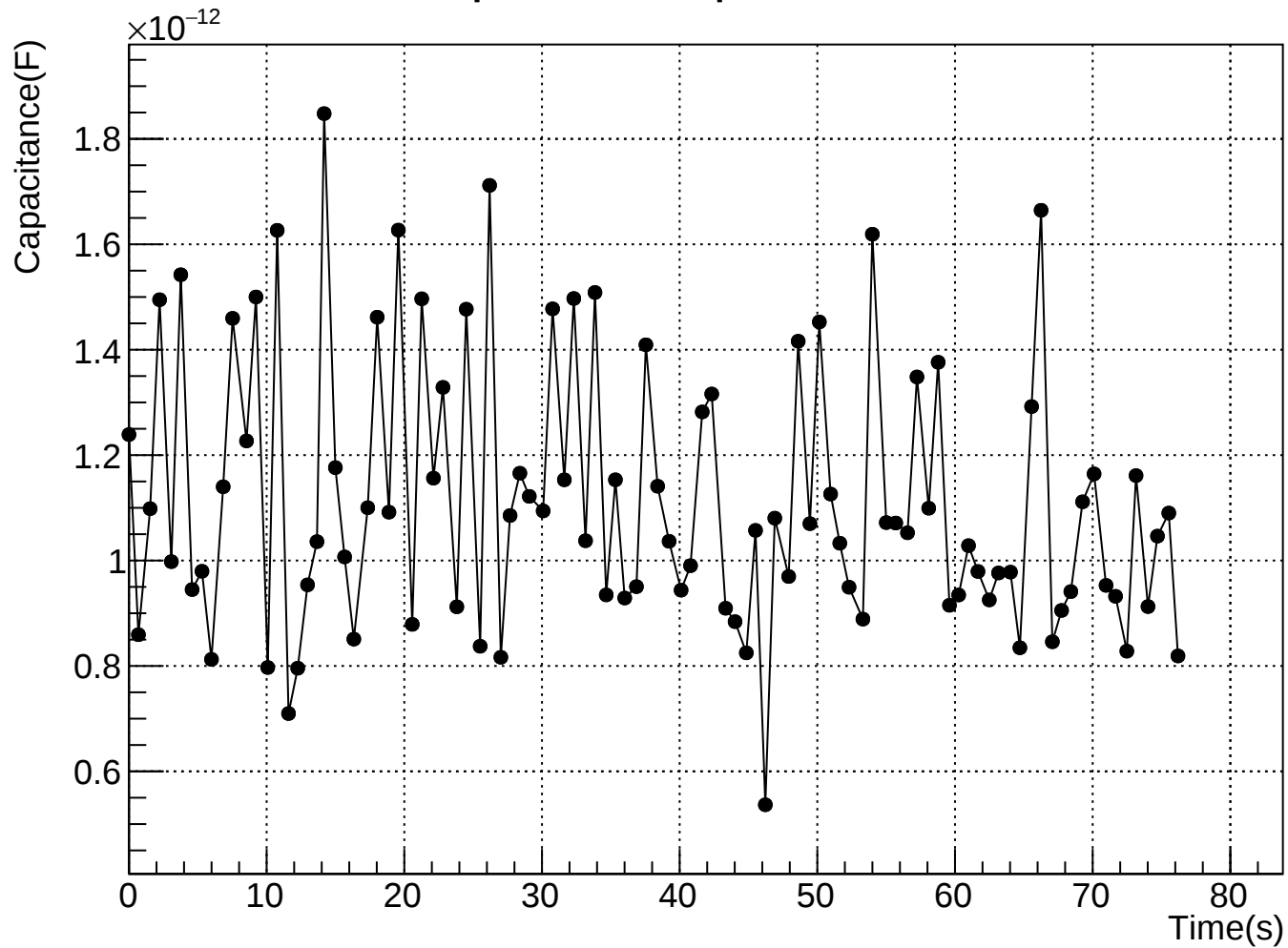
Sample101 Chip5 Channel017



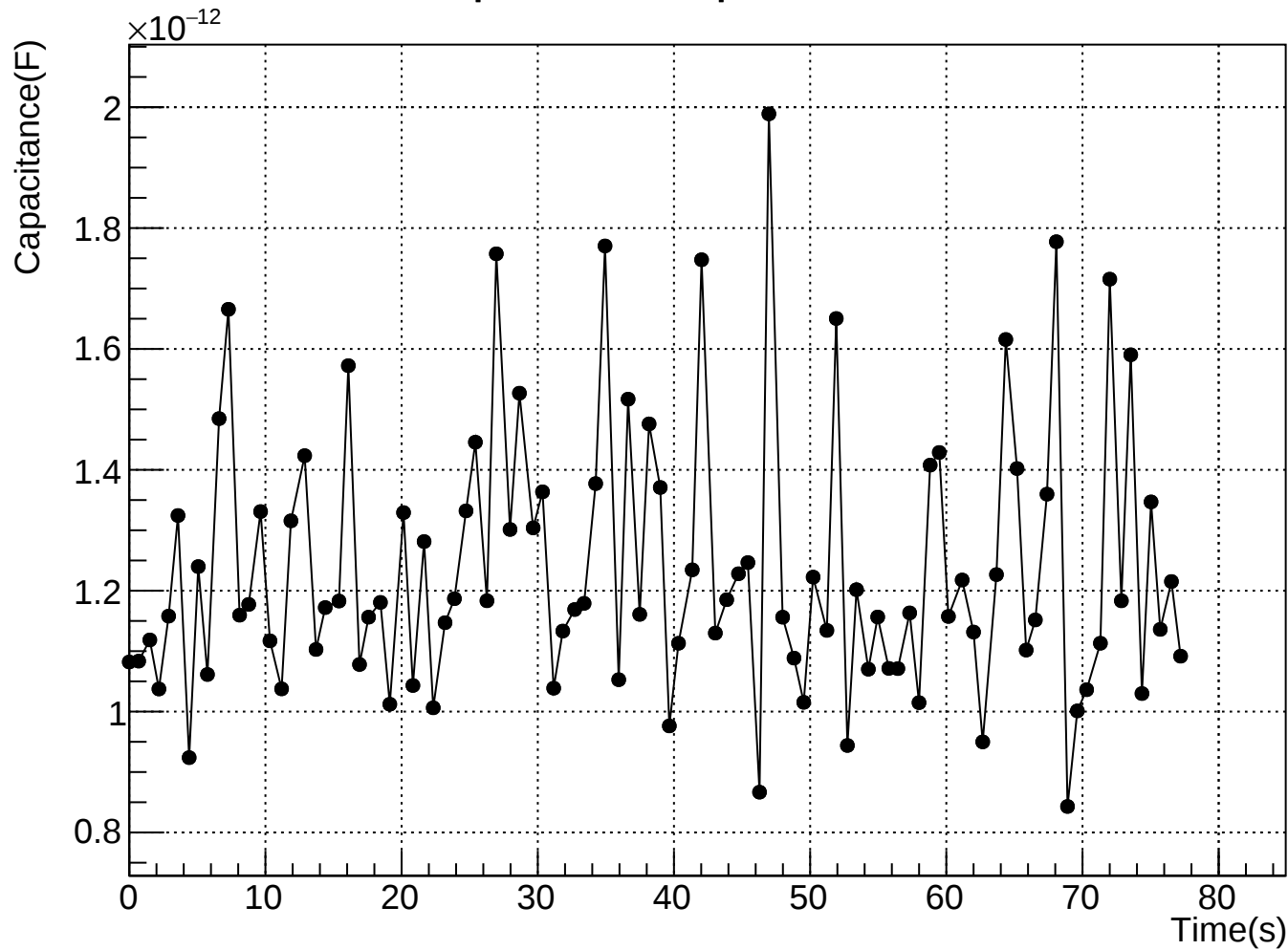
Sample101 Chip5 Channel018



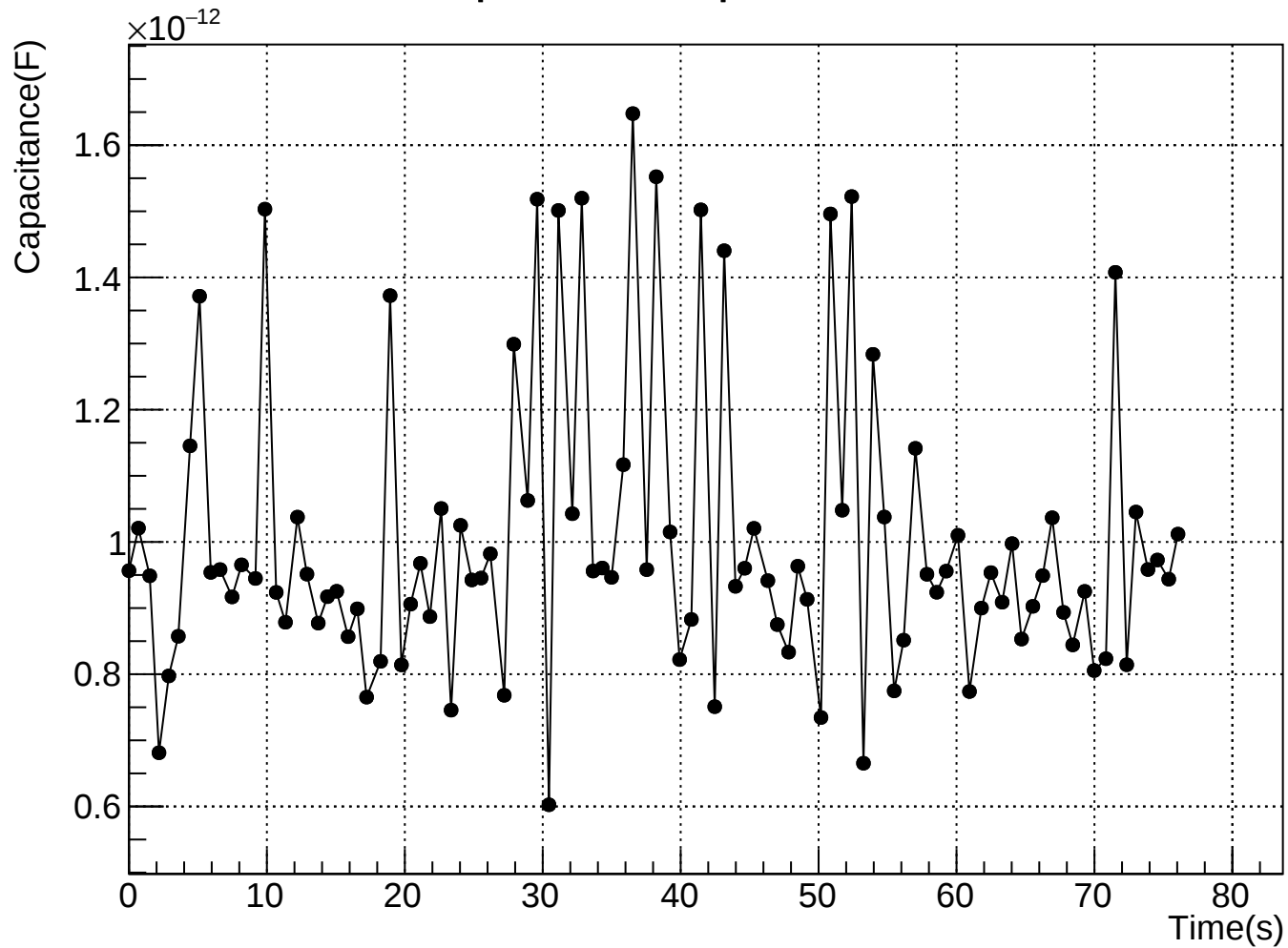
Sample101 Chip5 Channel019



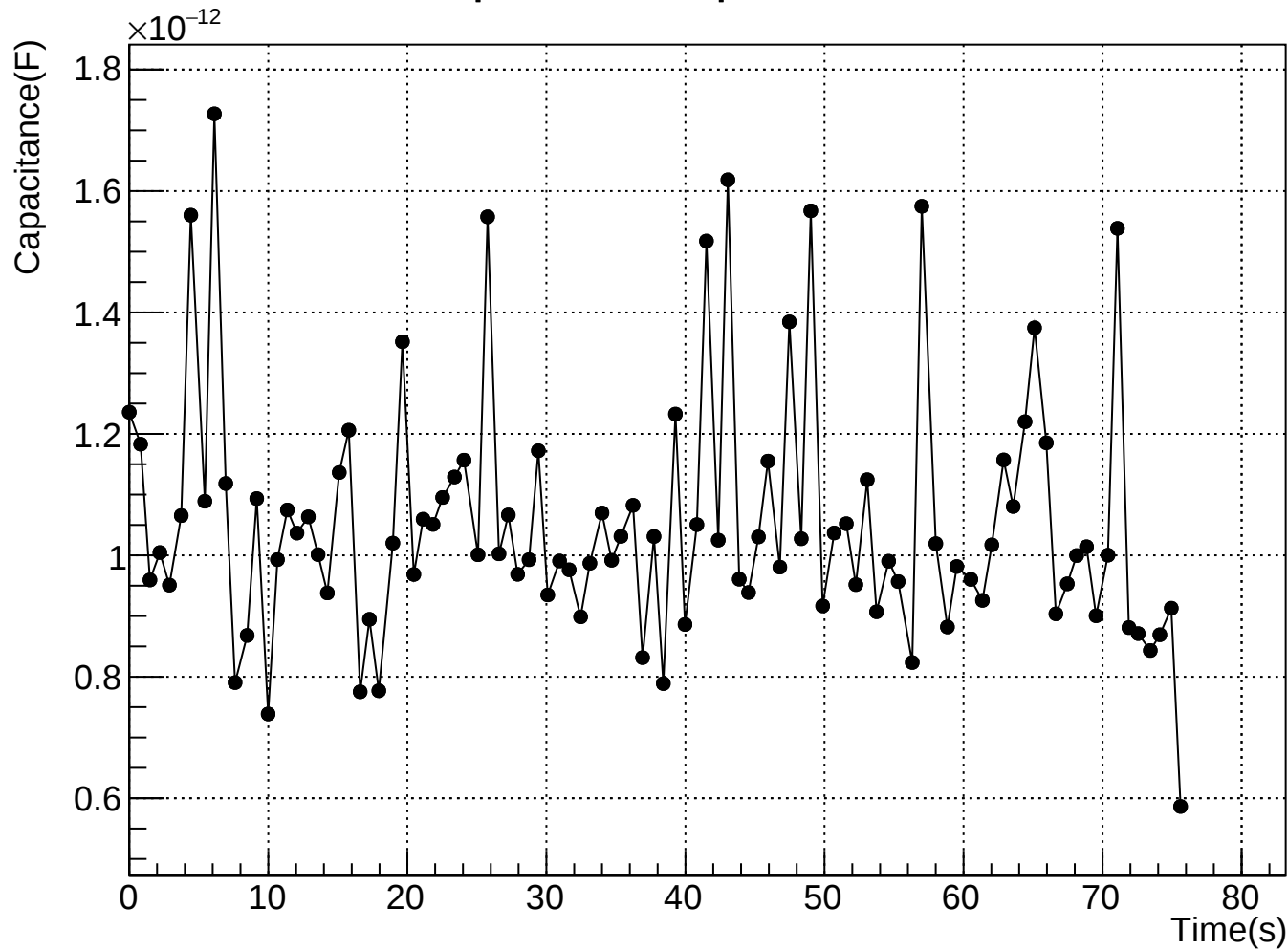
Sample101 Chip5 Channel020



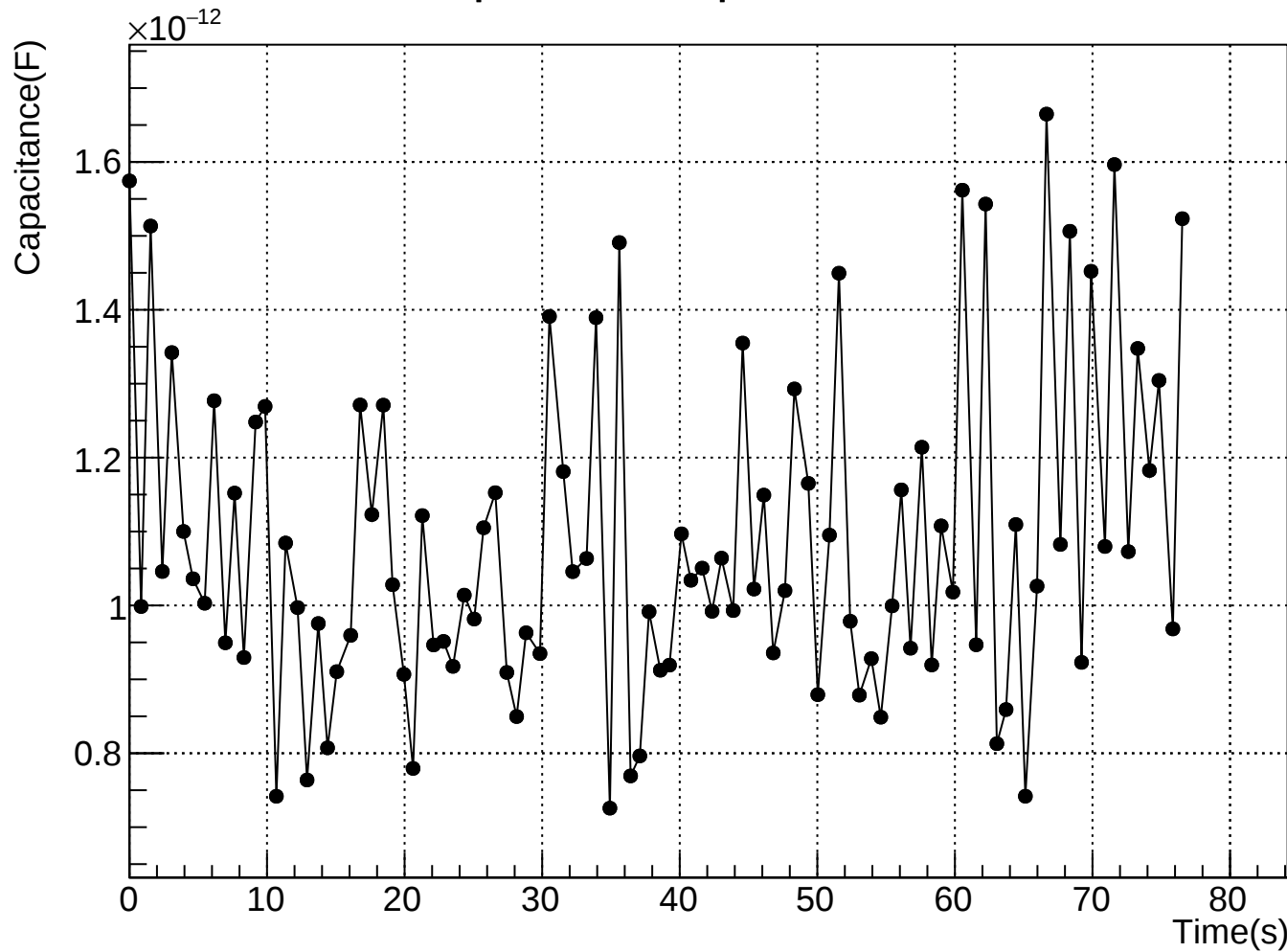
Sample101 Chip5 Channel021



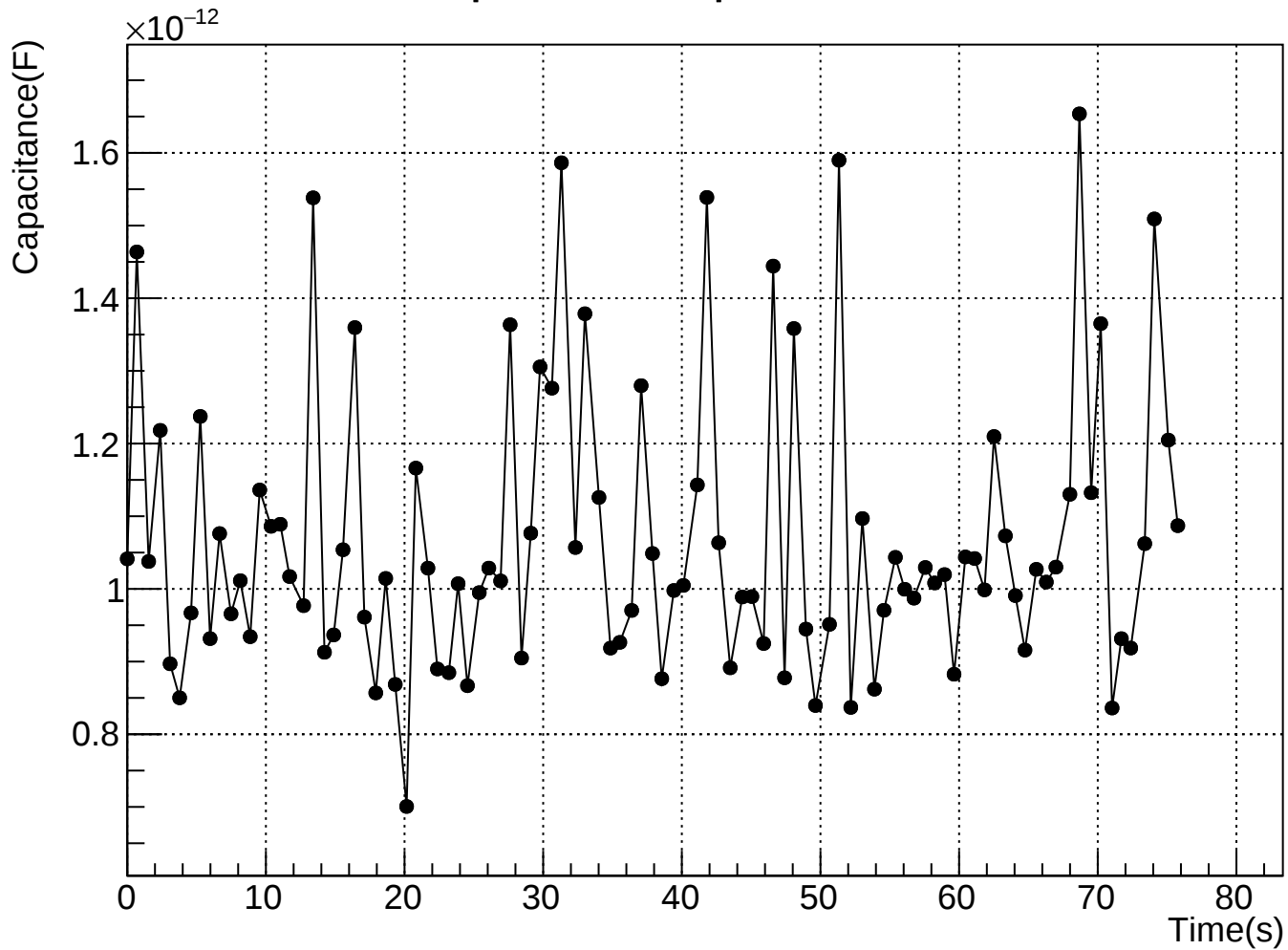
Sample101 Chip5 Channel022



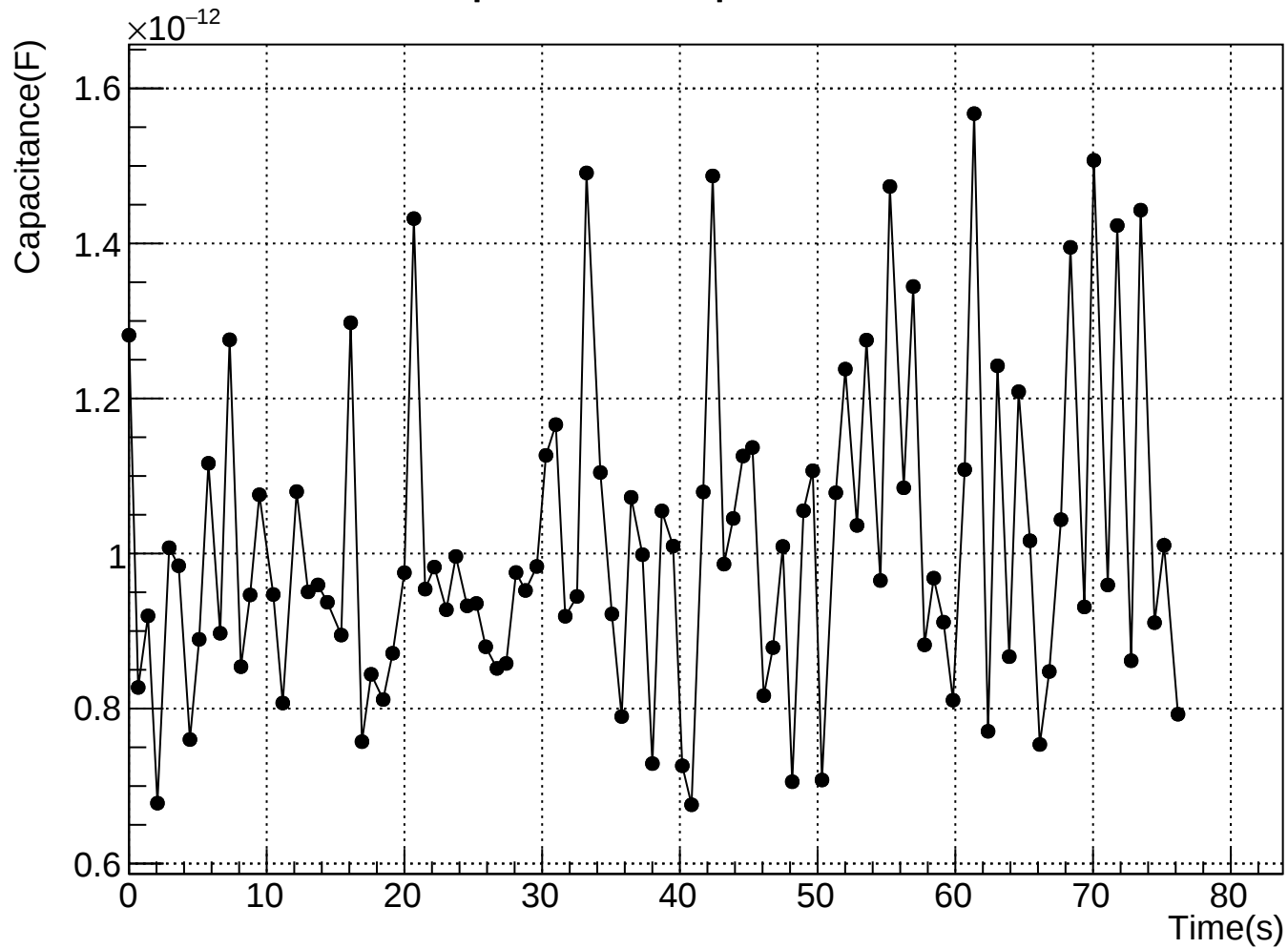
Sample101 Chip5 Channel023



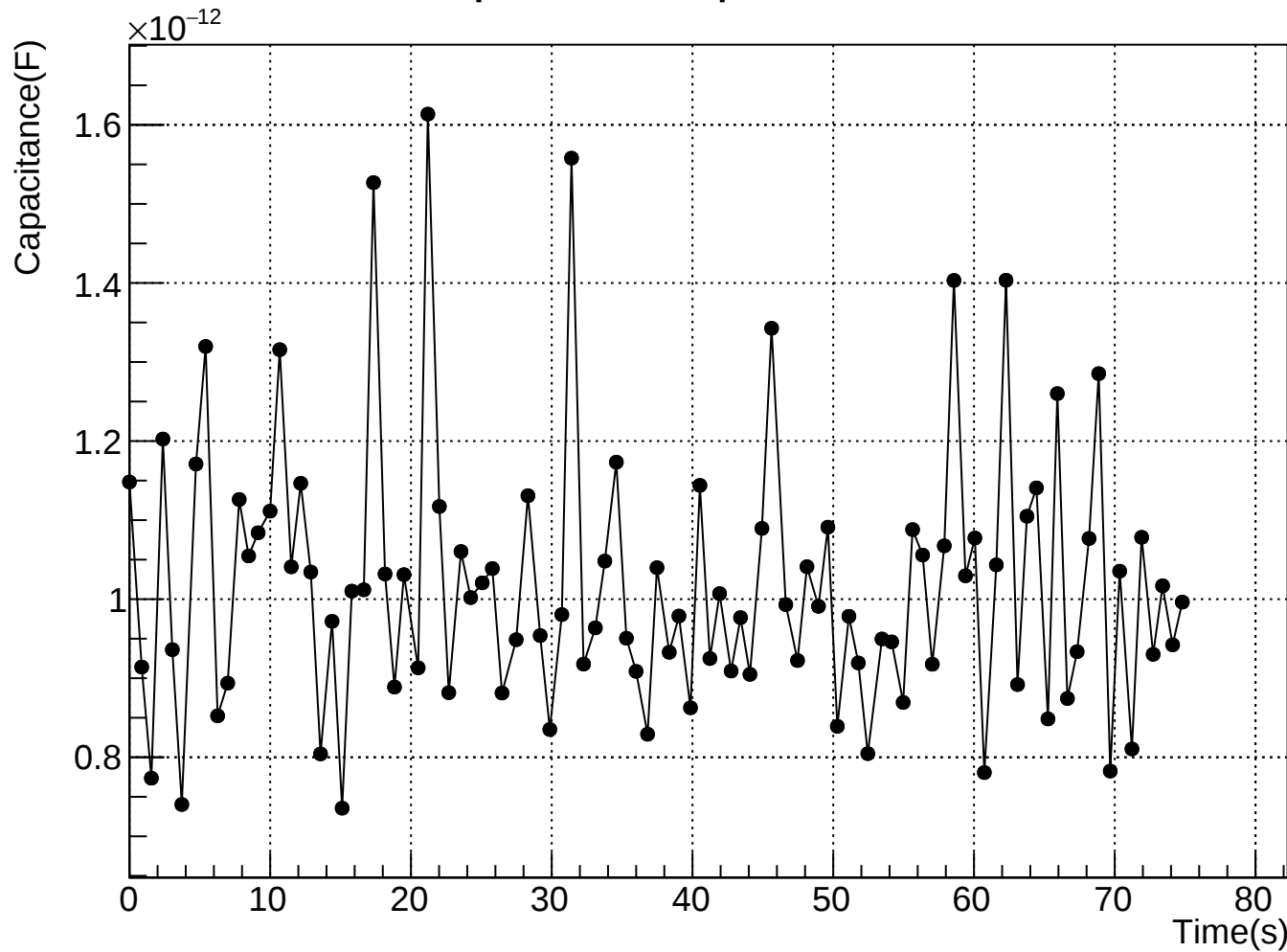
Sample101 Chip5 Channel024



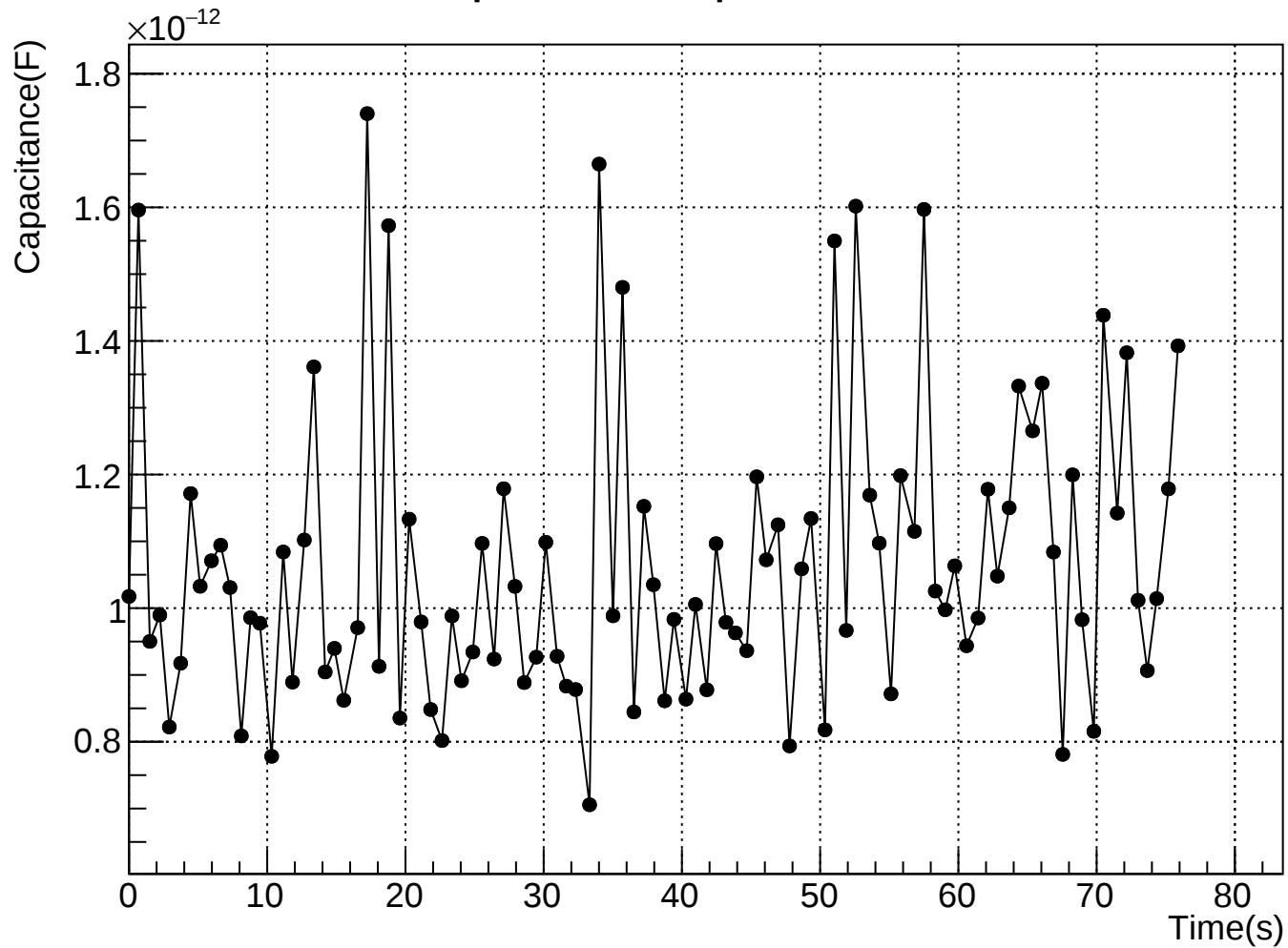
Sample101 Chip5 Channel025



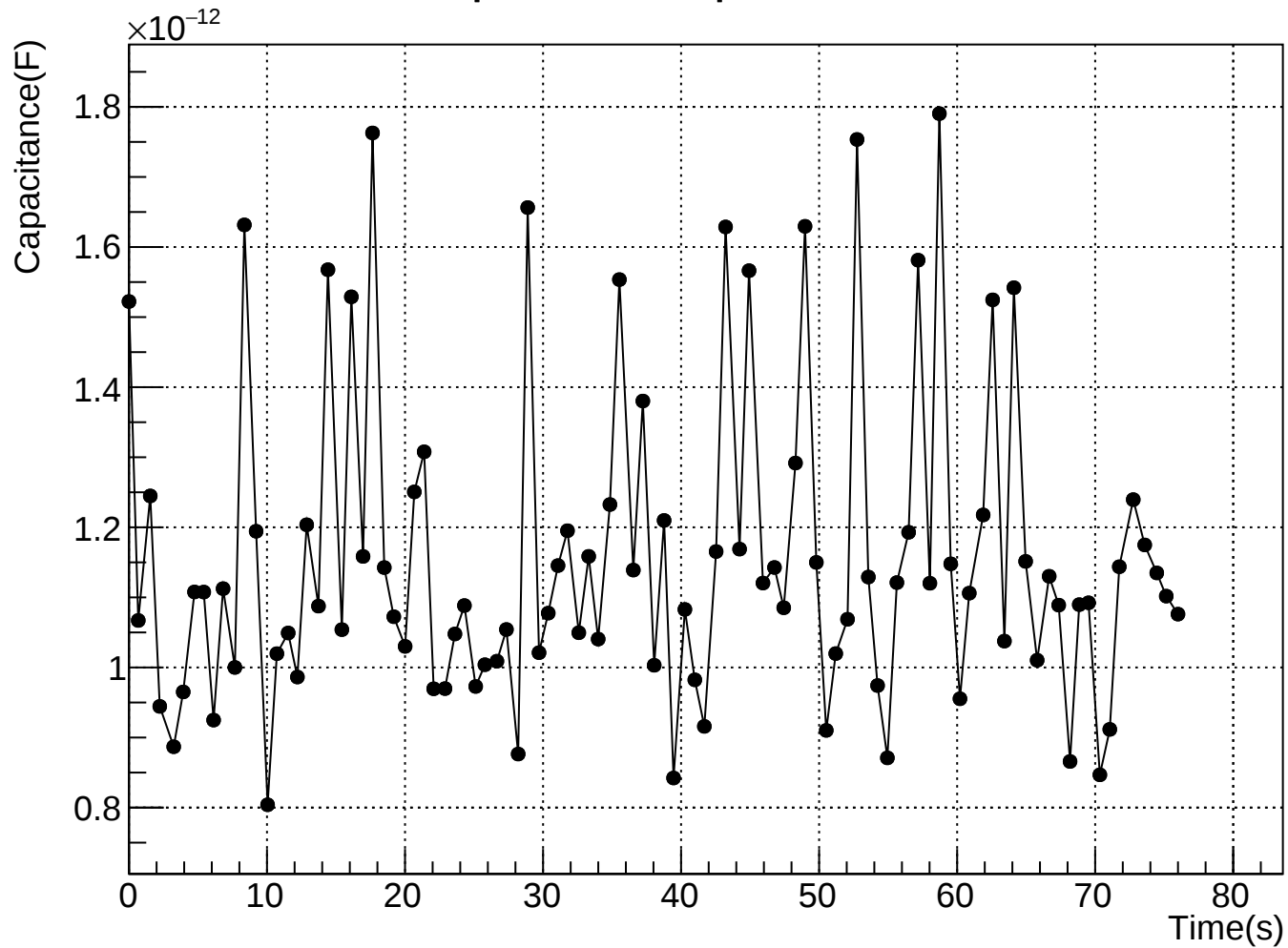
Sample101 Chip5 Channel026



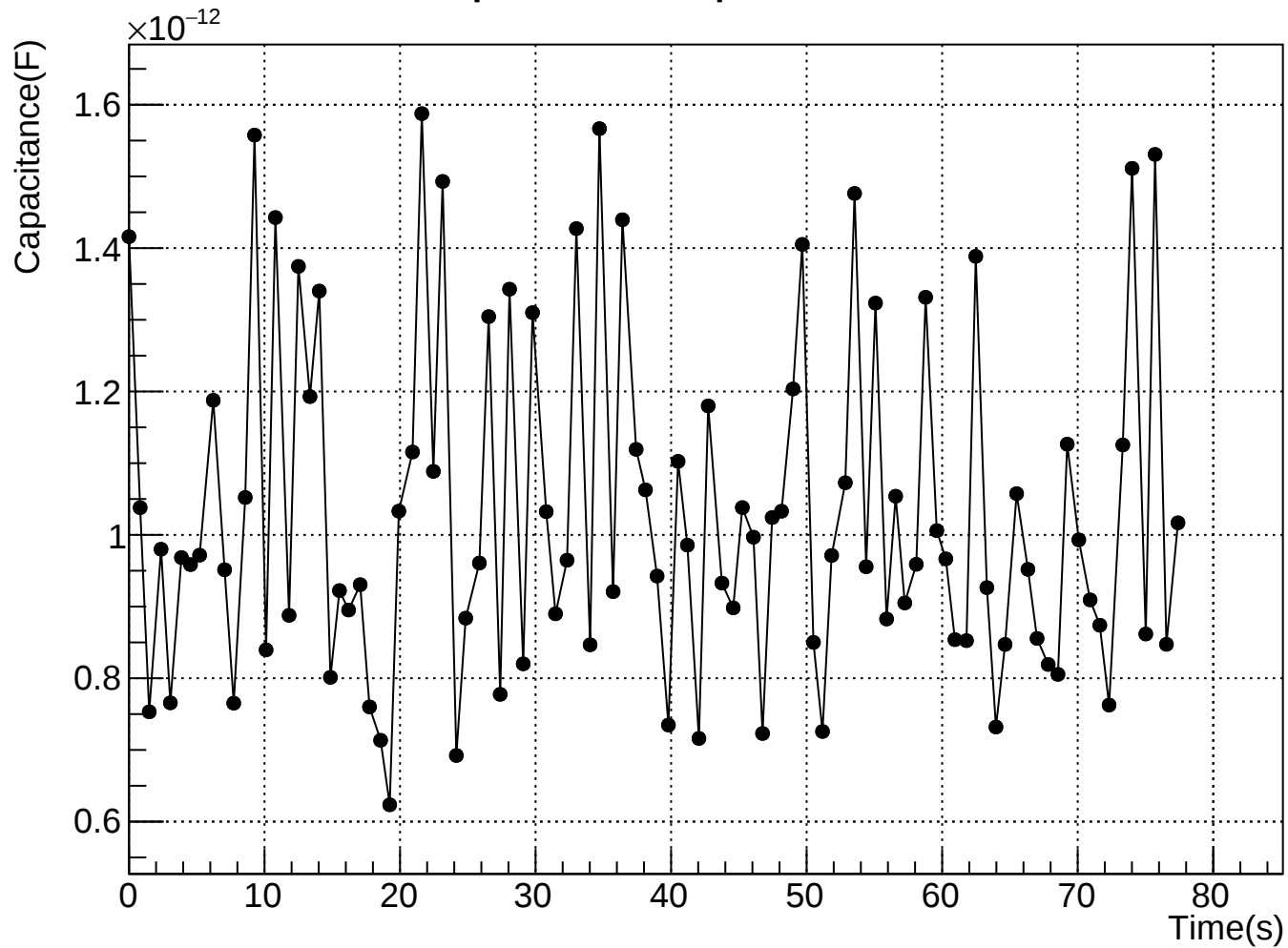
Sample101 Chip5 Channel027



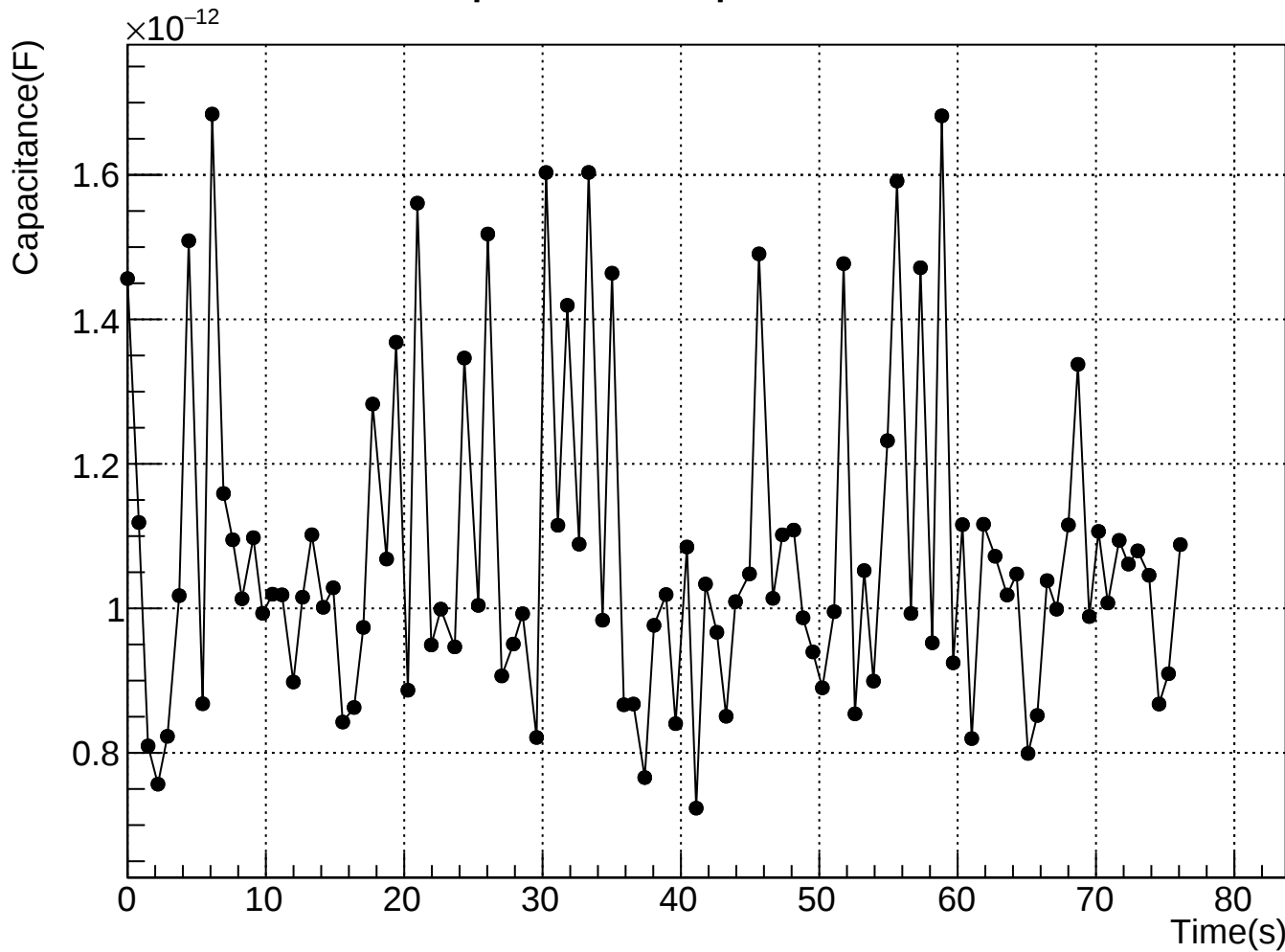
Sample101 Chip5 Channel028



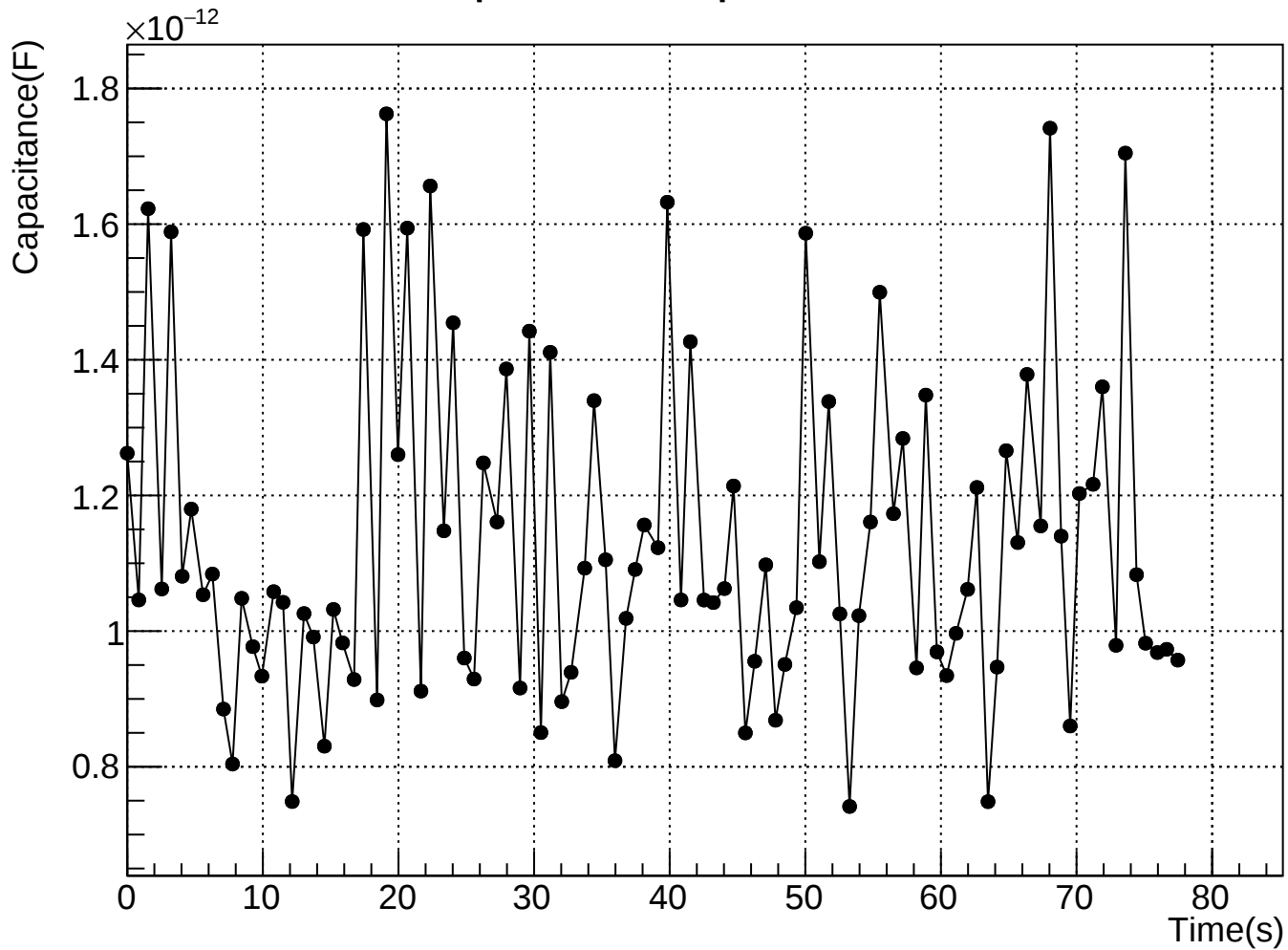
Sample101 Chip5 Channel029



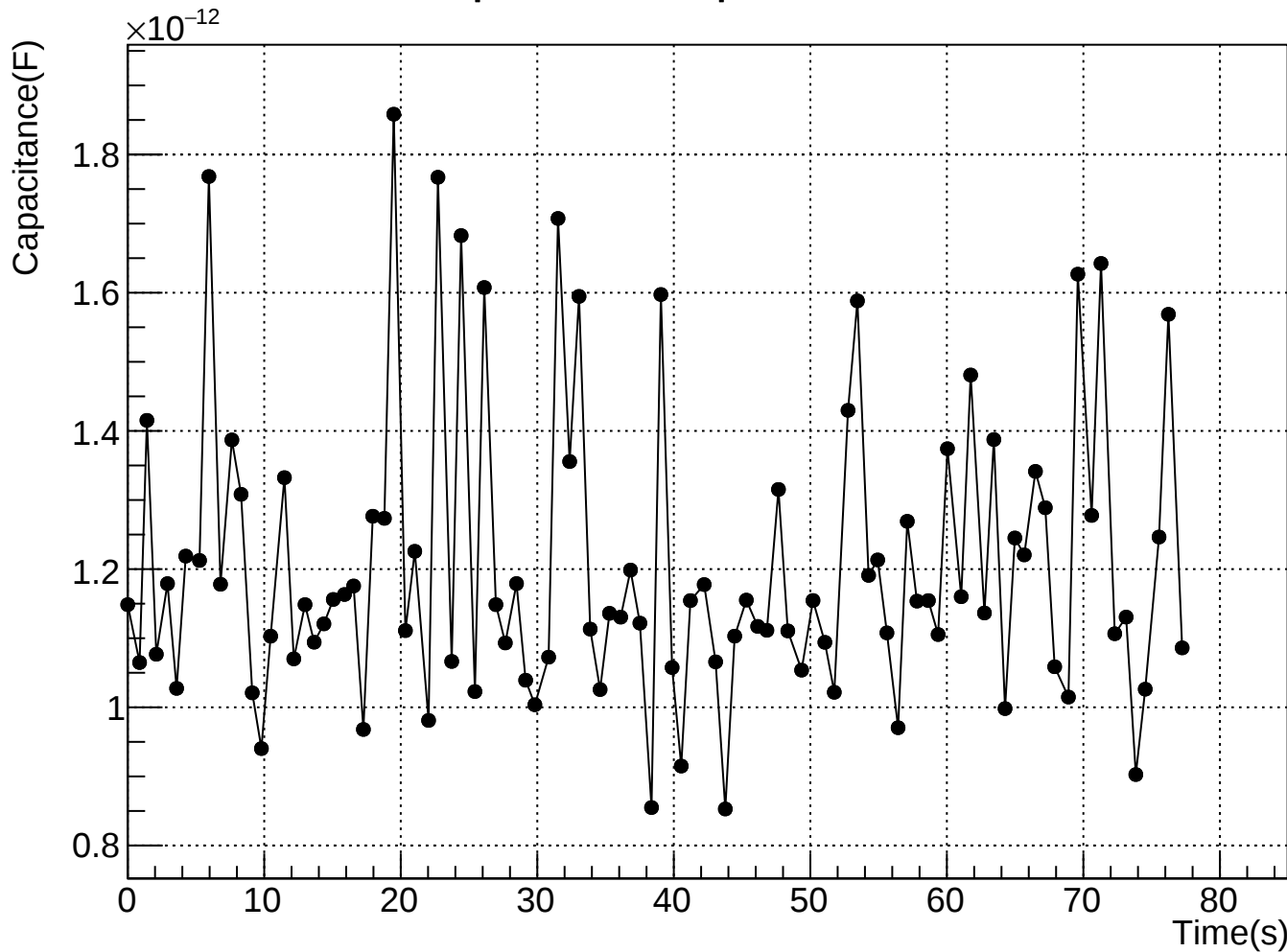
Sample101 Chip5 Channel030



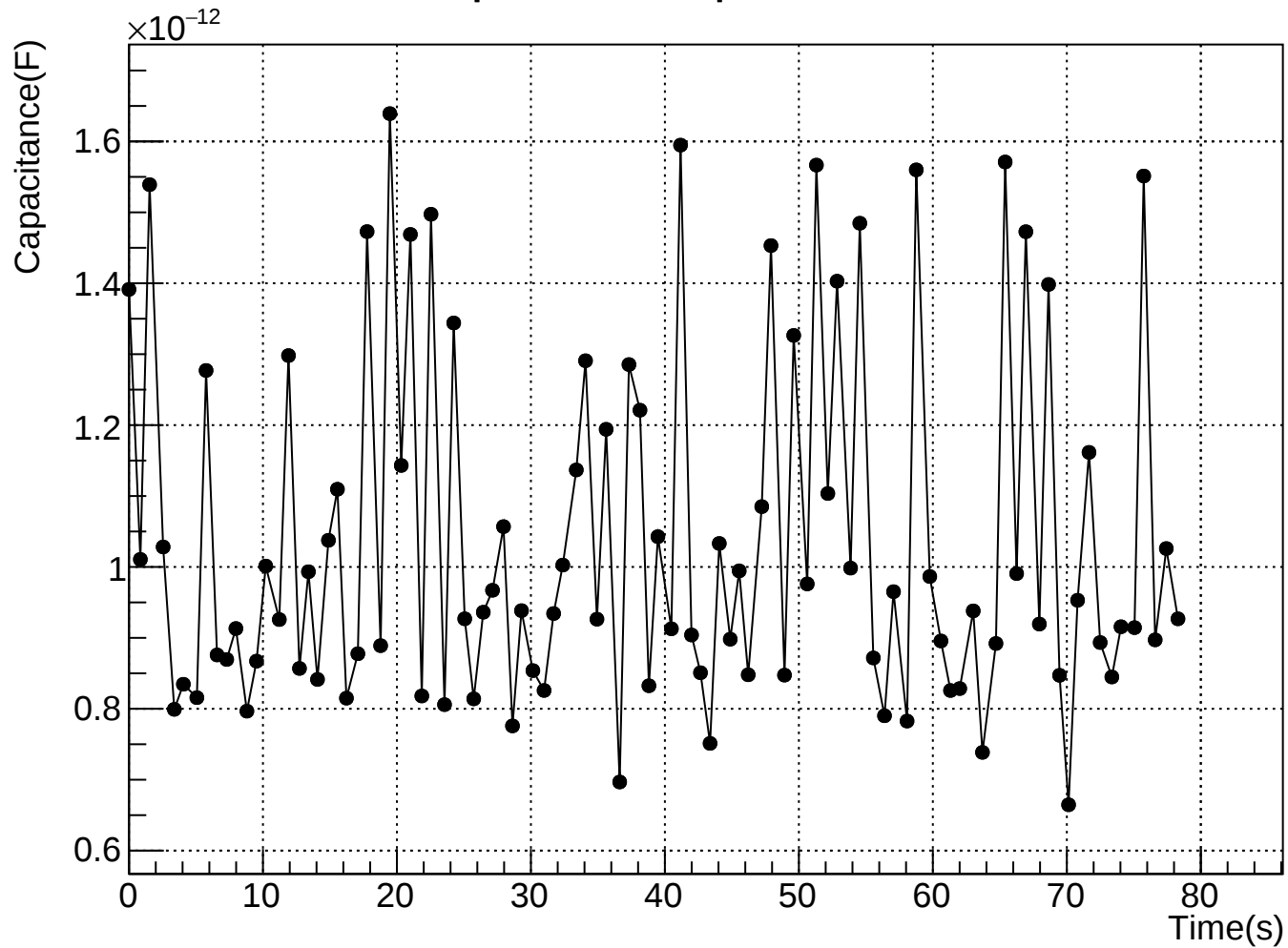
Sample101 Chip5 Channel031



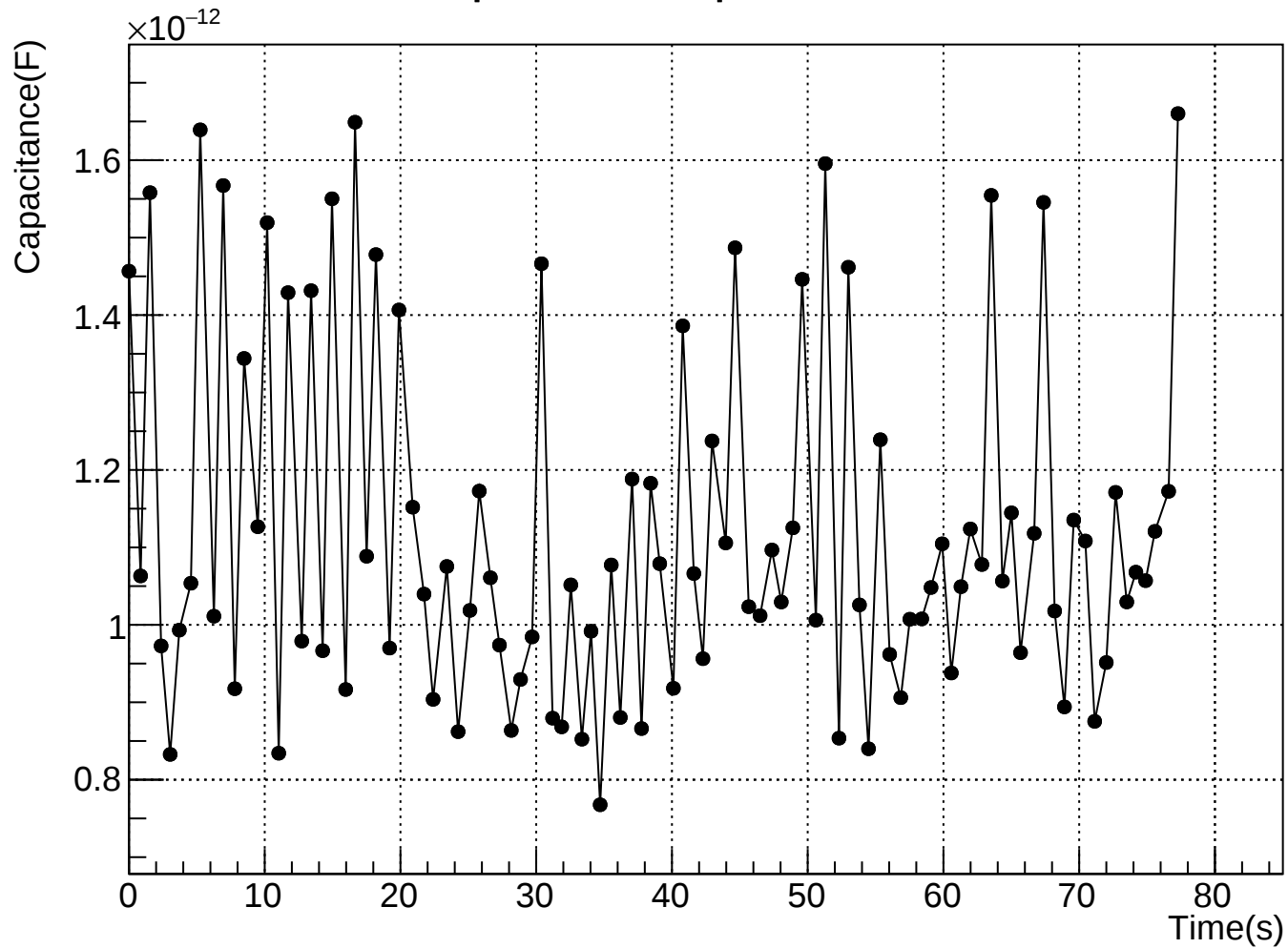
Sample101 Chip5 Channel032



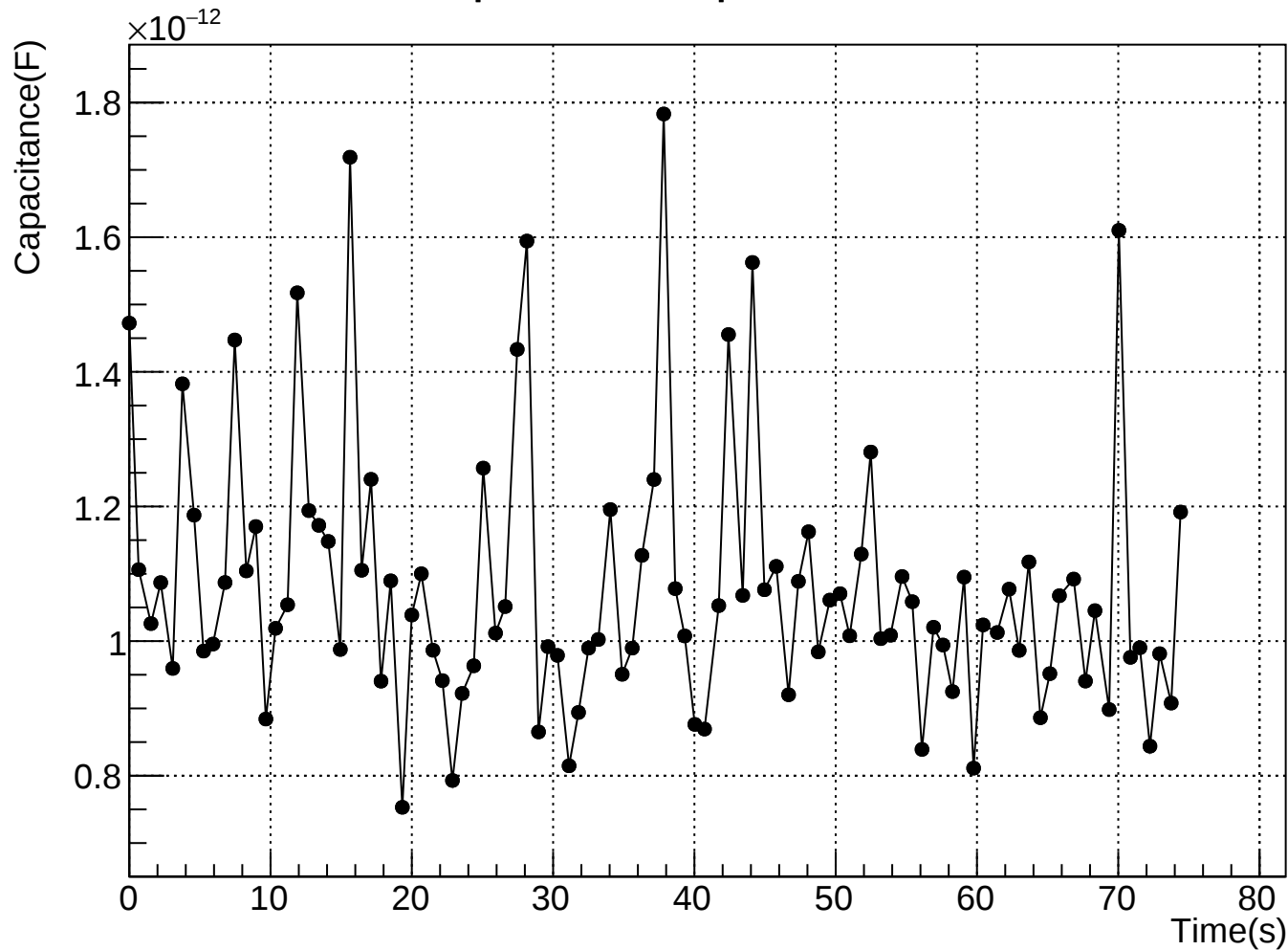
Sample101 Chip5 Channel033



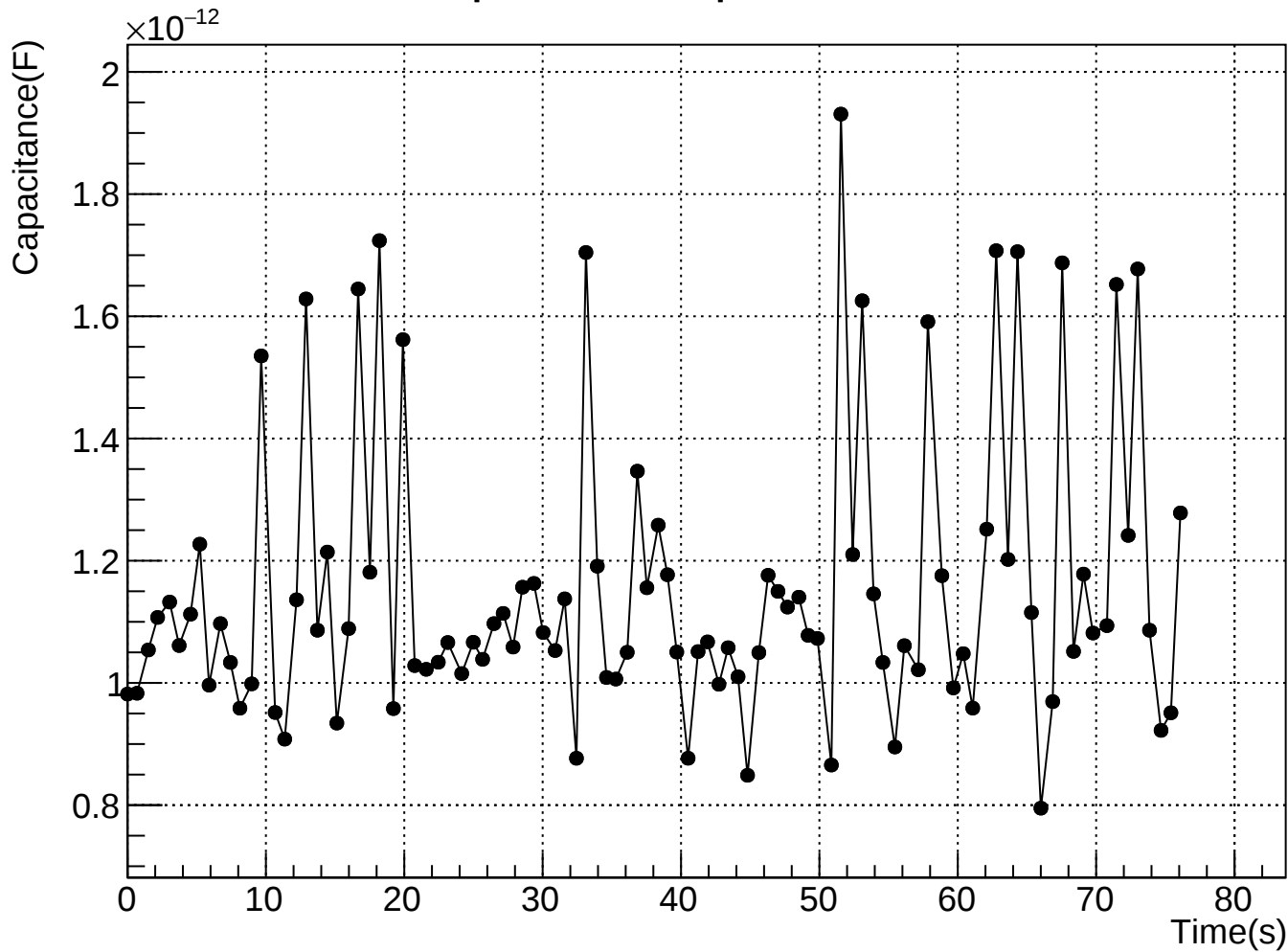
Sample101 Chip5 Channel034



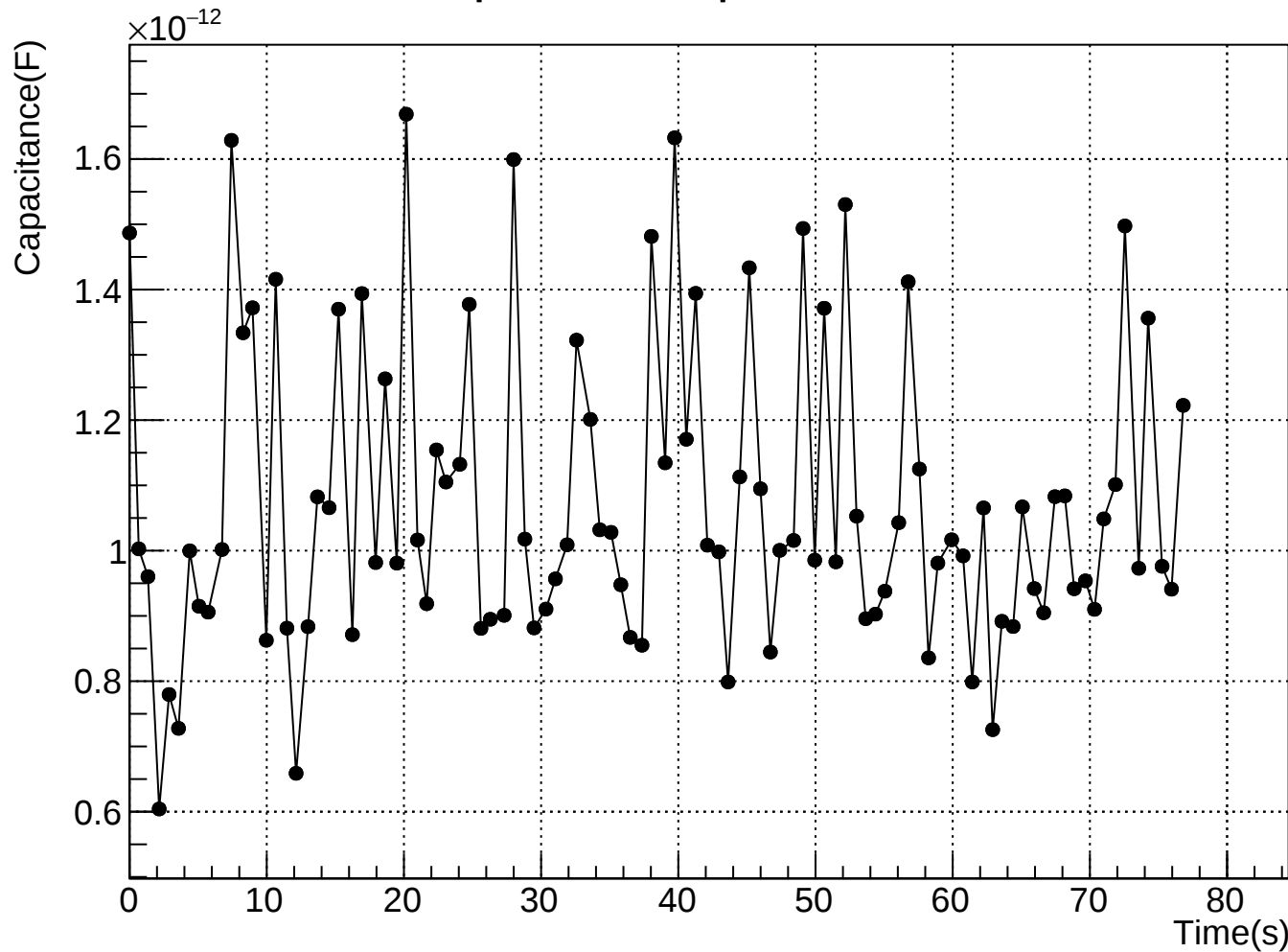
Sample101 Chip5 Channel035



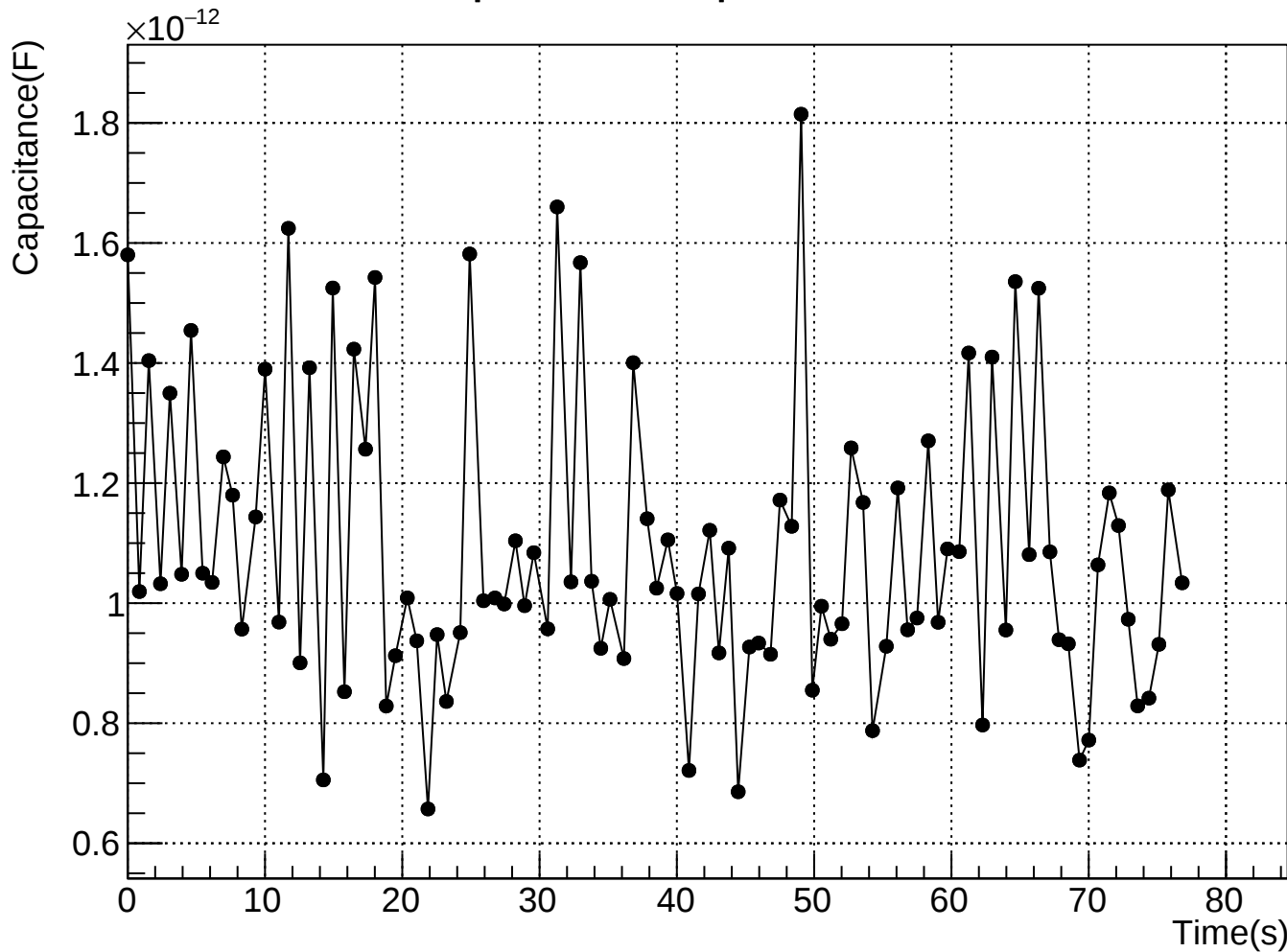
Sample101 Chip5 Channel036



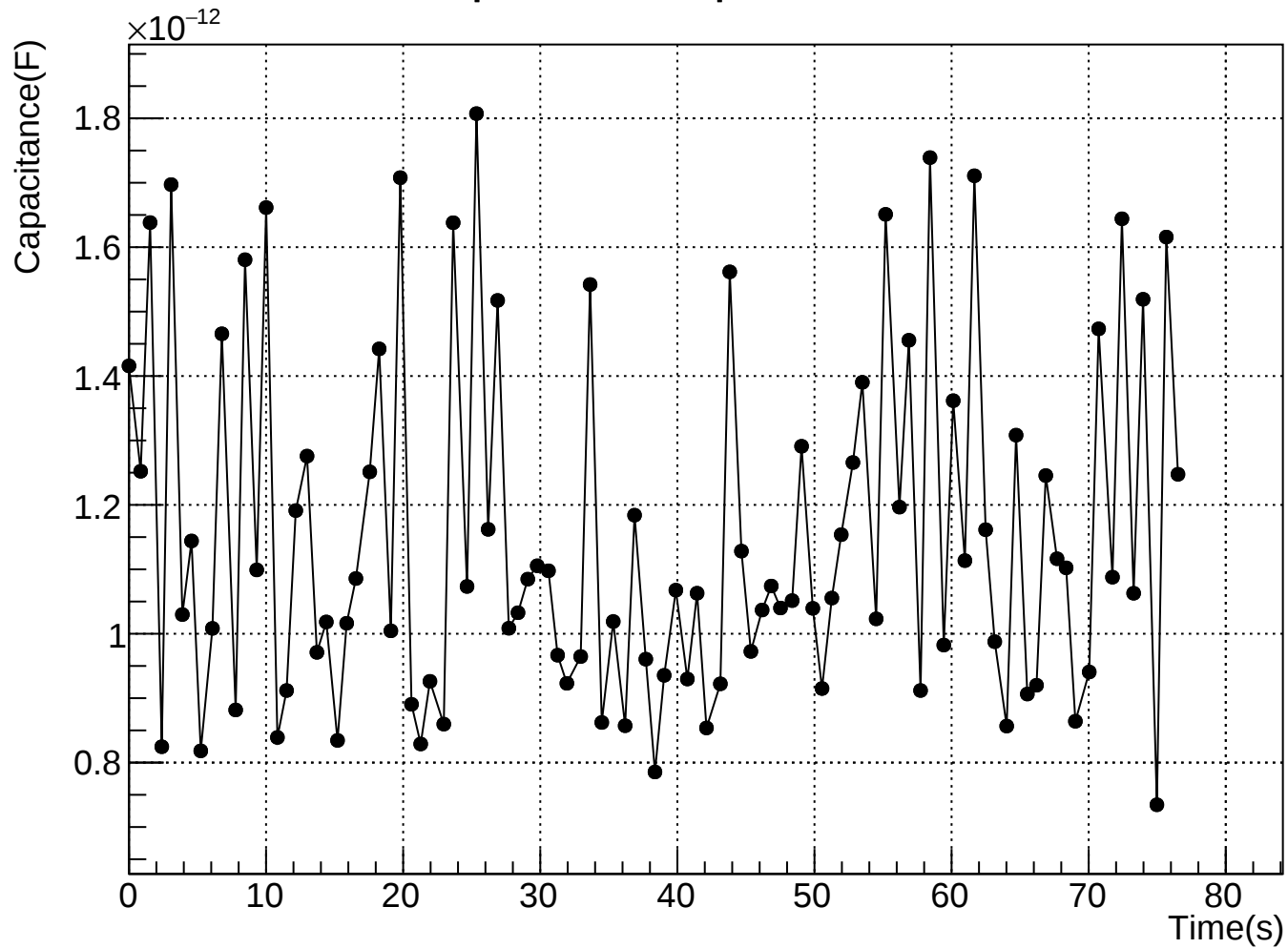
Sample101 Chip5 Channel037



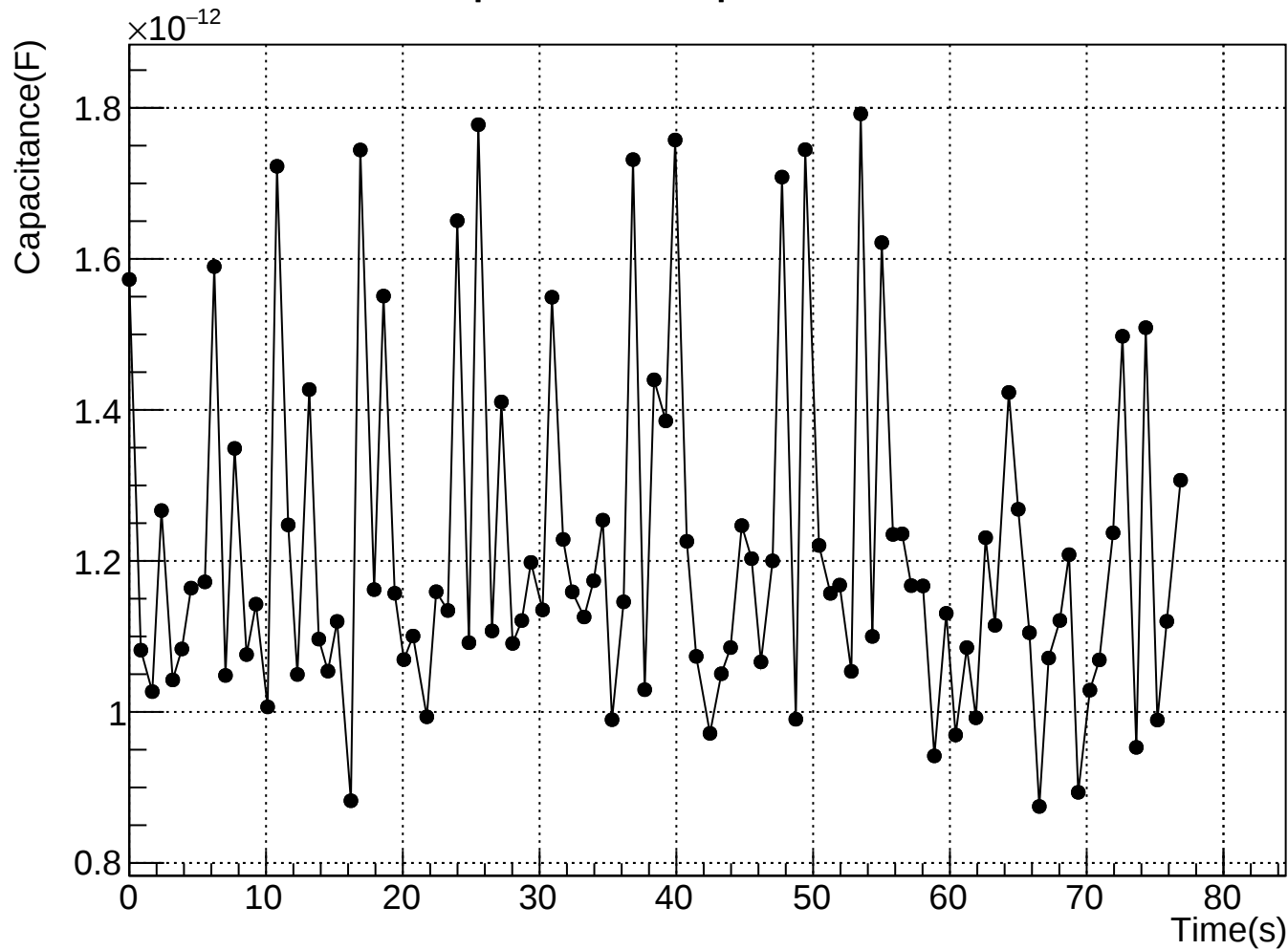
Sample101 Chip5 Channel038



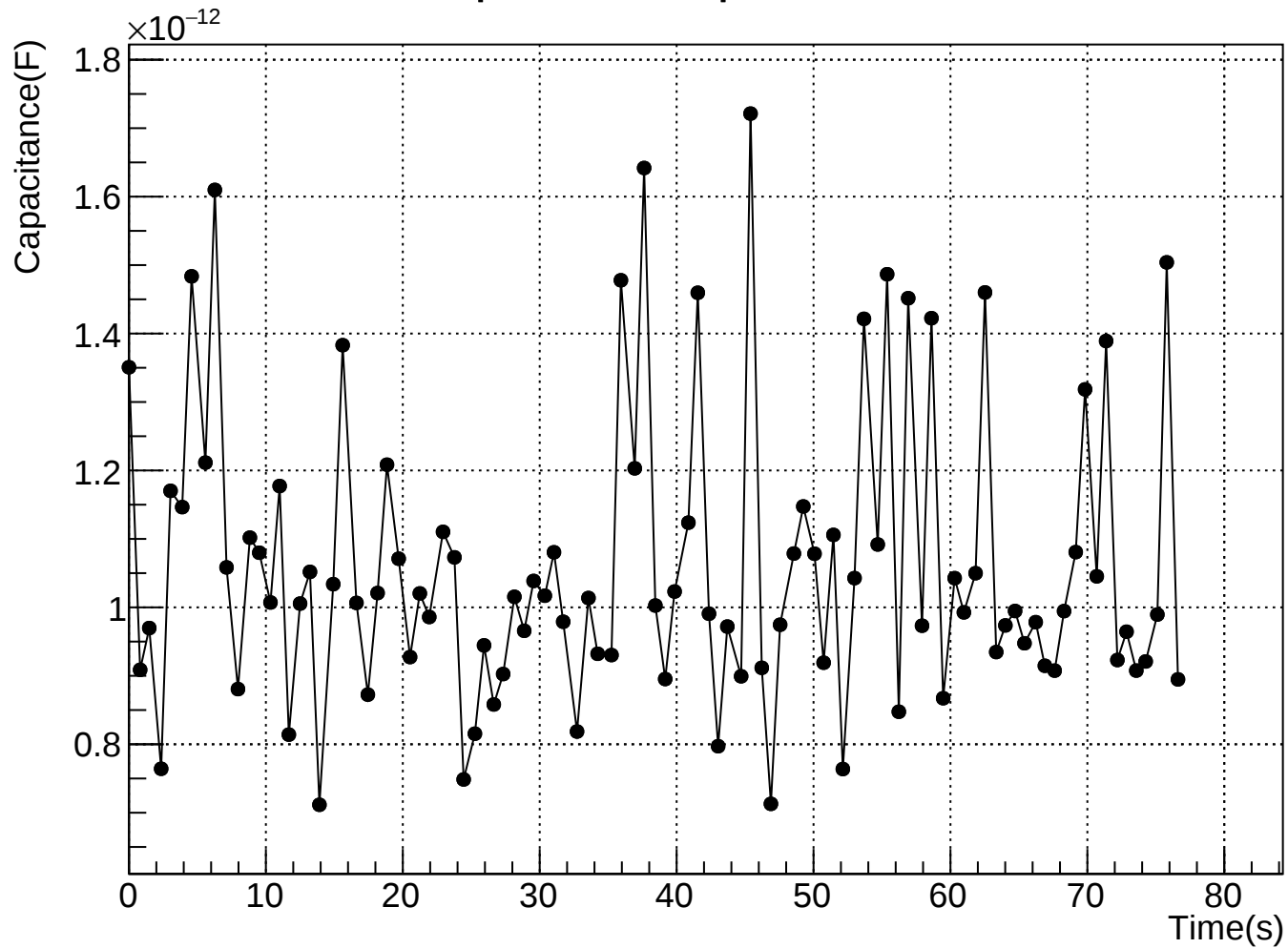
Sample101 Chip5 Channel039



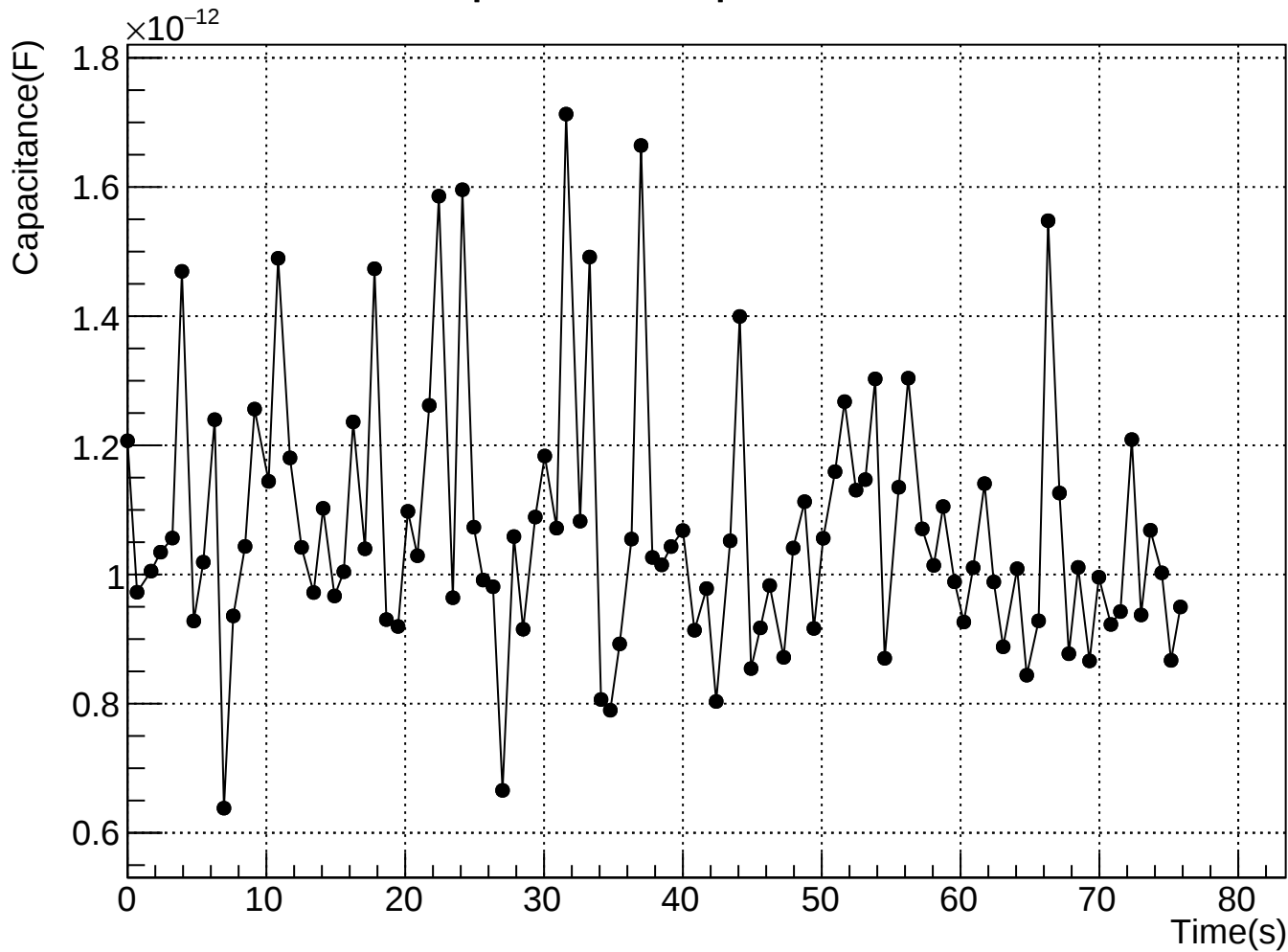
Sample101 Chip5 Channel040



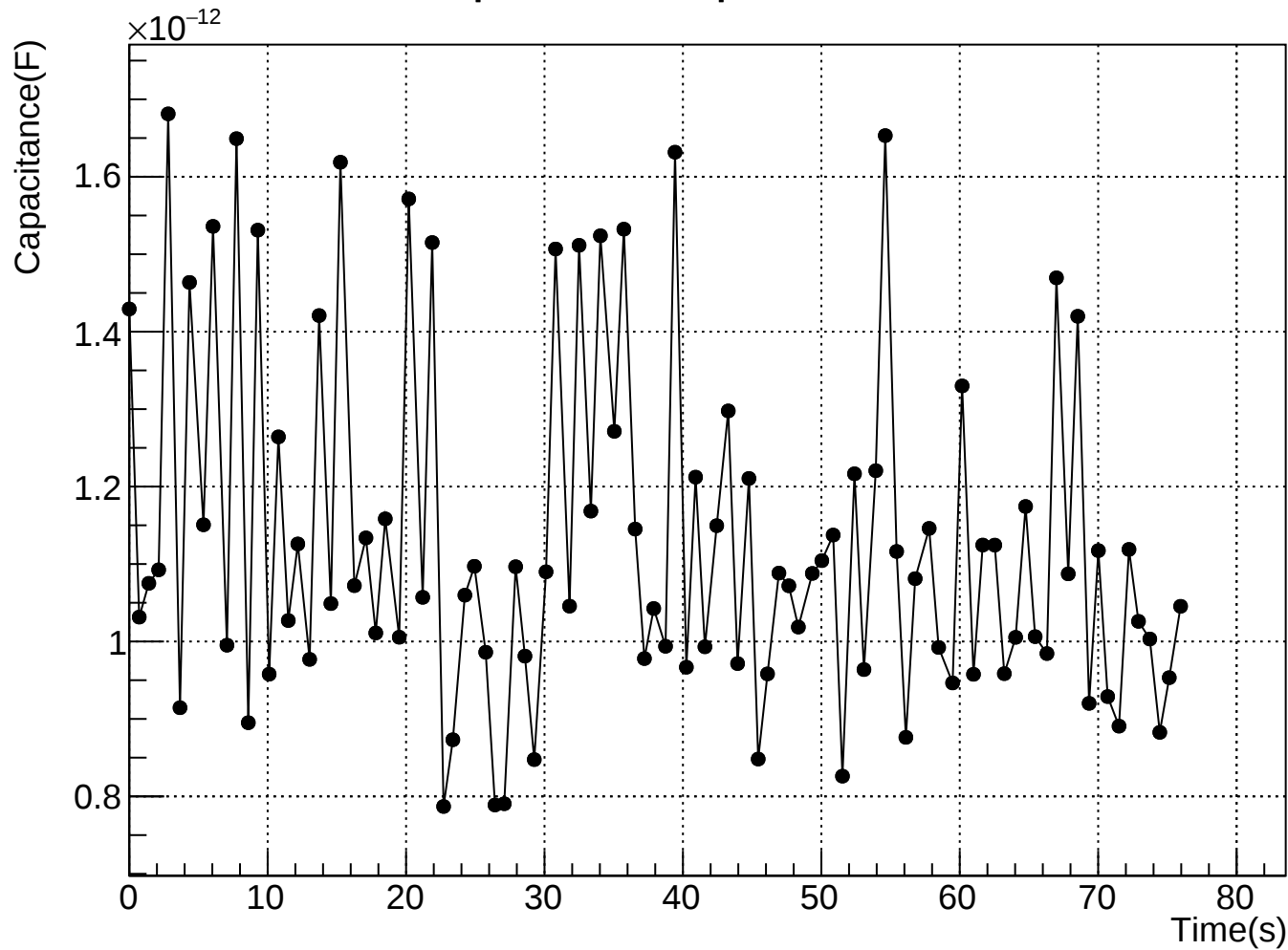
Sample101 Chip5 Channel041



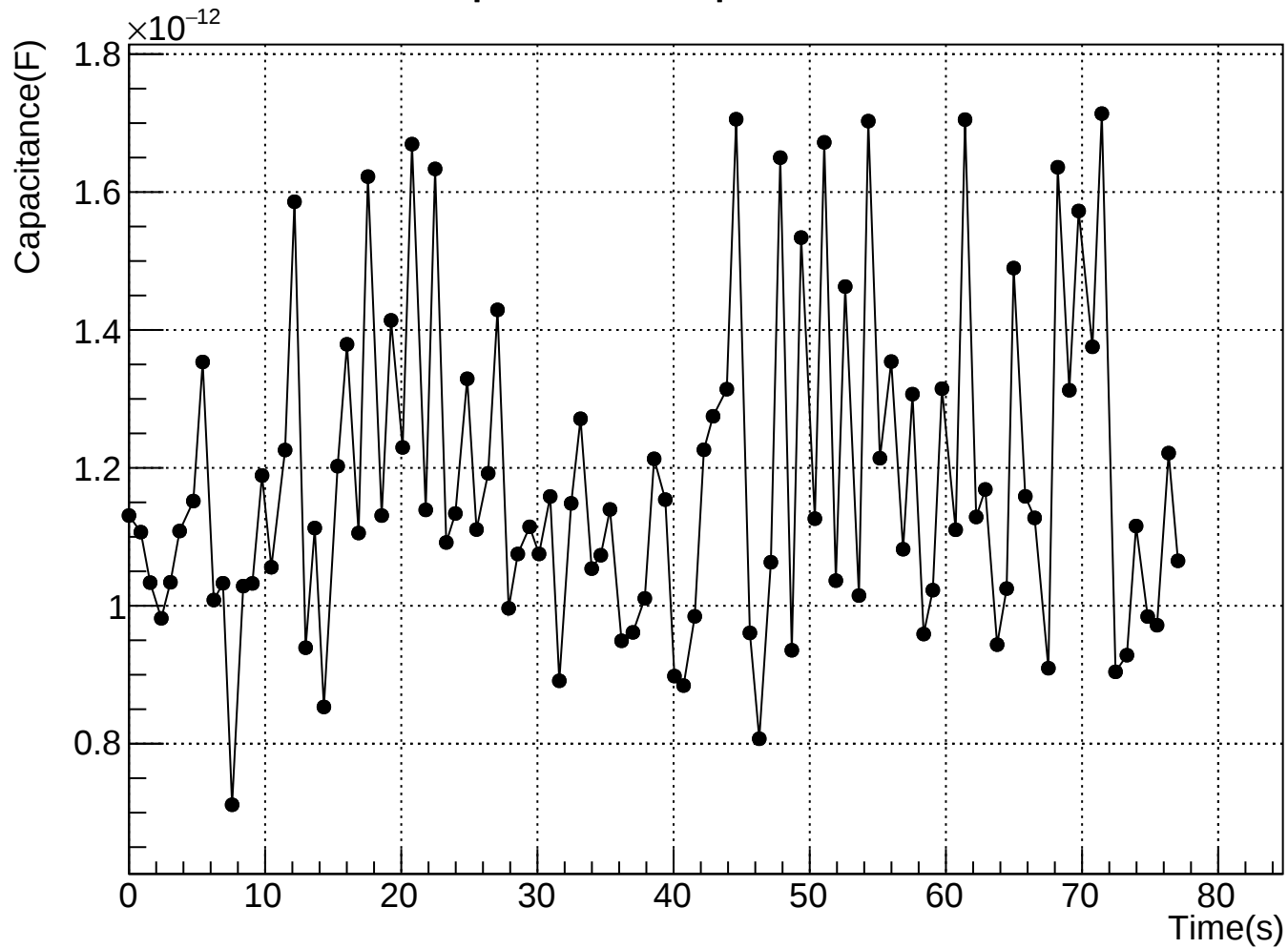
Sample101 Chip5 Channel042



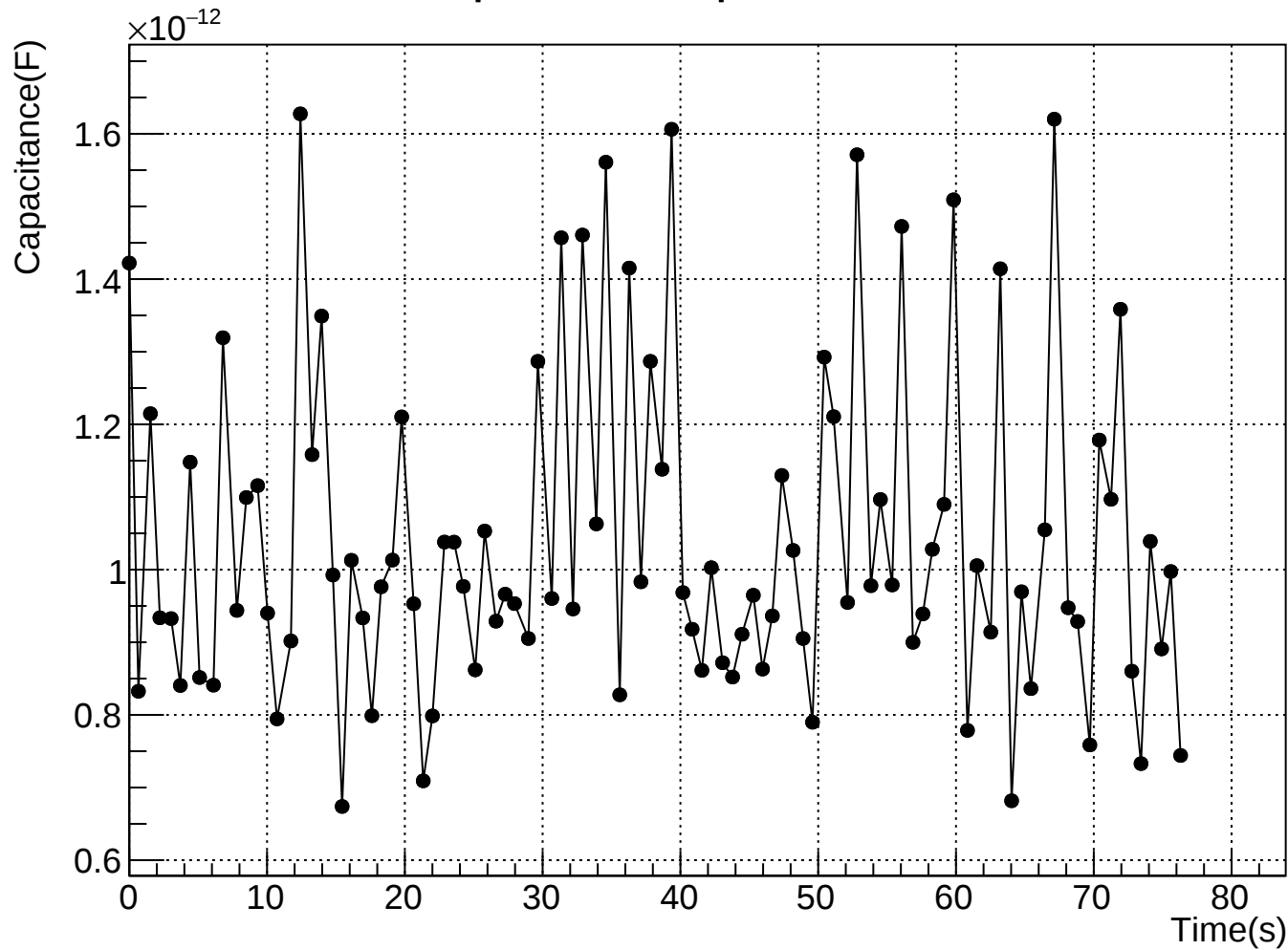
Sample101 Chip5 Channel043



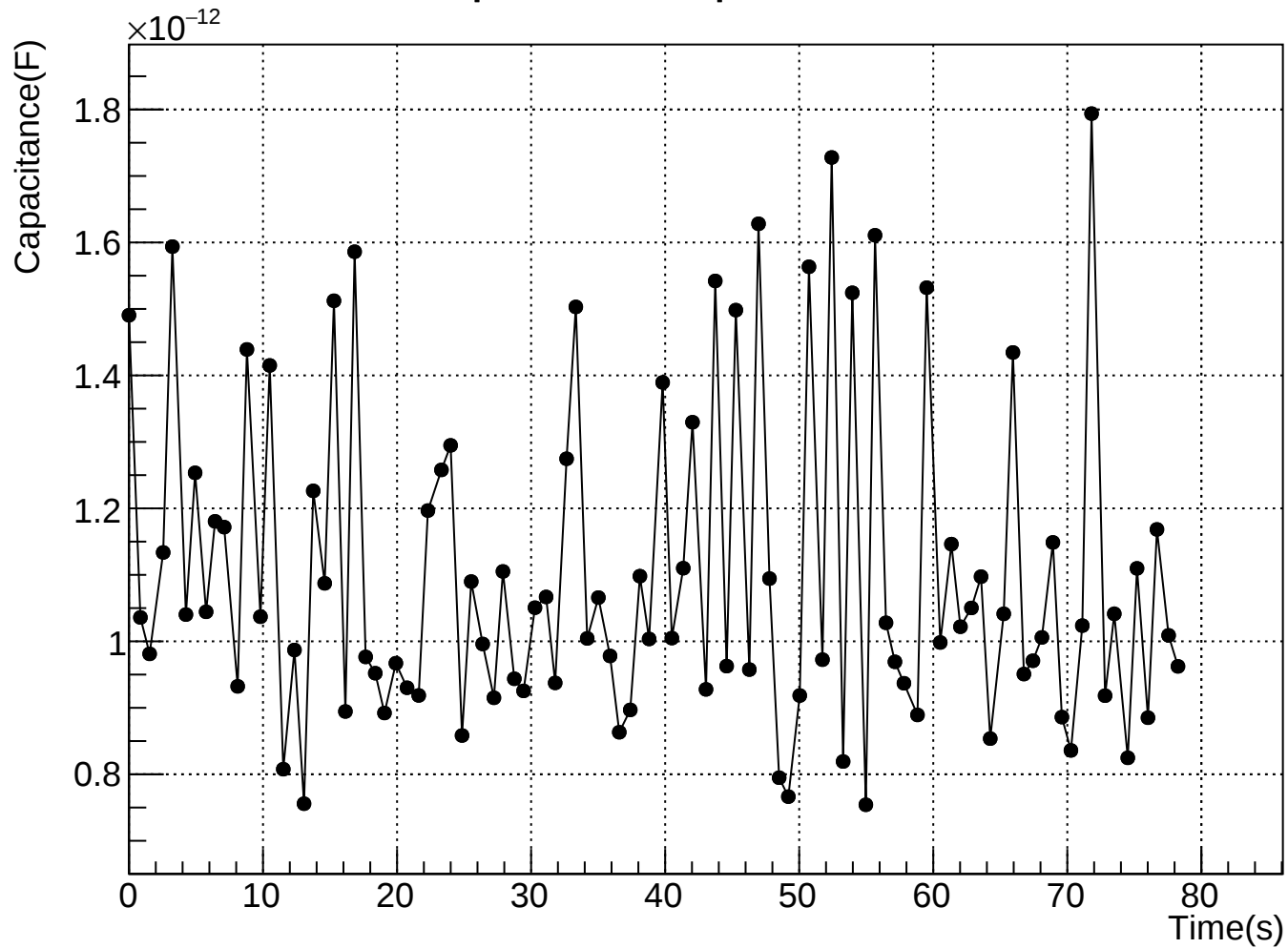
Sample101 Chip5 Channel044



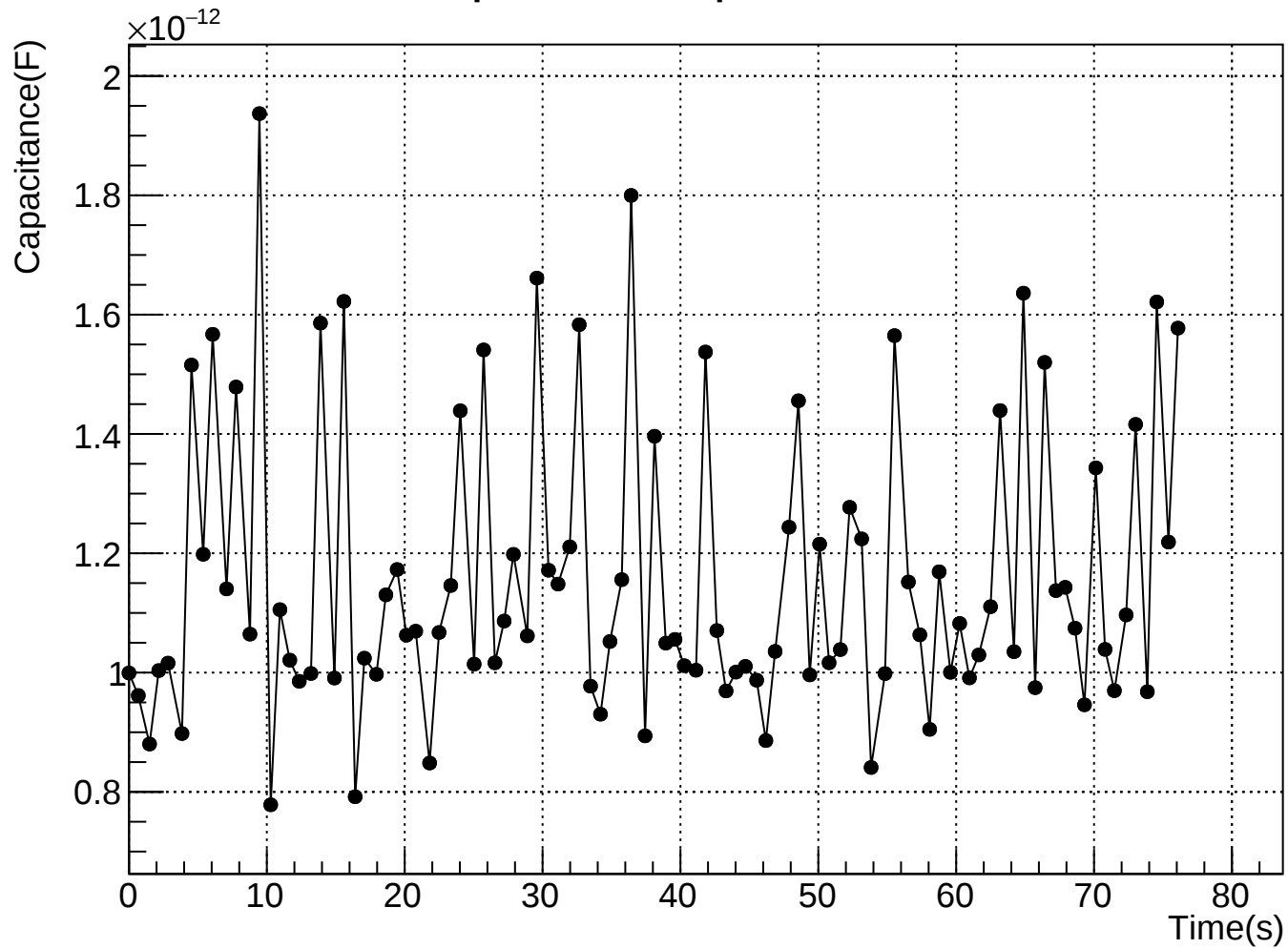
Sample101 Chip5 Channel045



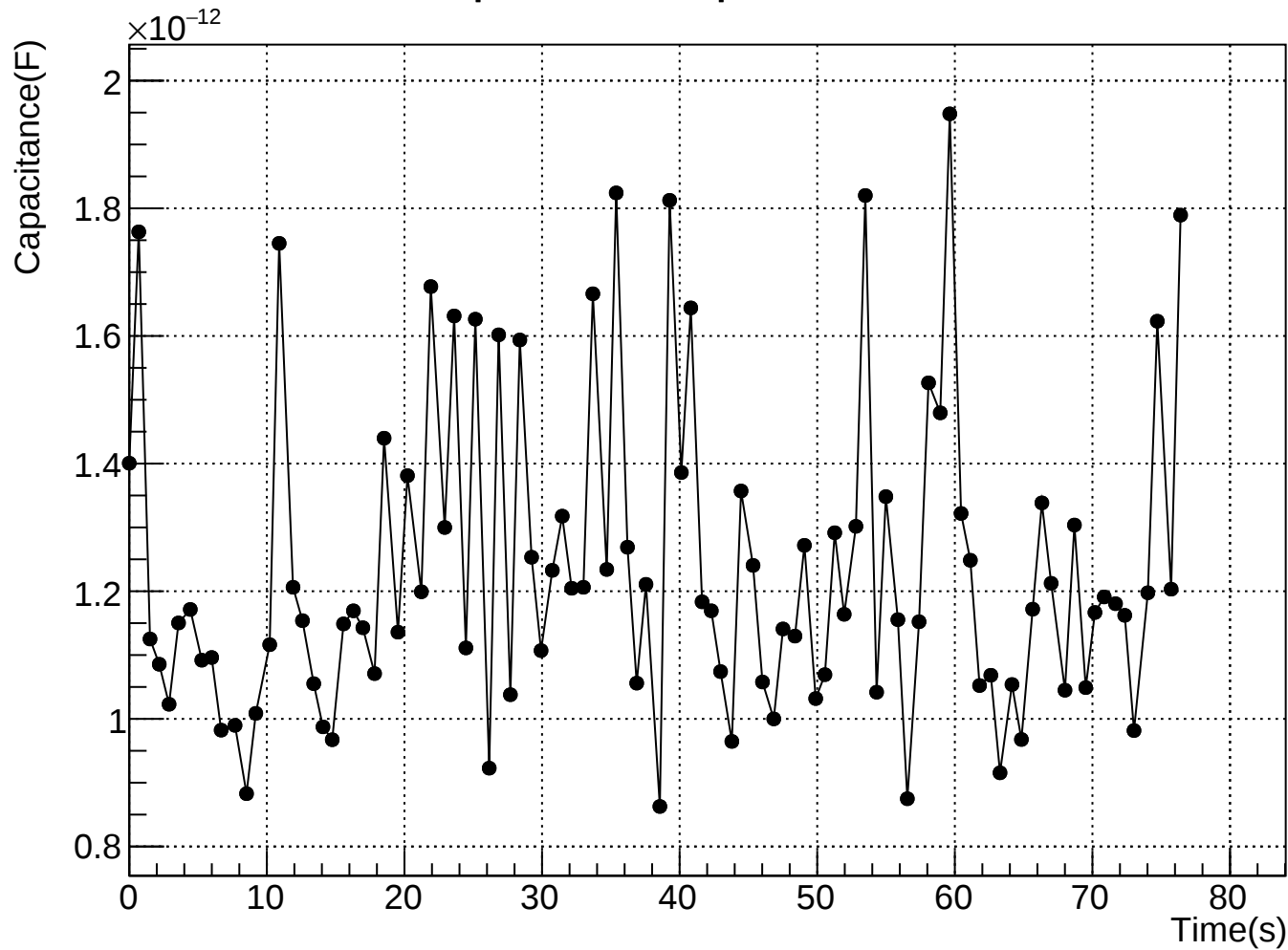
Sample101 Chip5 Channel046



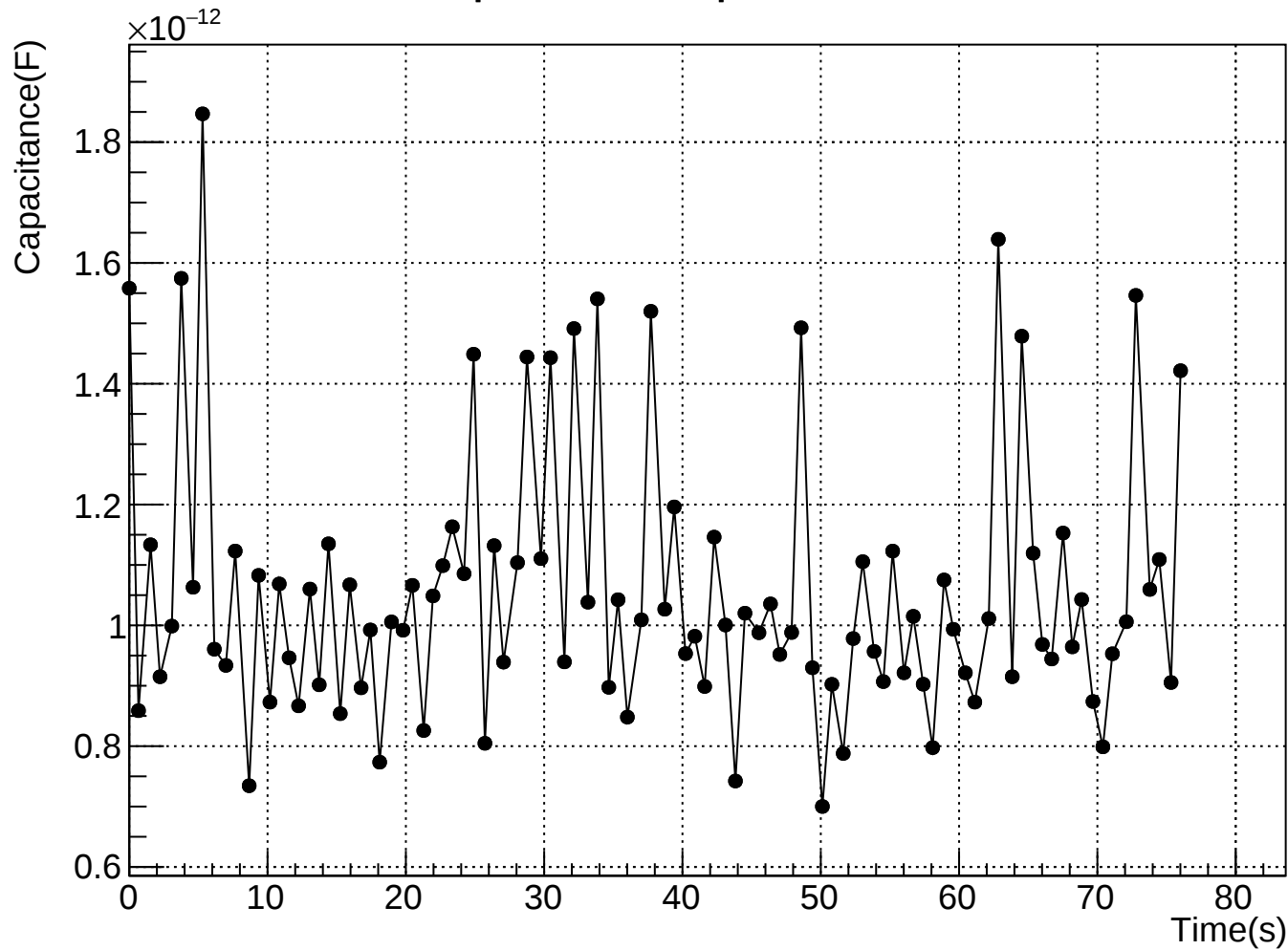
Sample101 Chip5 Channel047



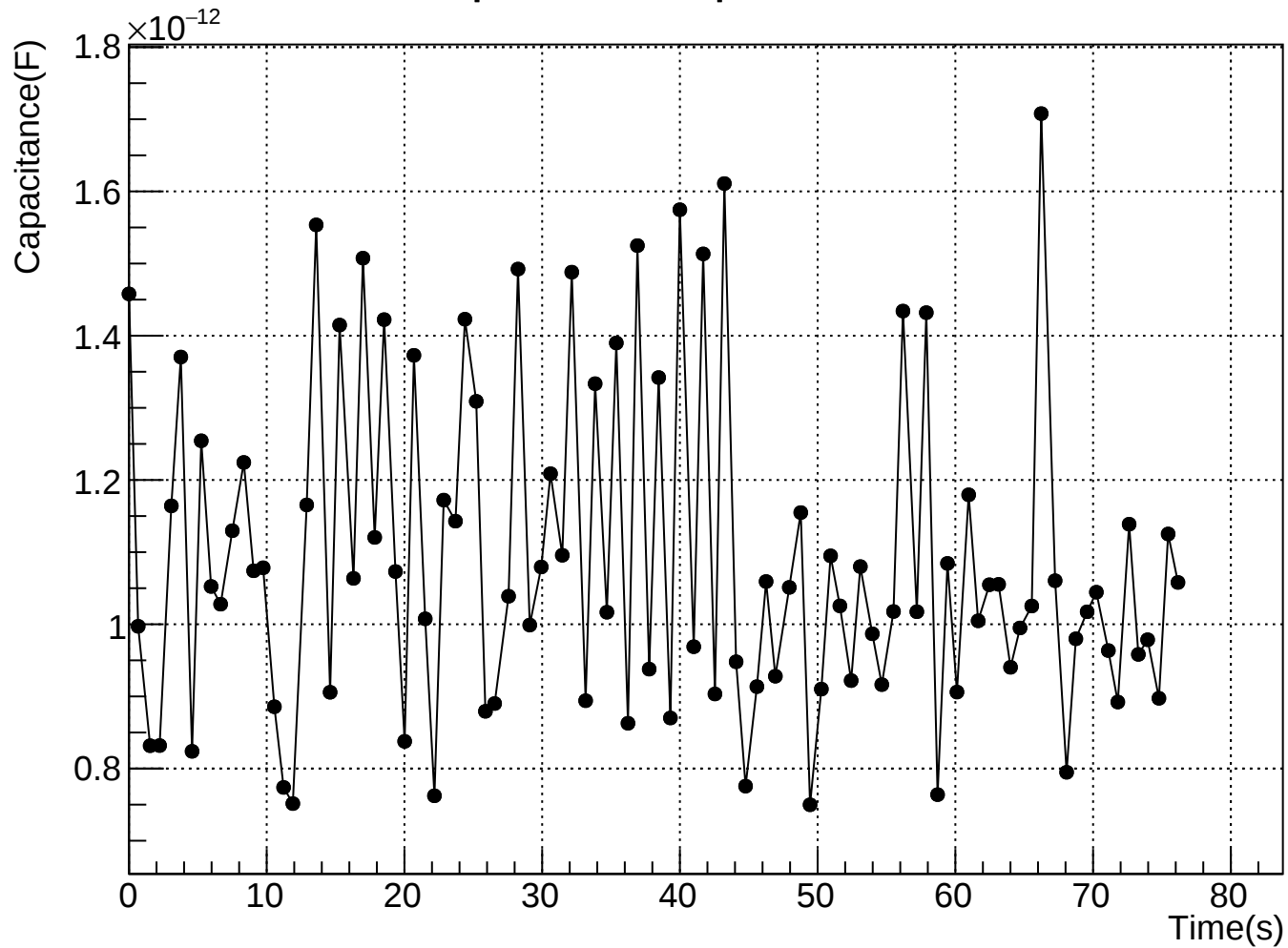
Sample101 Chip5 Channel048



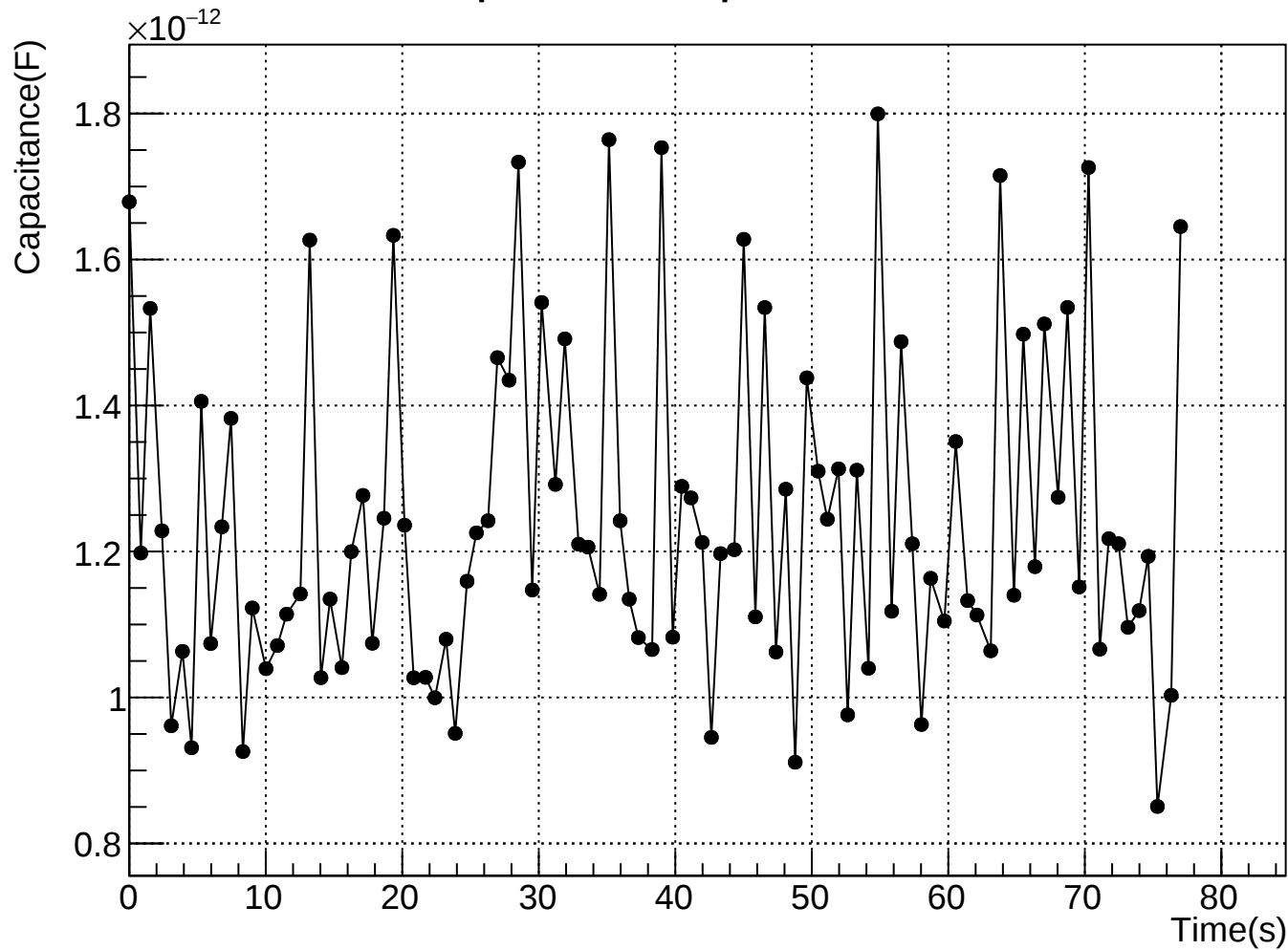
Sample101 Chip5 Channel049



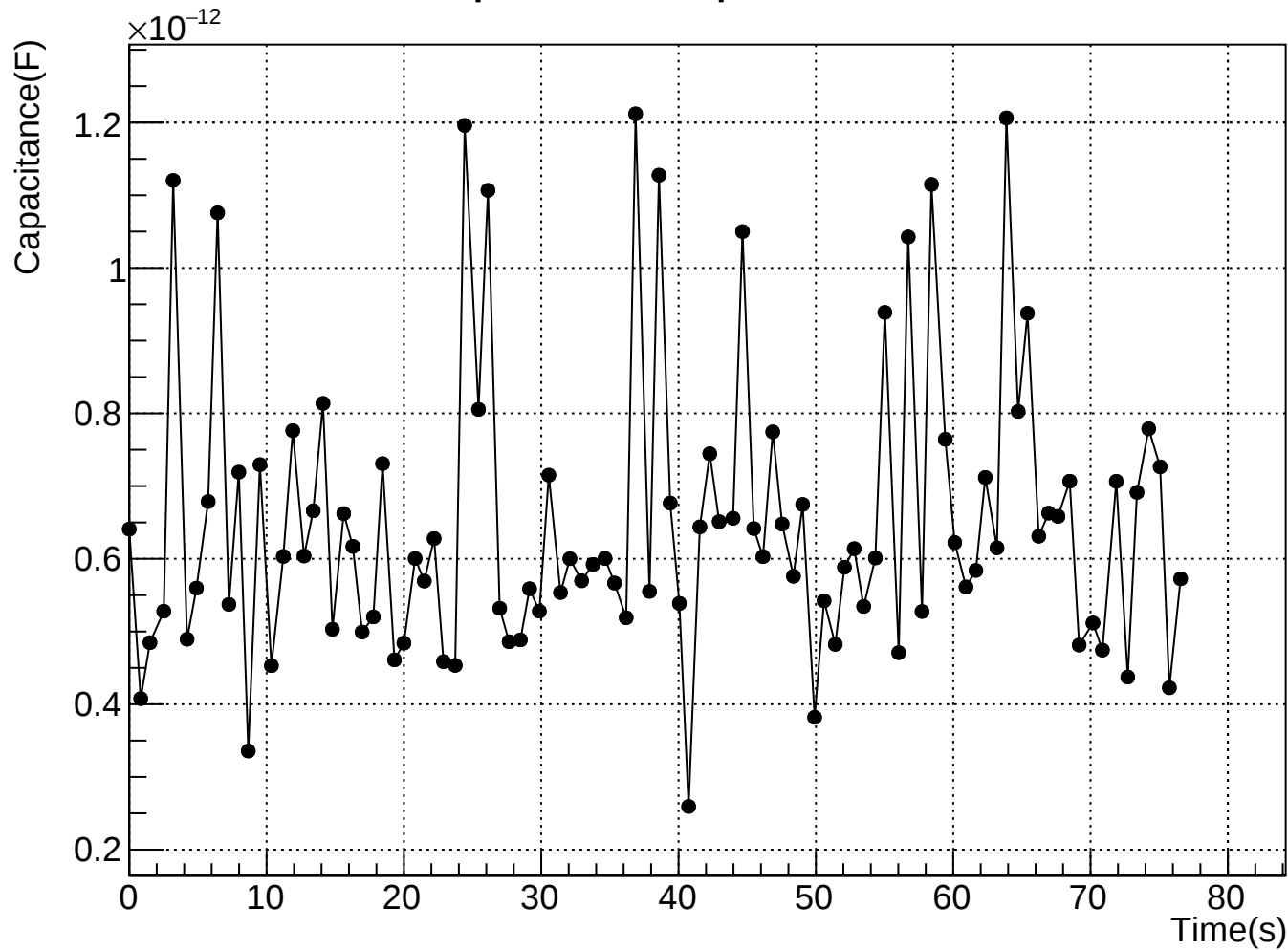
Sample101 Chip5 Channel050



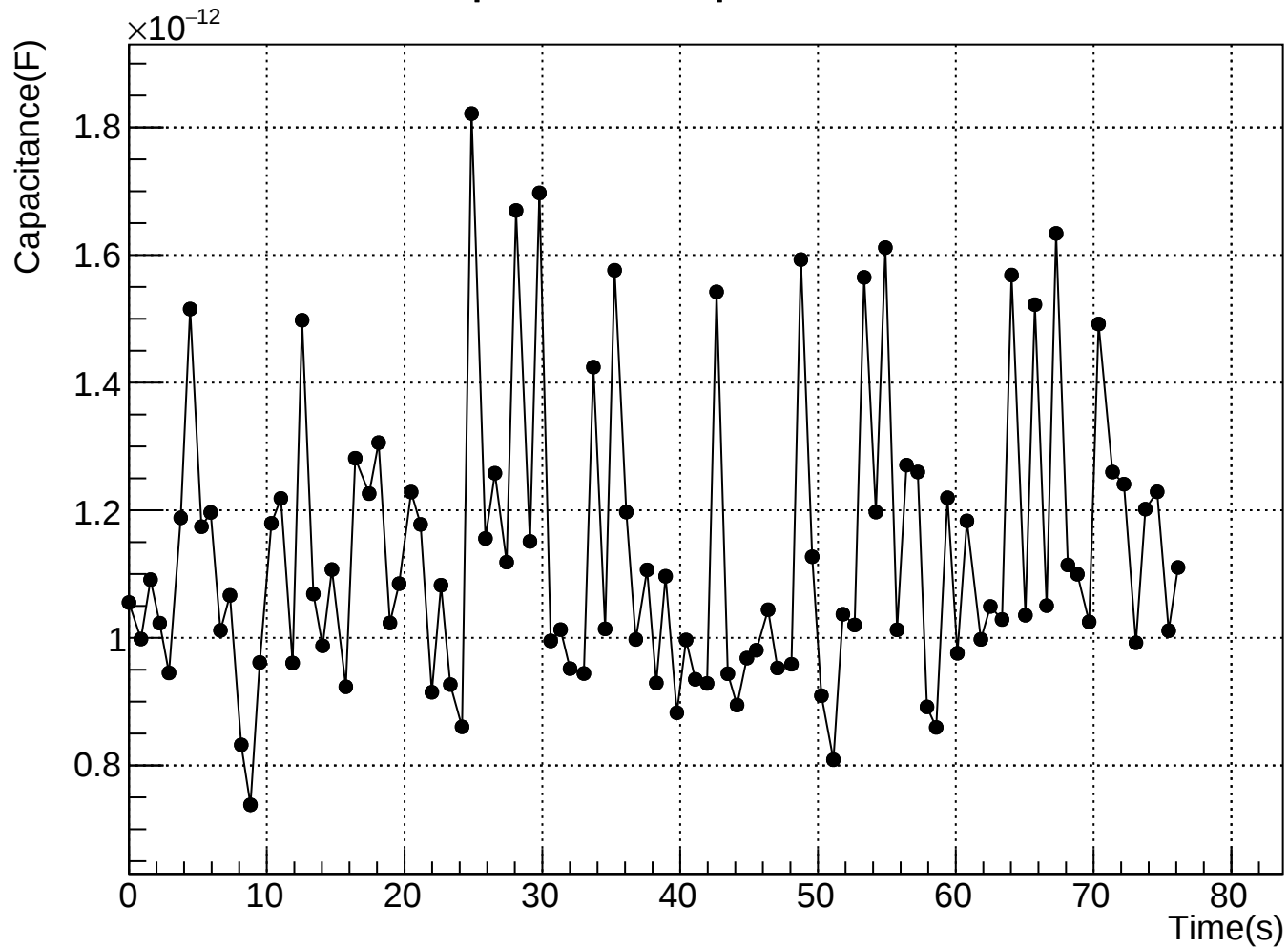
Sample101 Chip5 Channel051



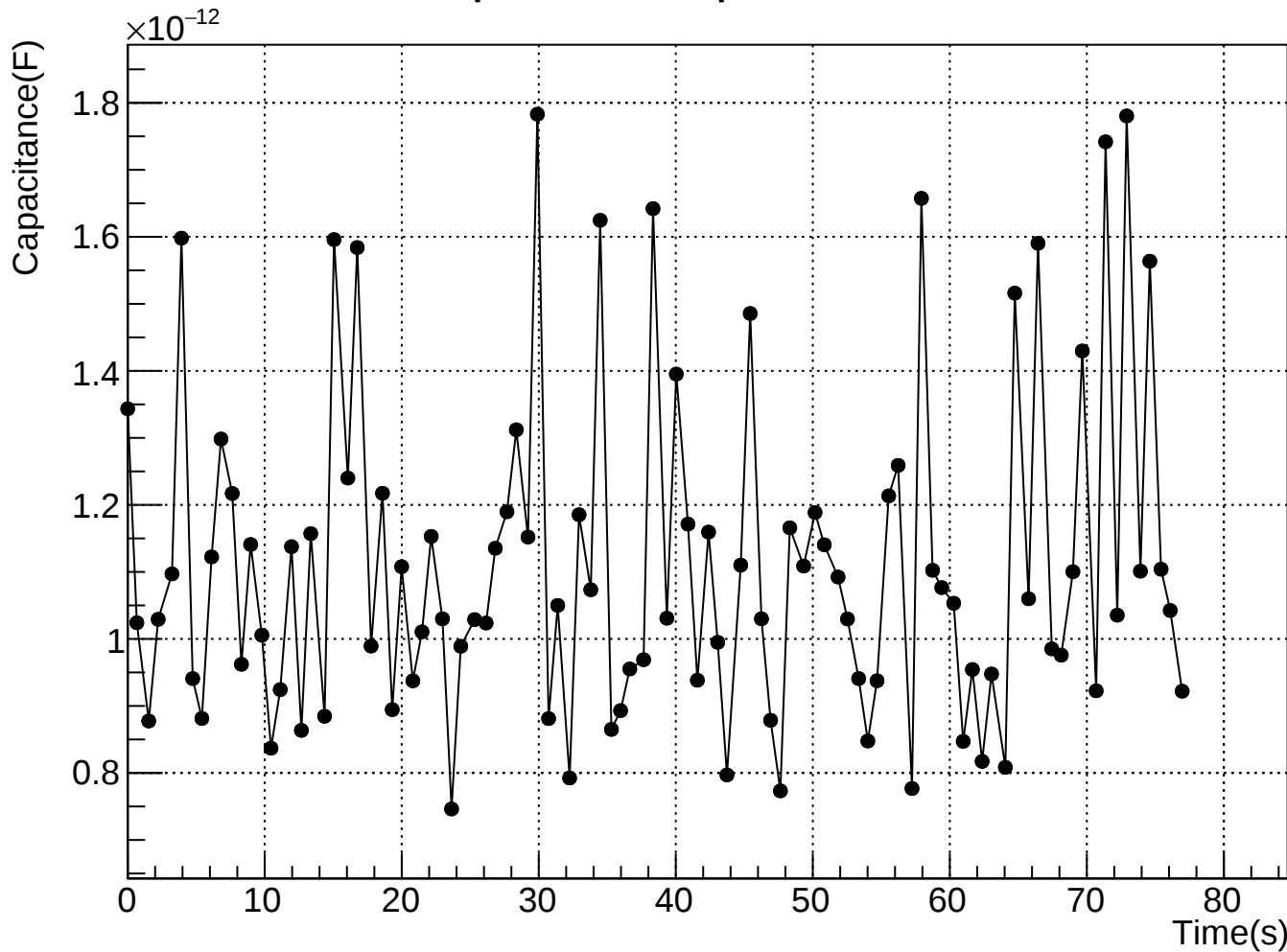
Sample101 Chip5 Channel052



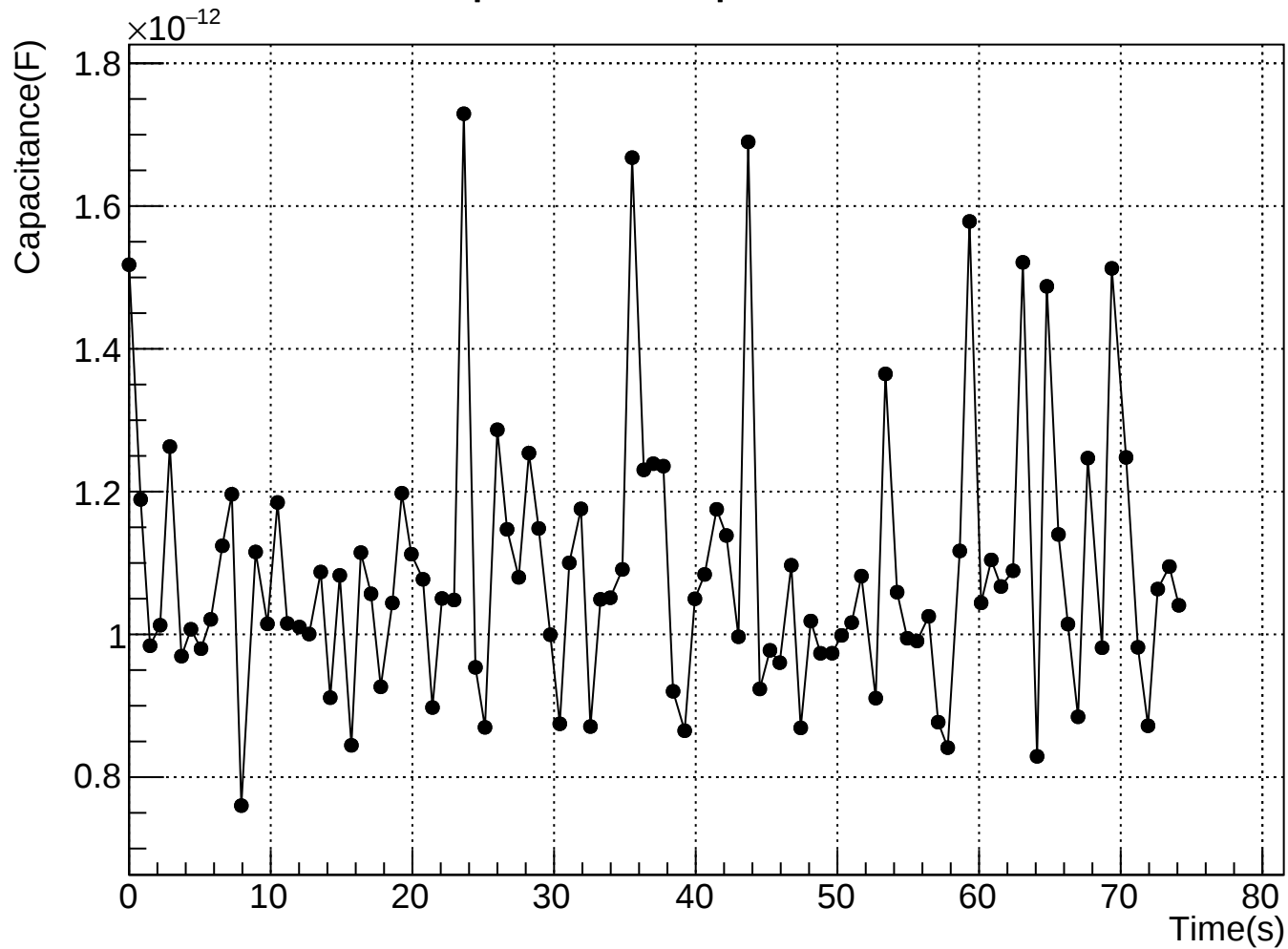
Sample101 Chip5 Channel053



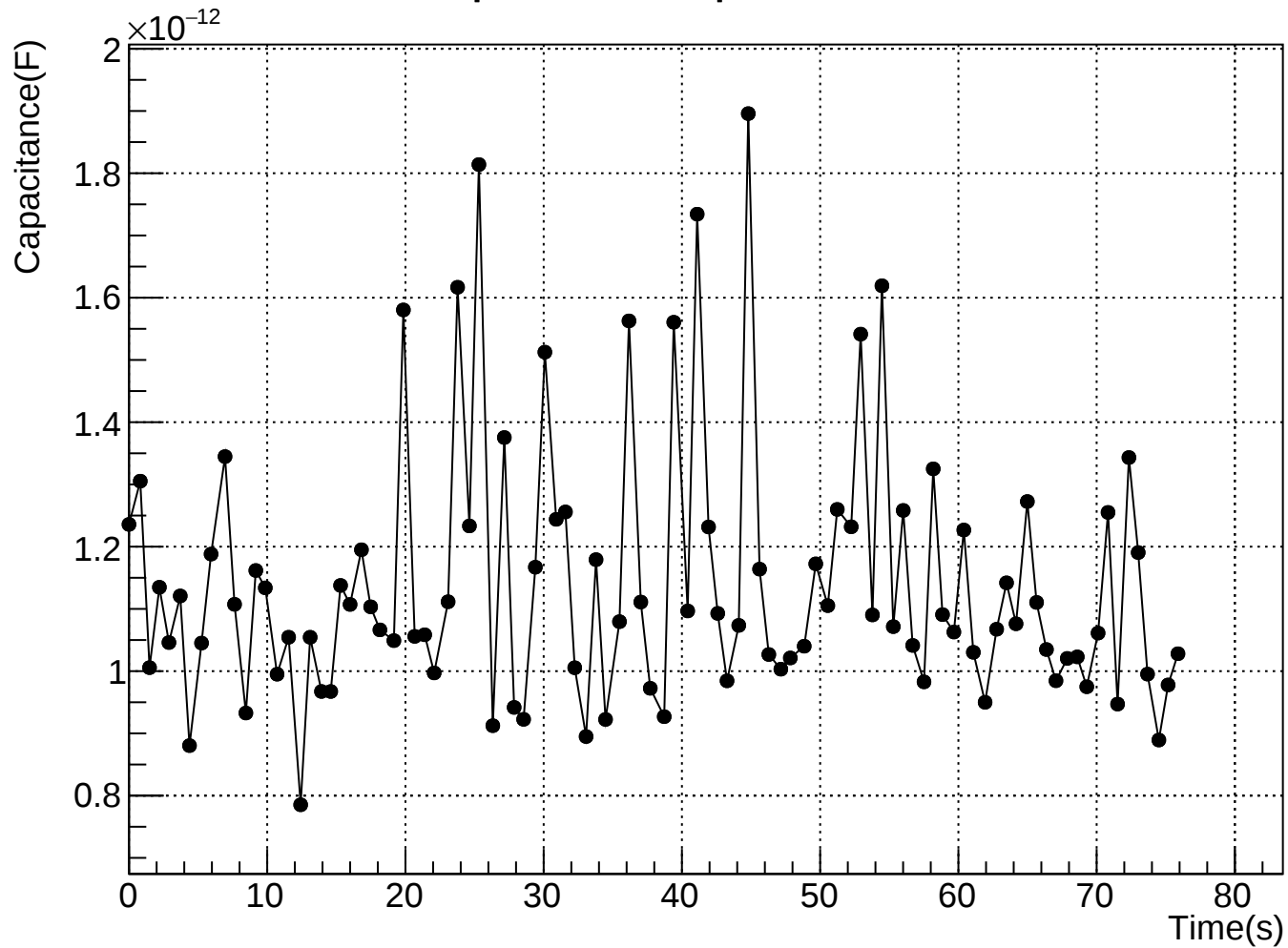
Sample101 Chip5 Channel054



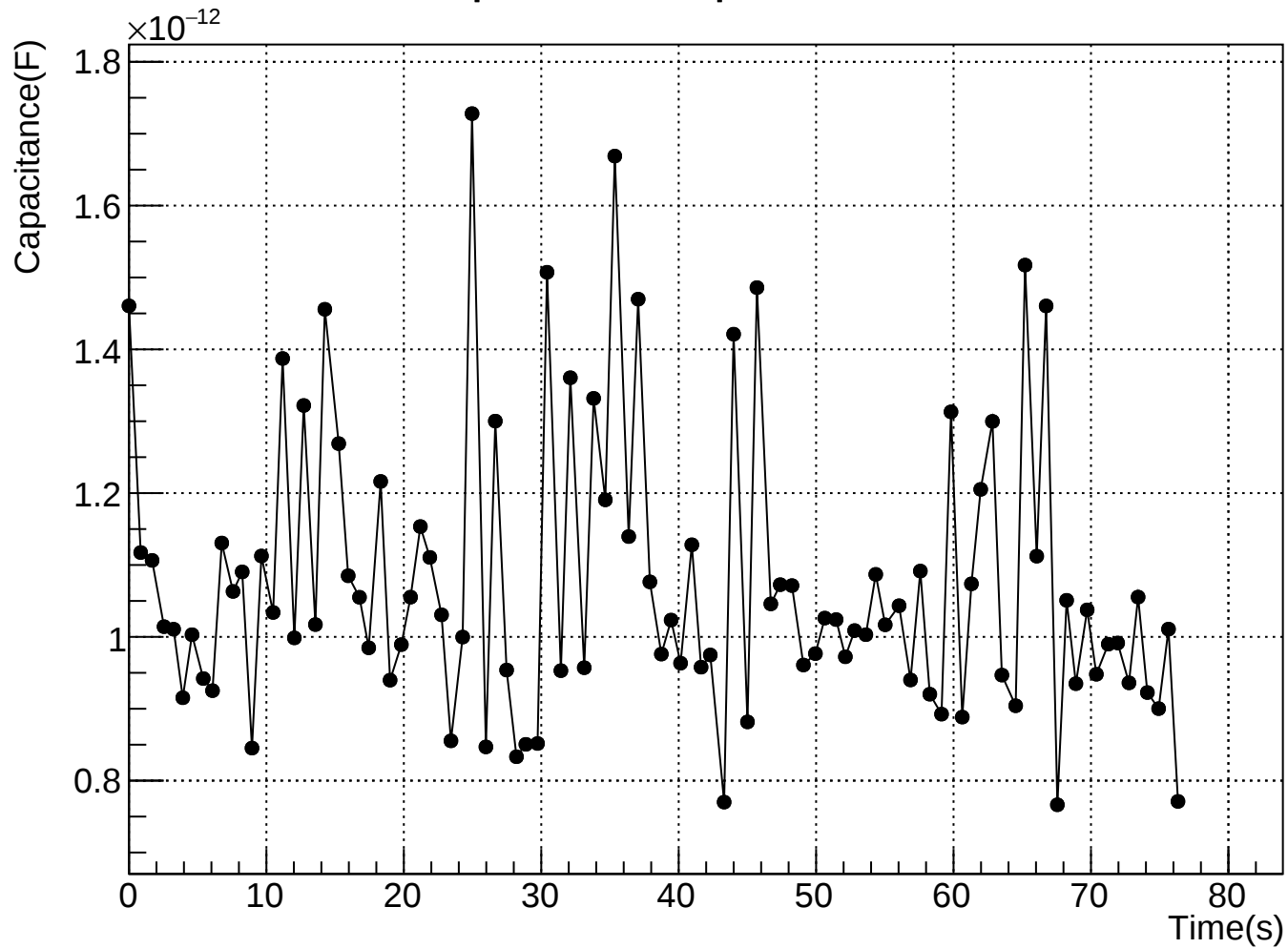
Sample101 Chip5 Channel055



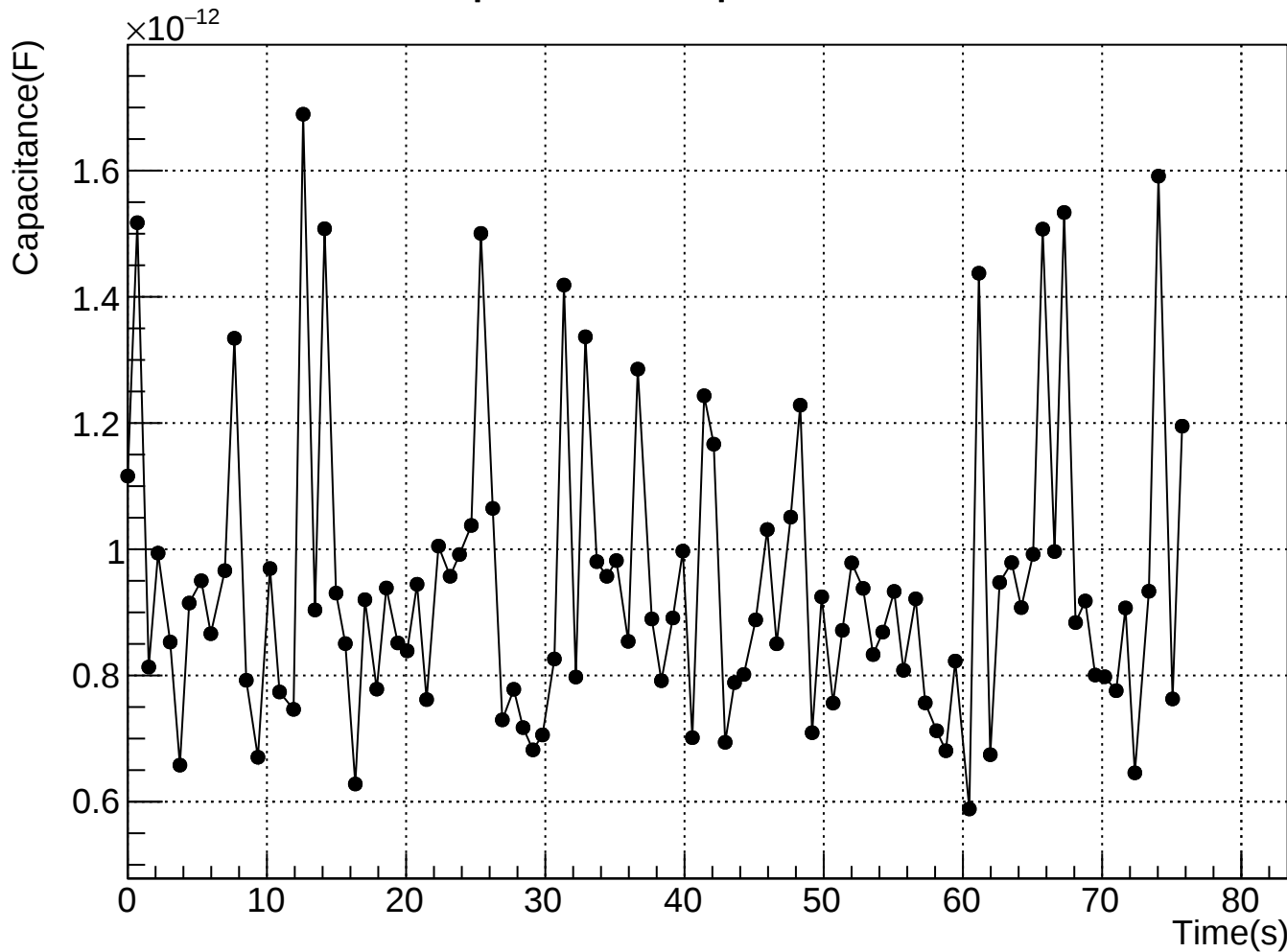
Sample101 Chip5 Channel056



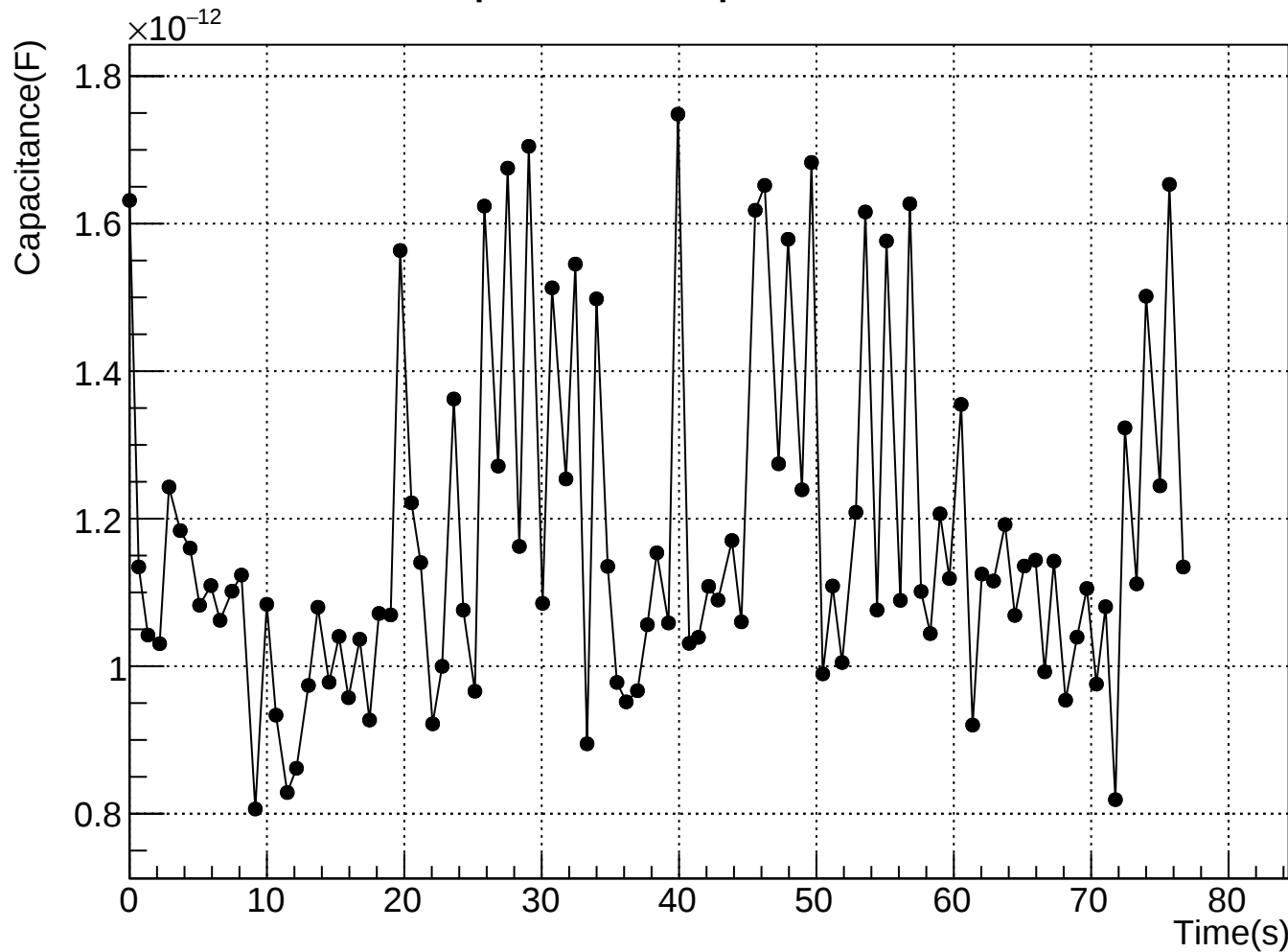
Sample101 Chip5 Channel057



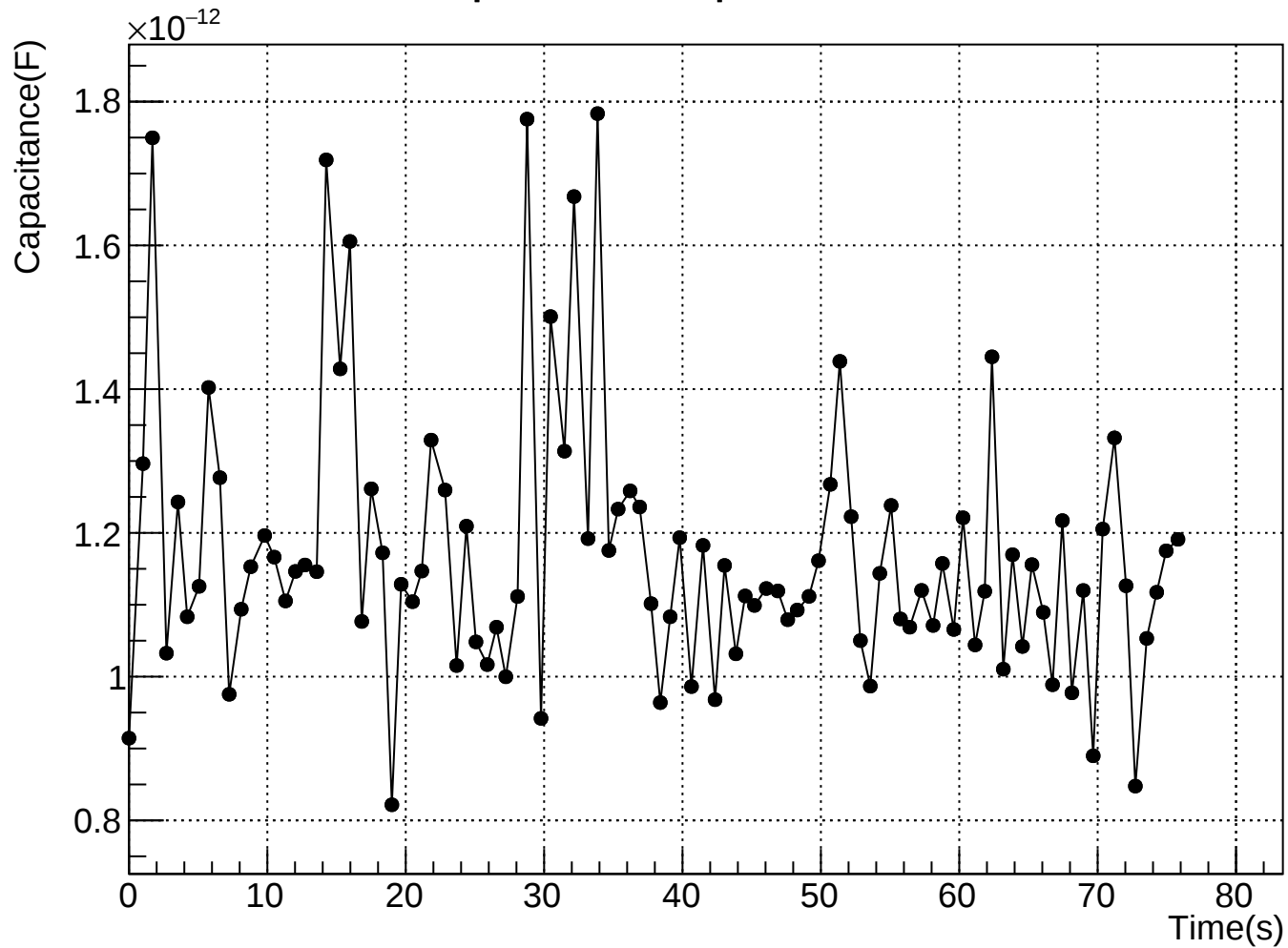
Sample101 Chip5 Channel058



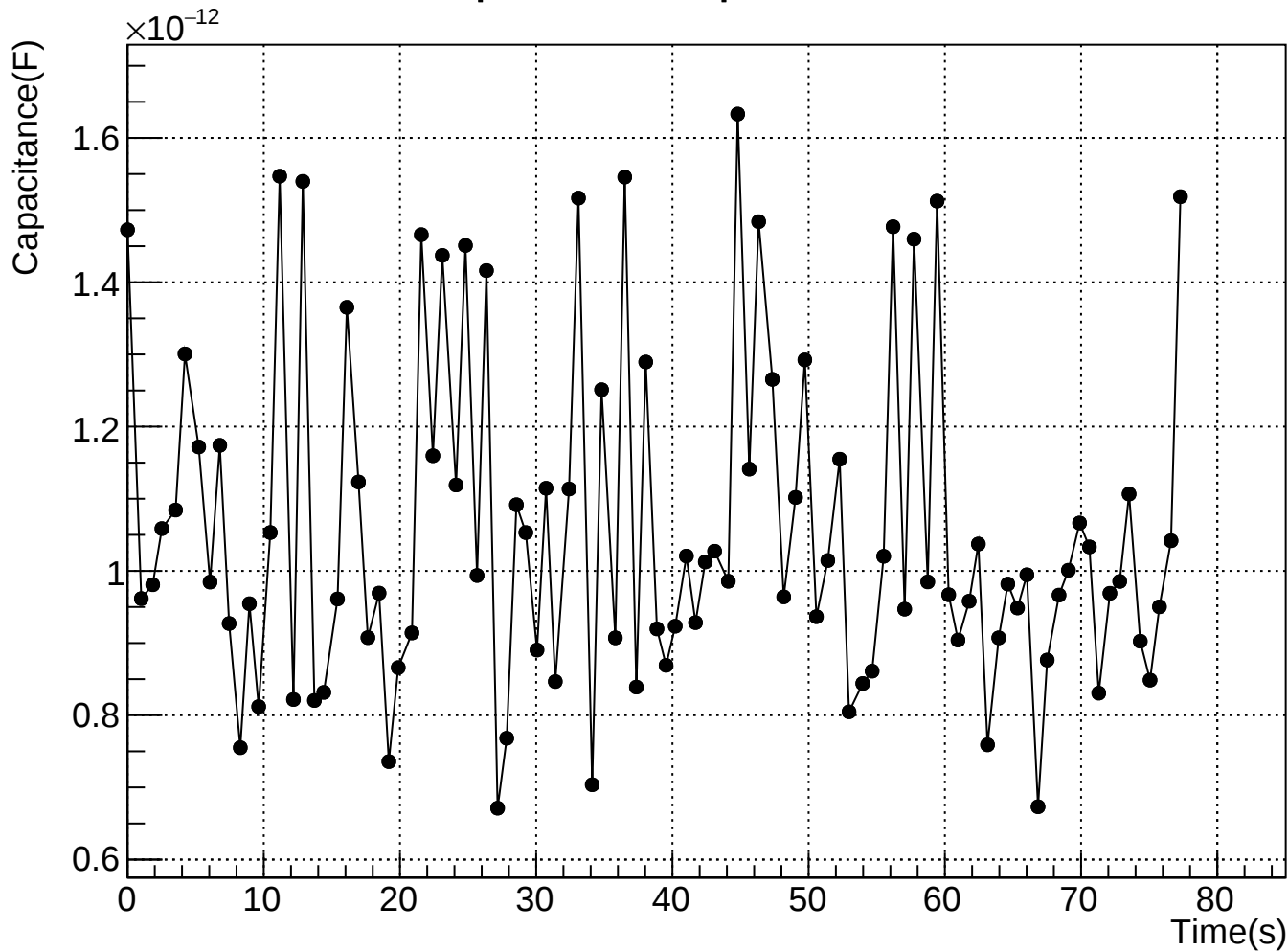
Sample101 Chip5 Channel059



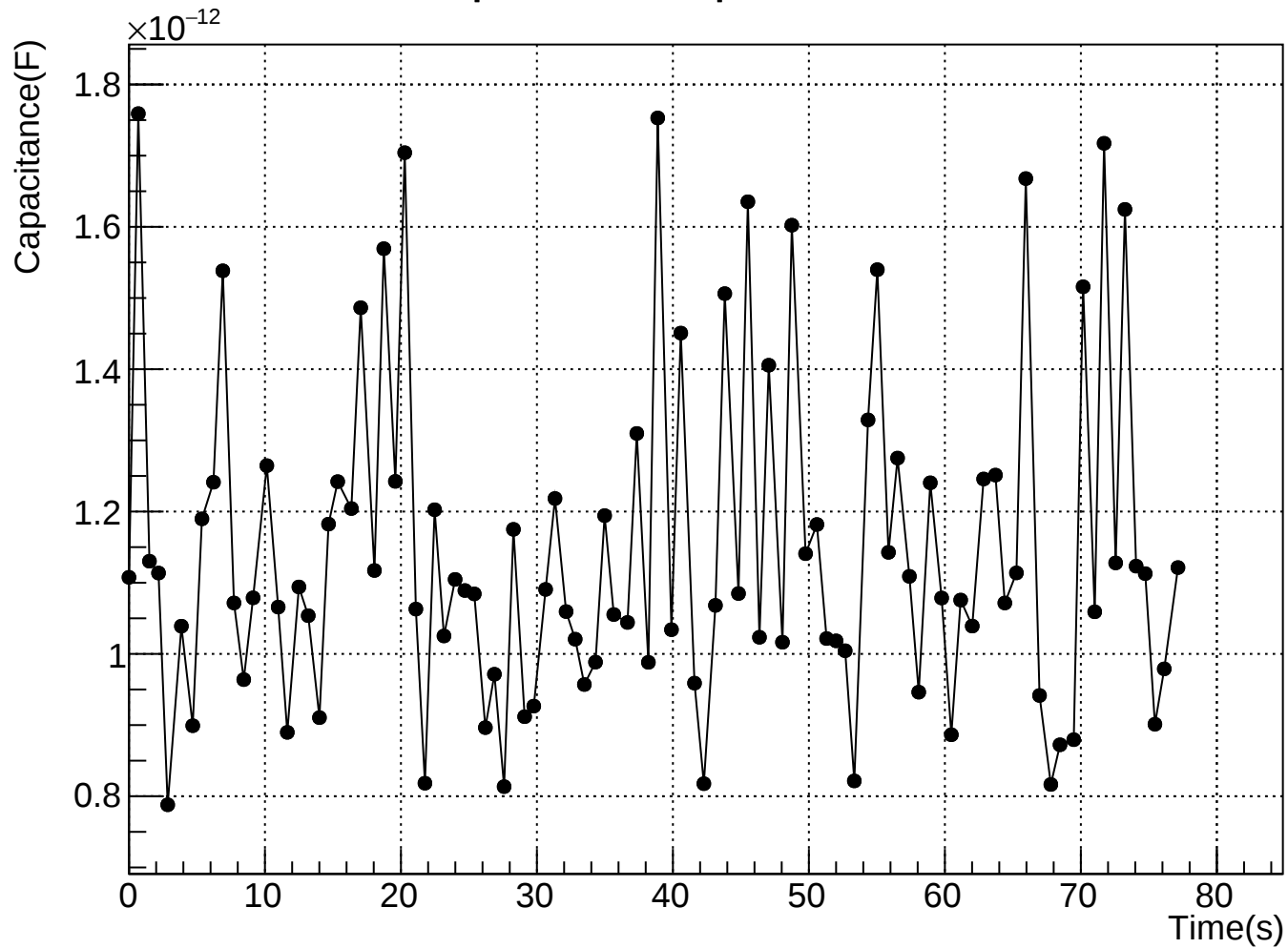
Sample101 Chip5 Channel060



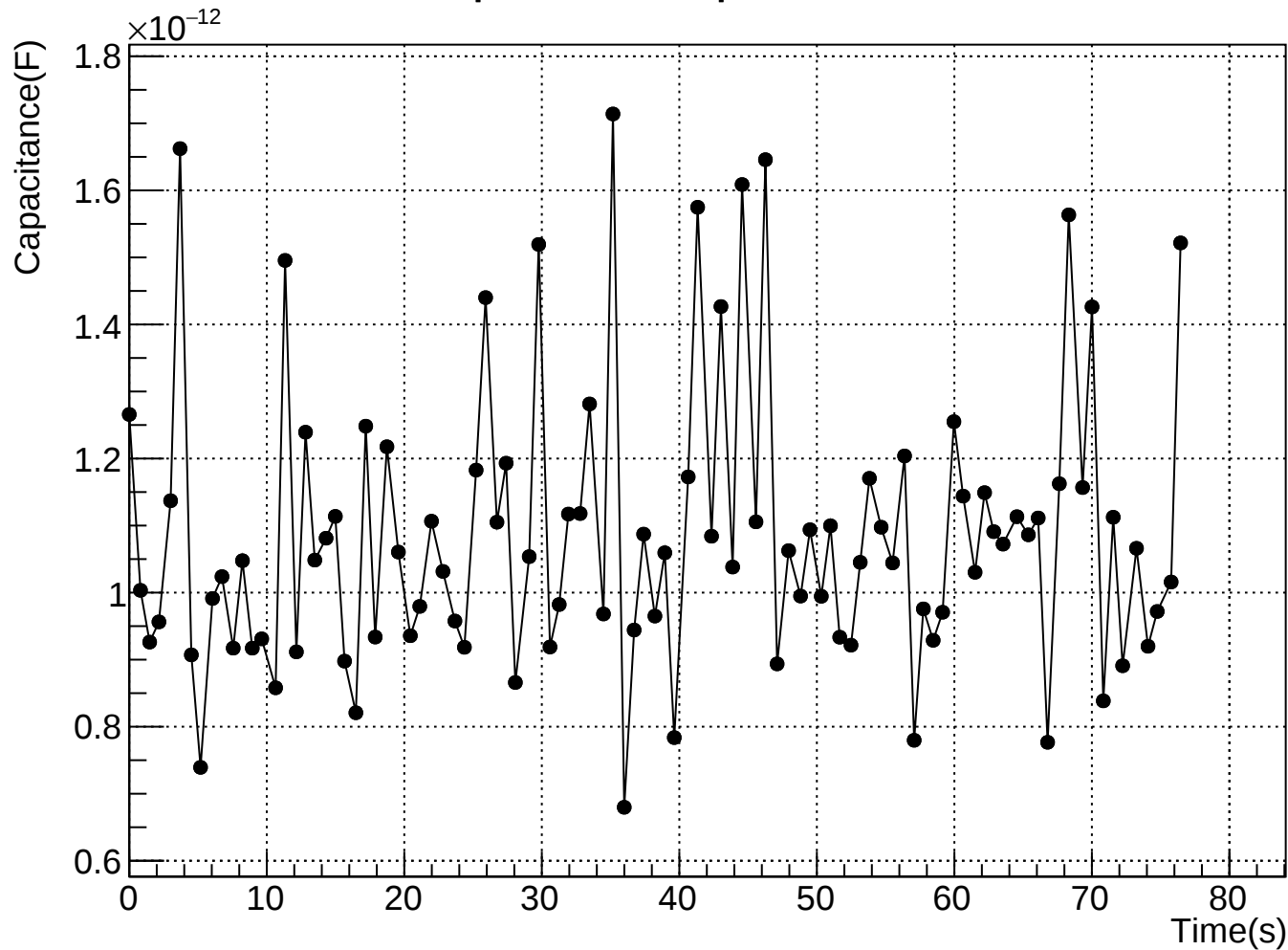
Sample101 Chip5 Channel061



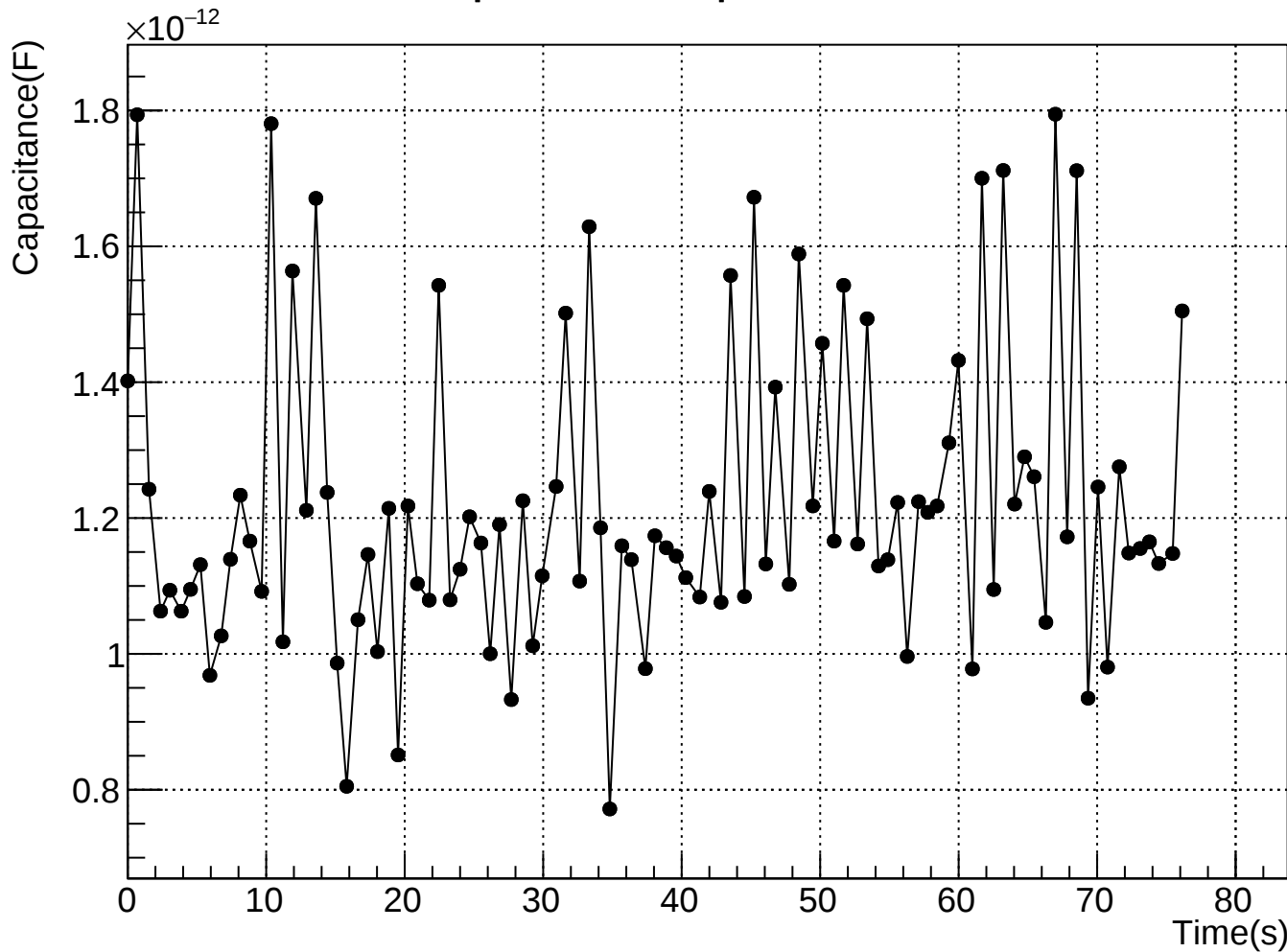
Sample101 Chip5 Channel062



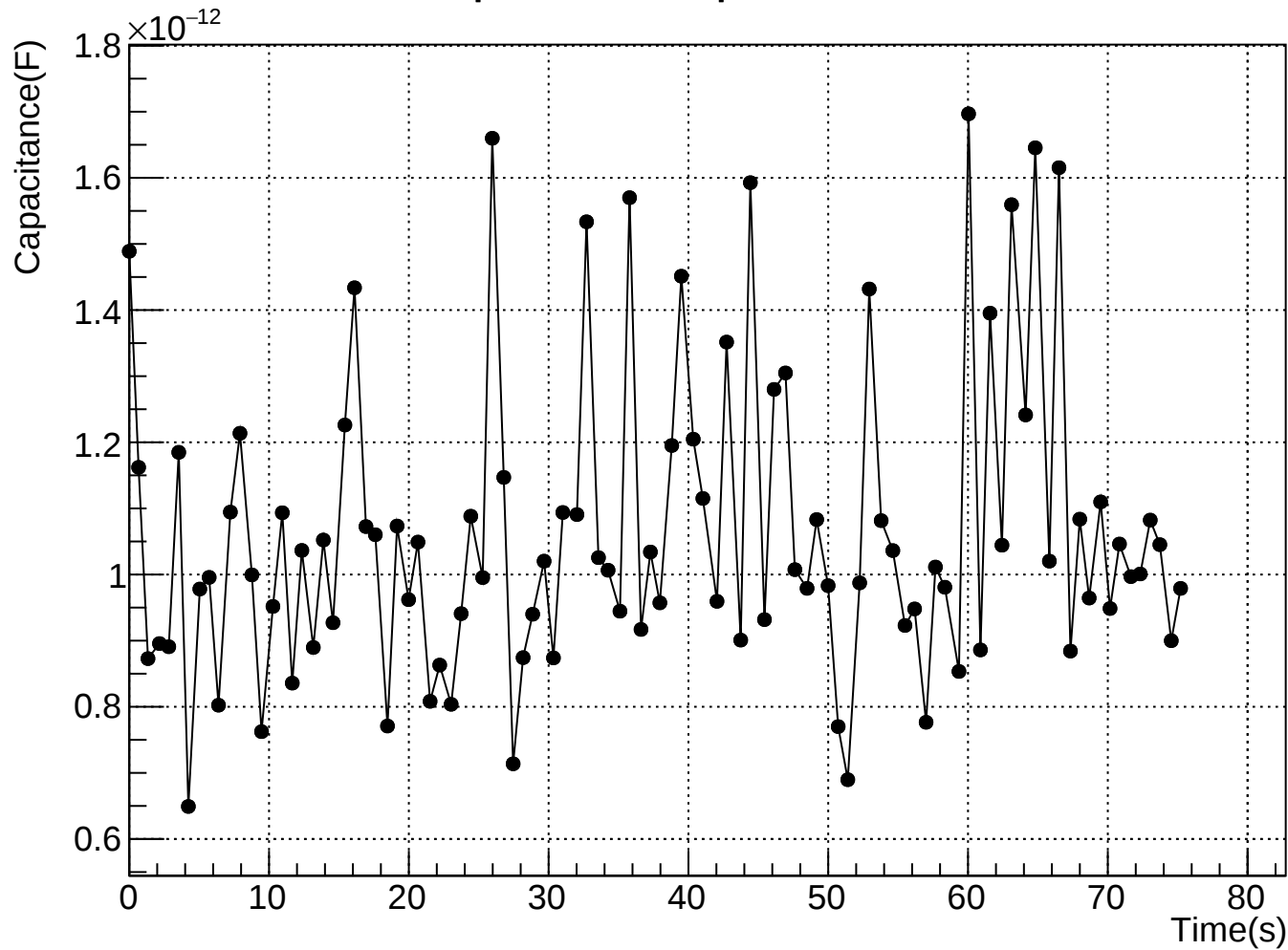
Sample101 Chip5 Channel063



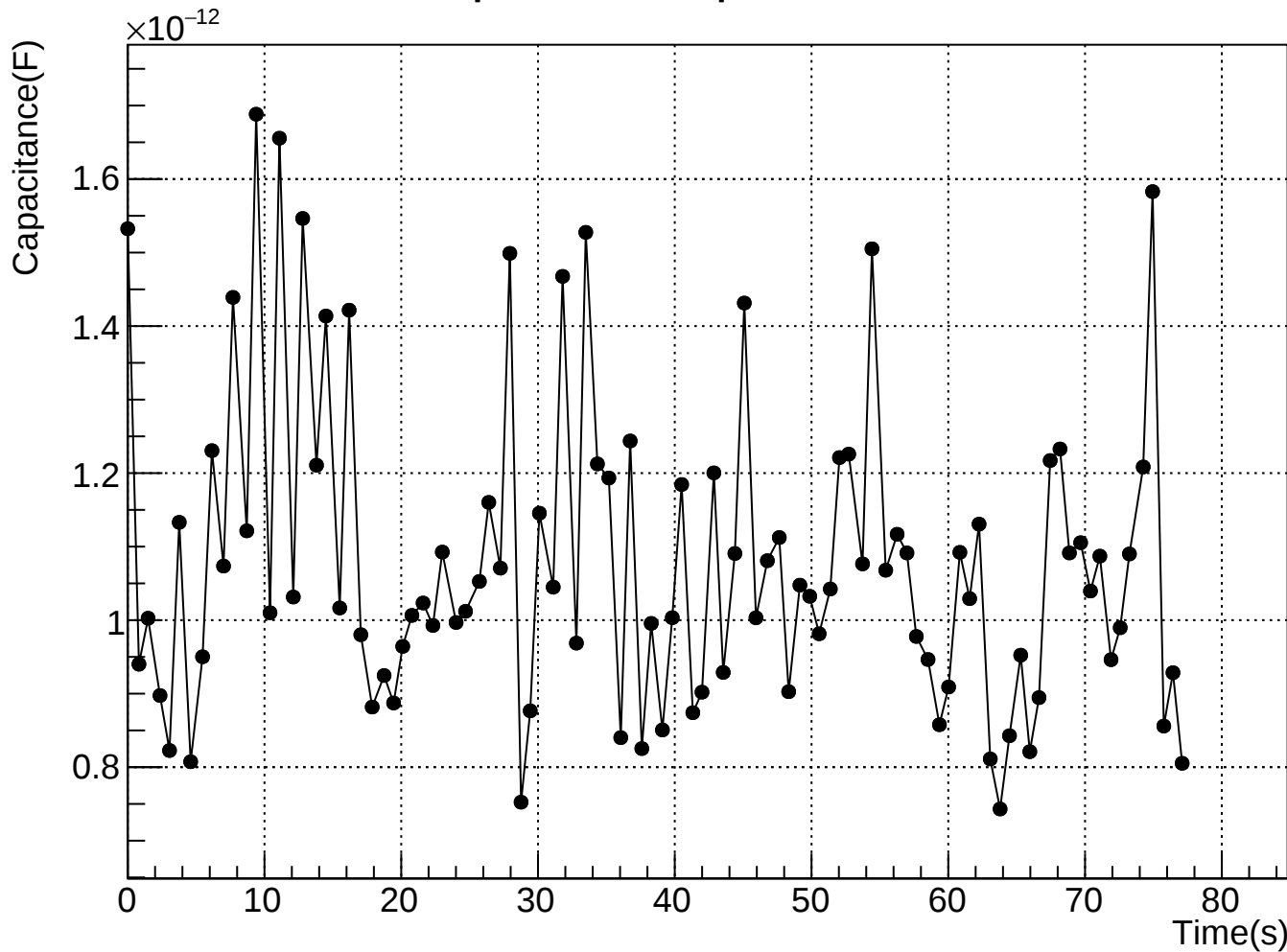
Sample101 Chip5 Channel064



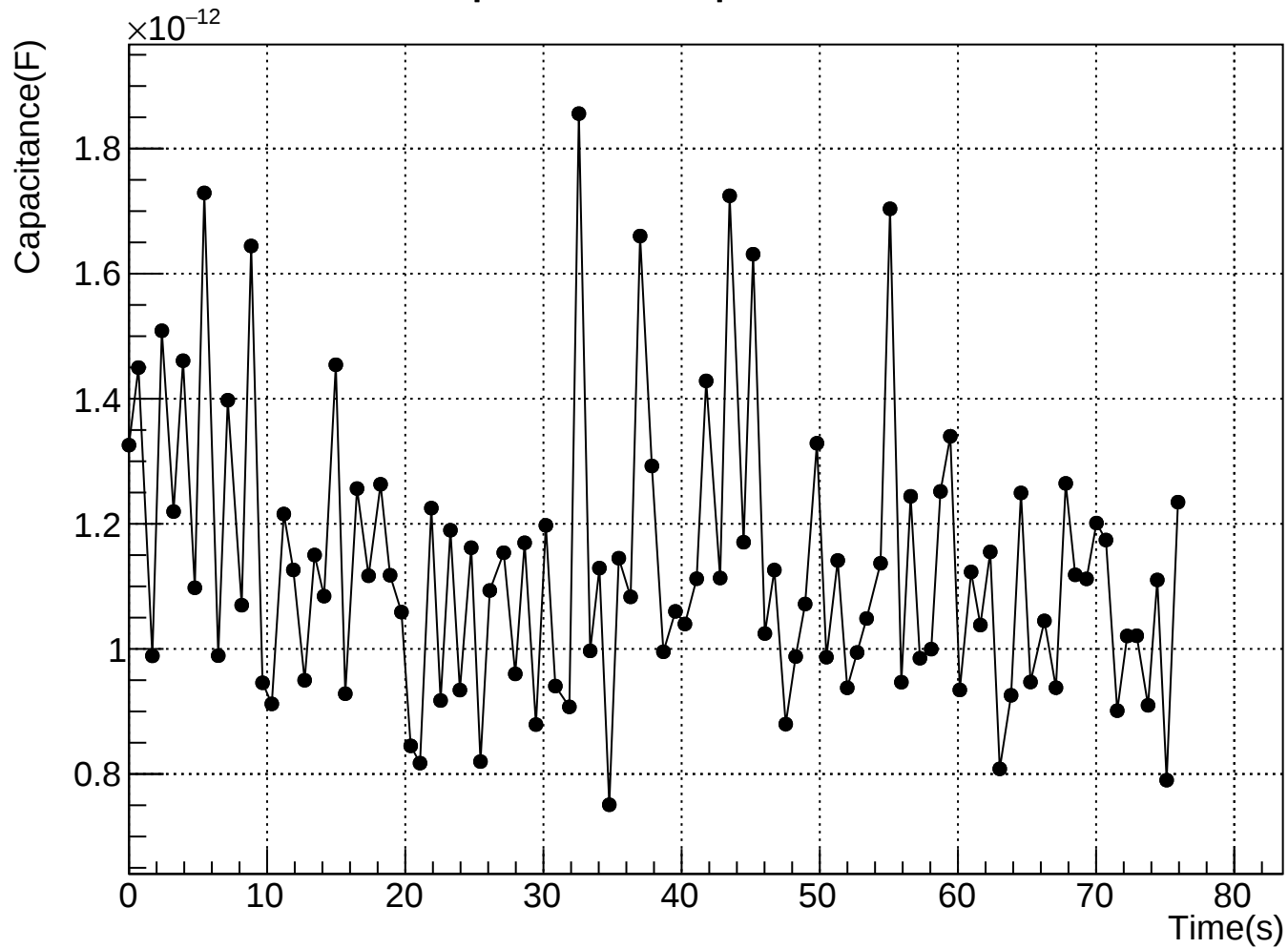
Sample101 Chip5 Channel065



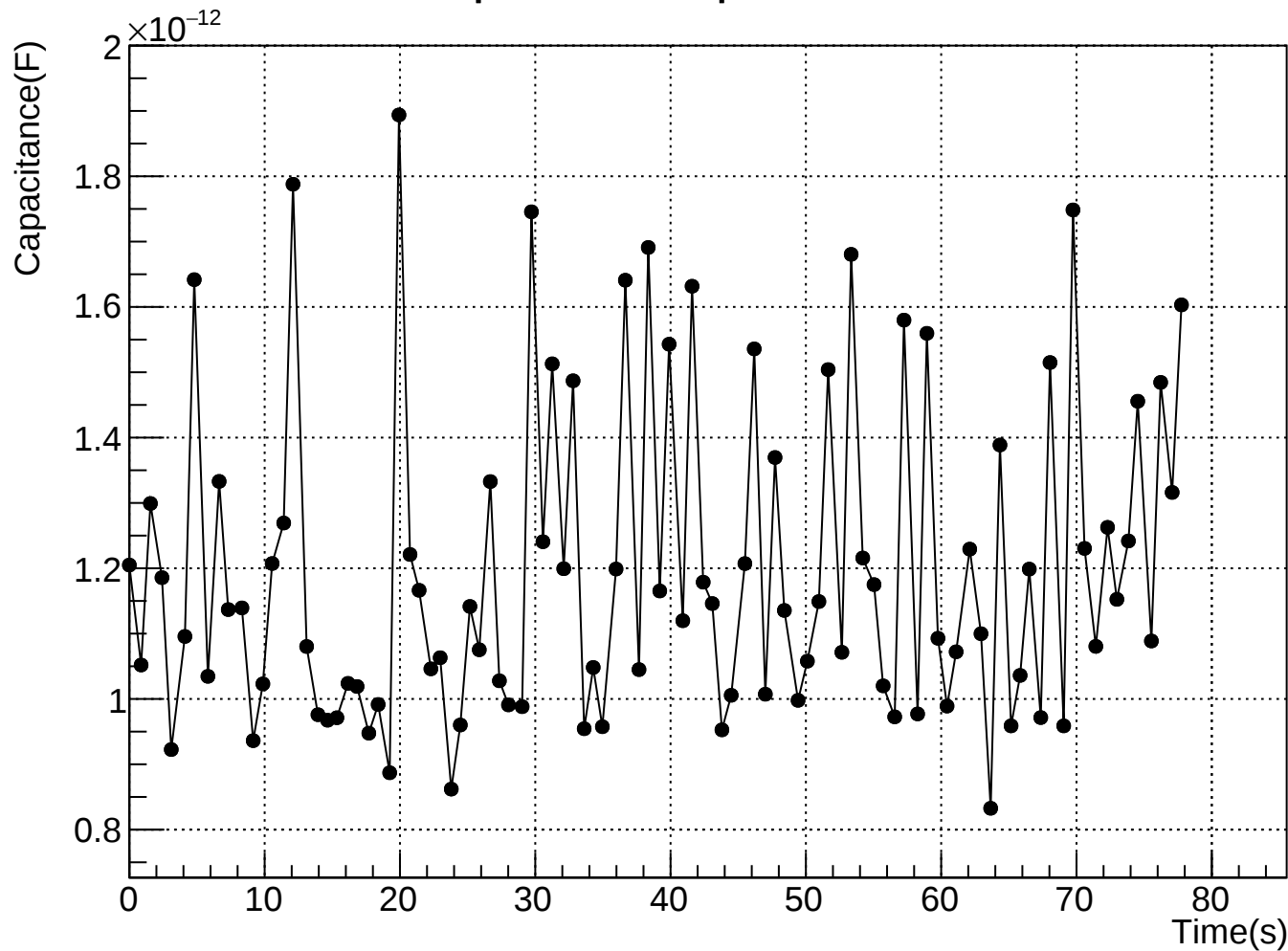
Sample101 Chip5 Channel066



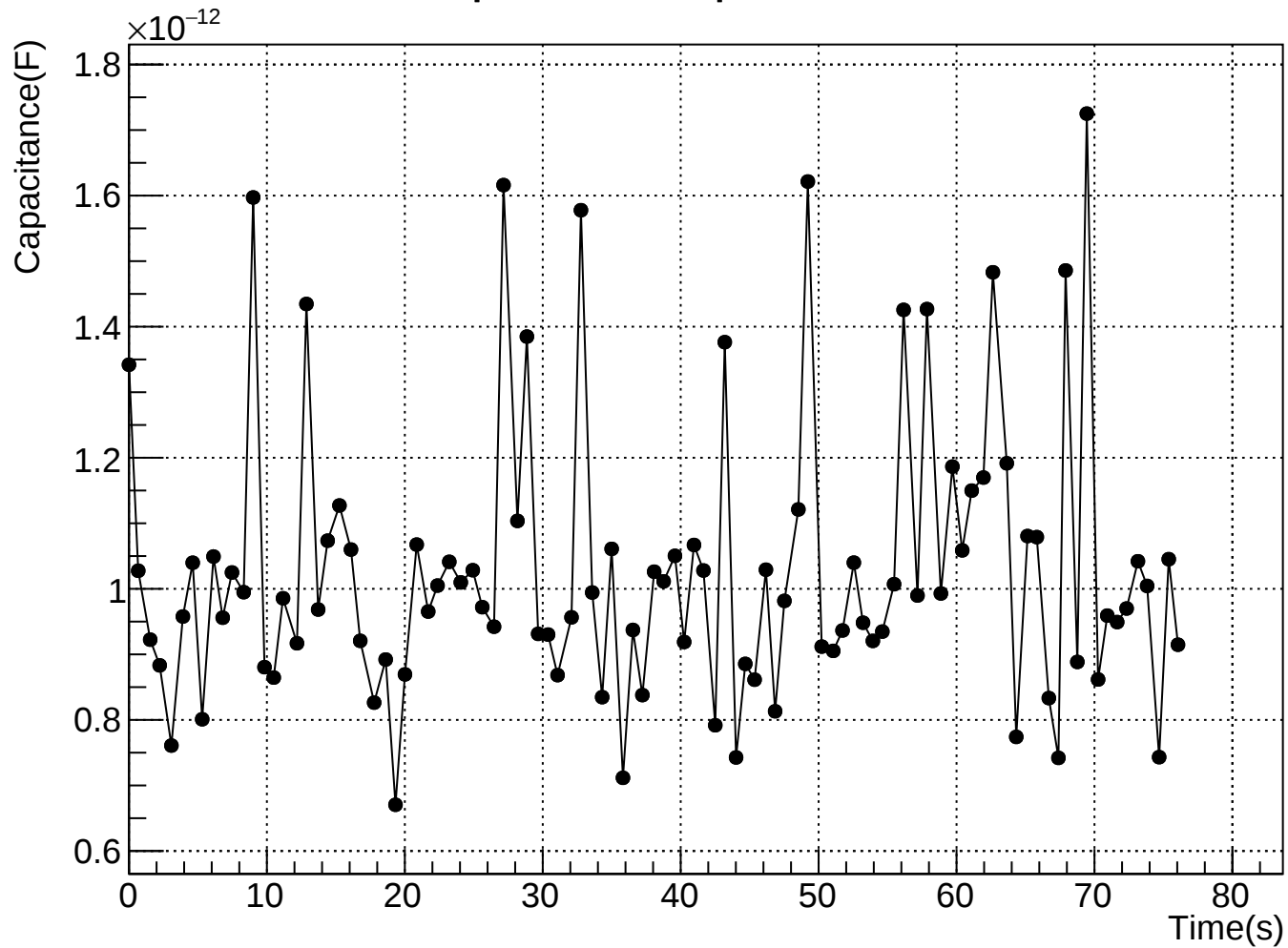
Sample101 Chip5 Channel067



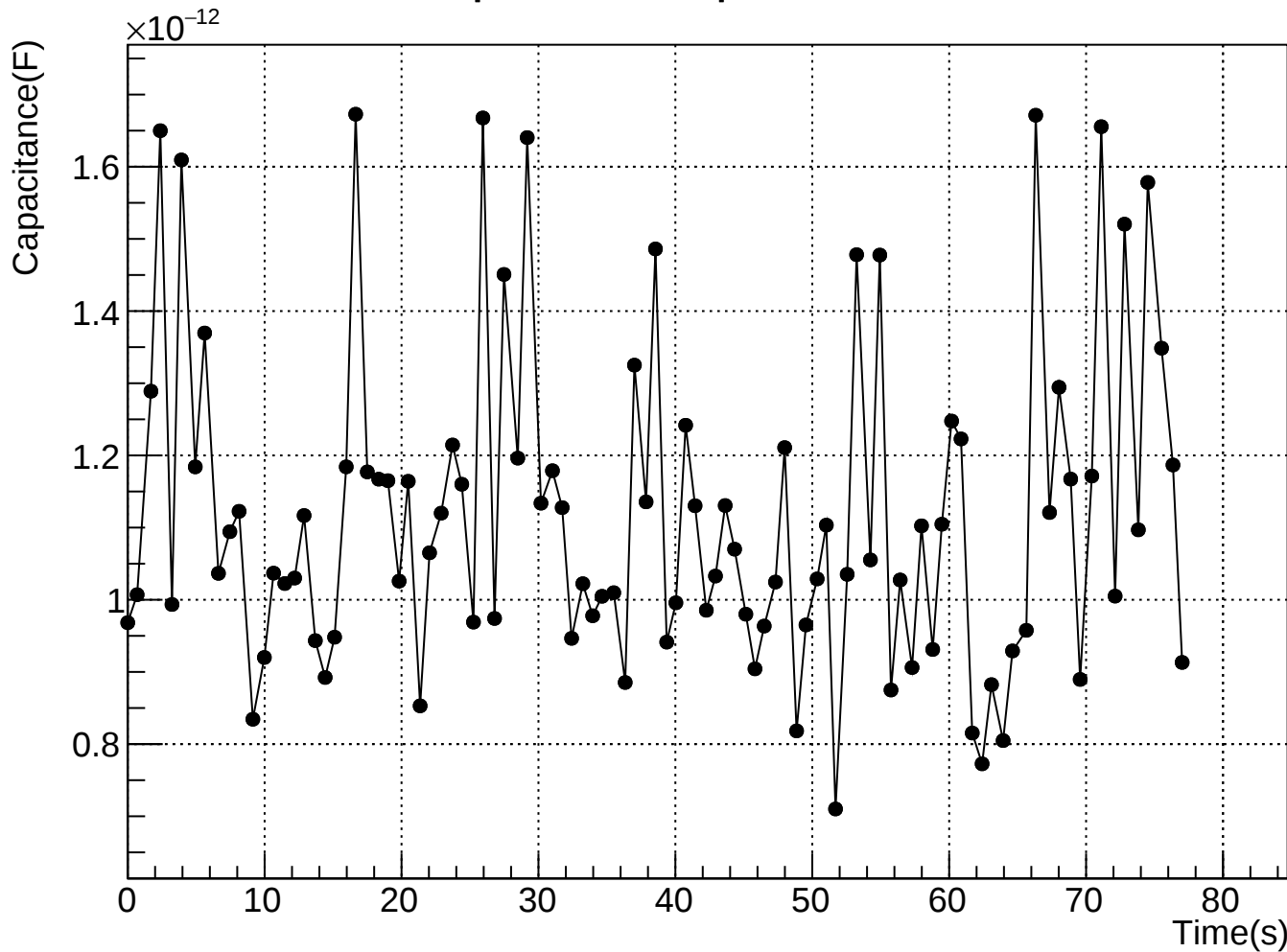
Sample101 Chip5 Channel068



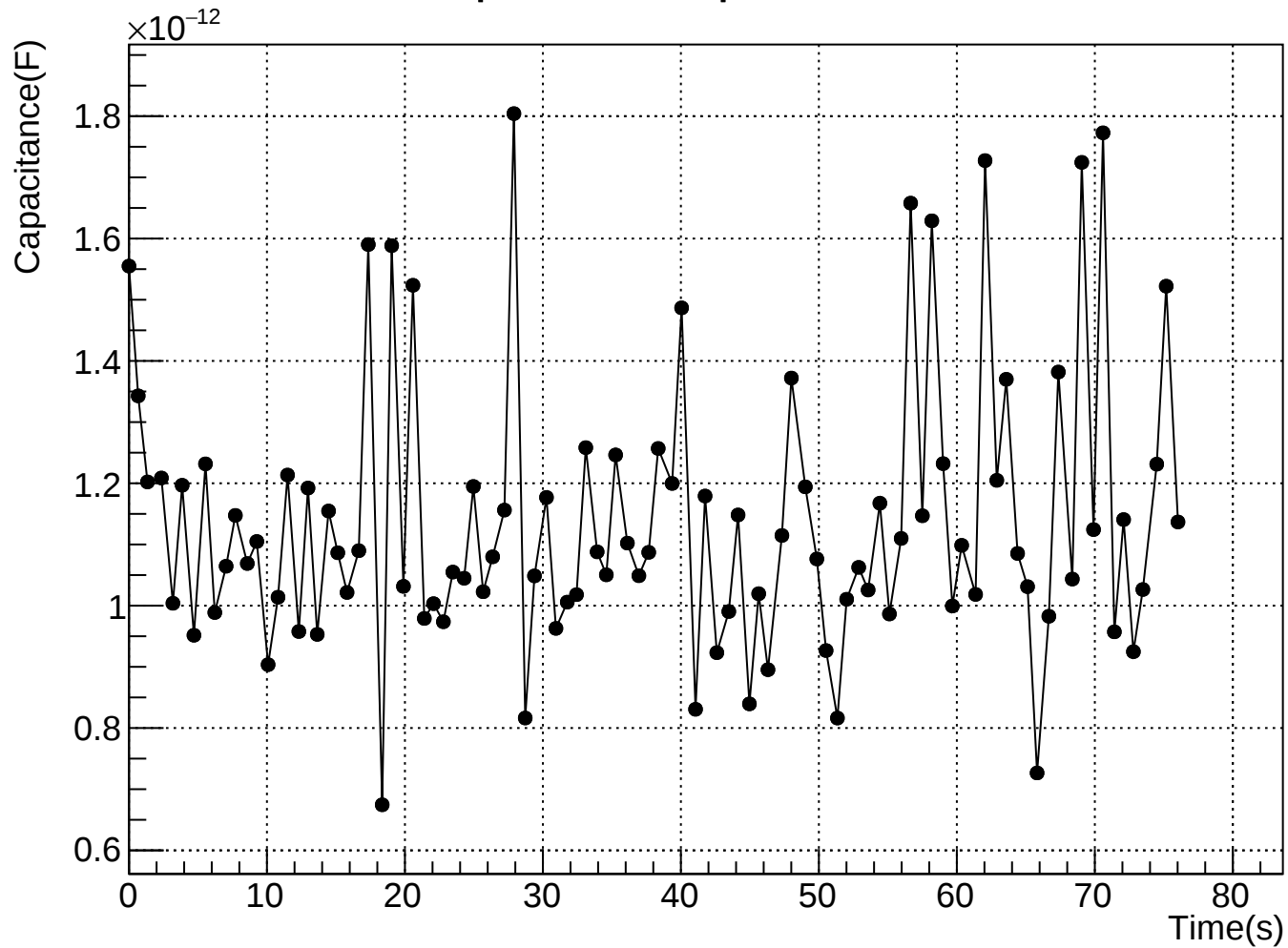
Sample101 Chip5 Channel069



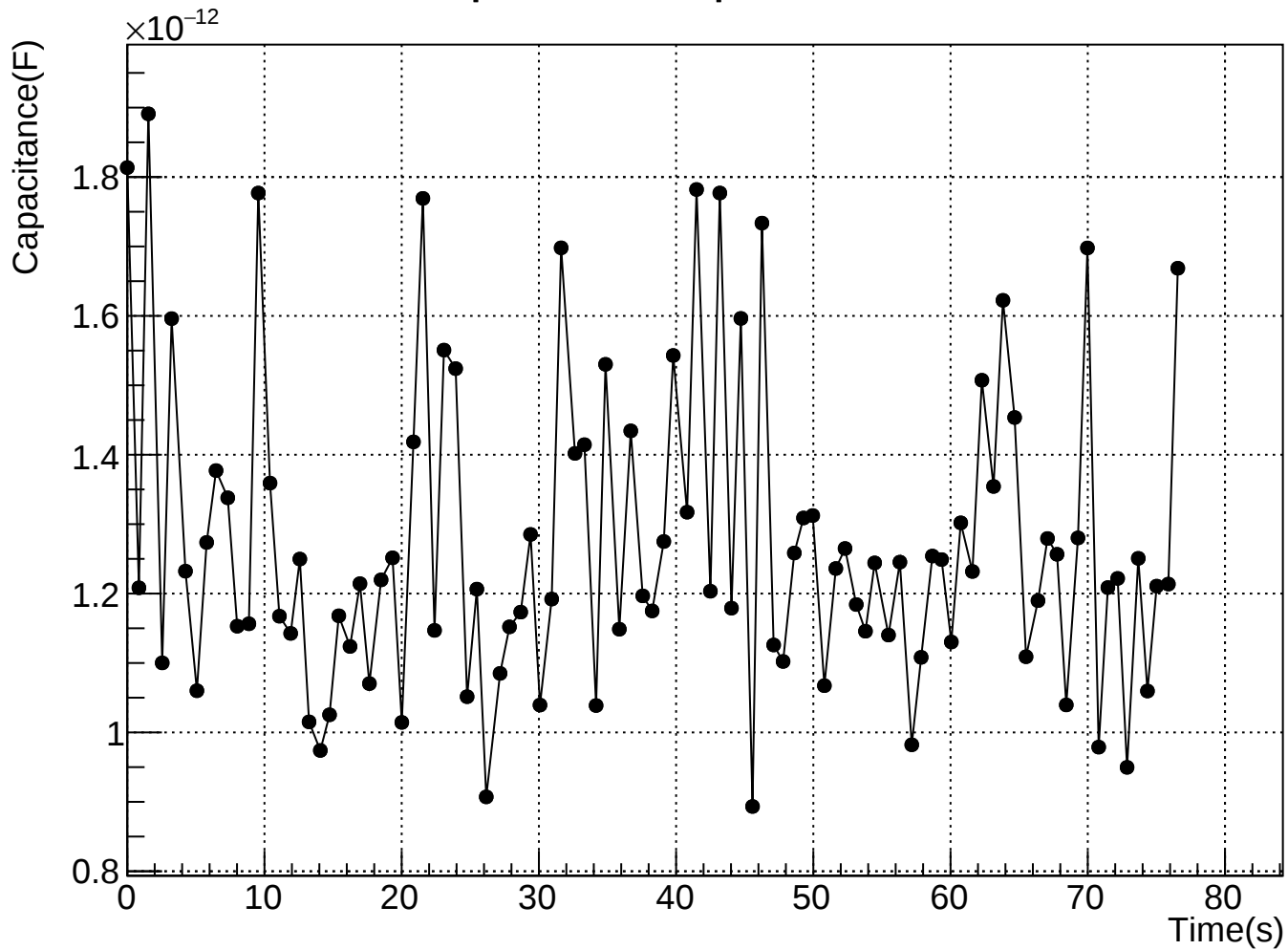
Sample101 Chip5 Channel070



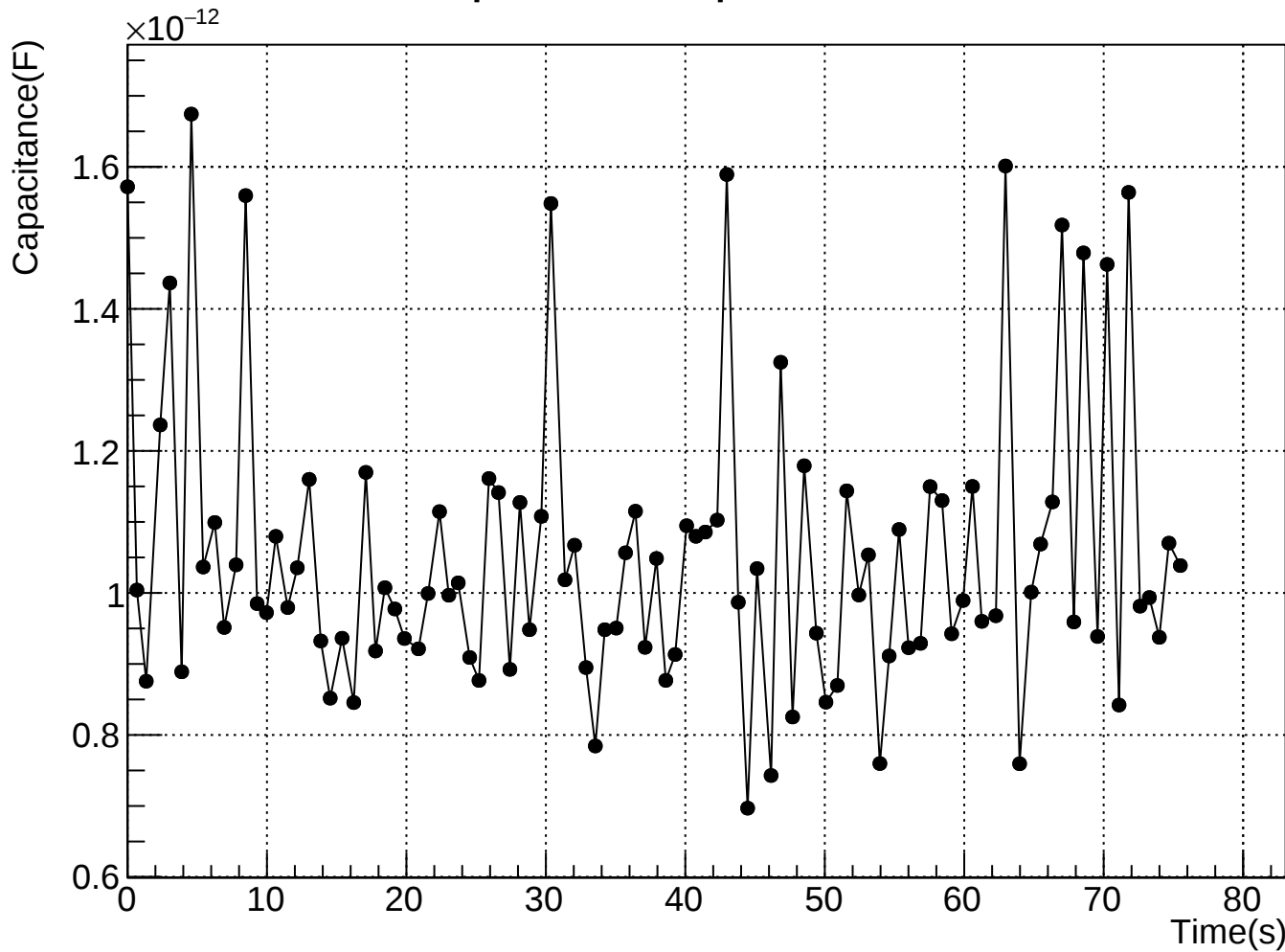
Sample101 Chip5 Channel071



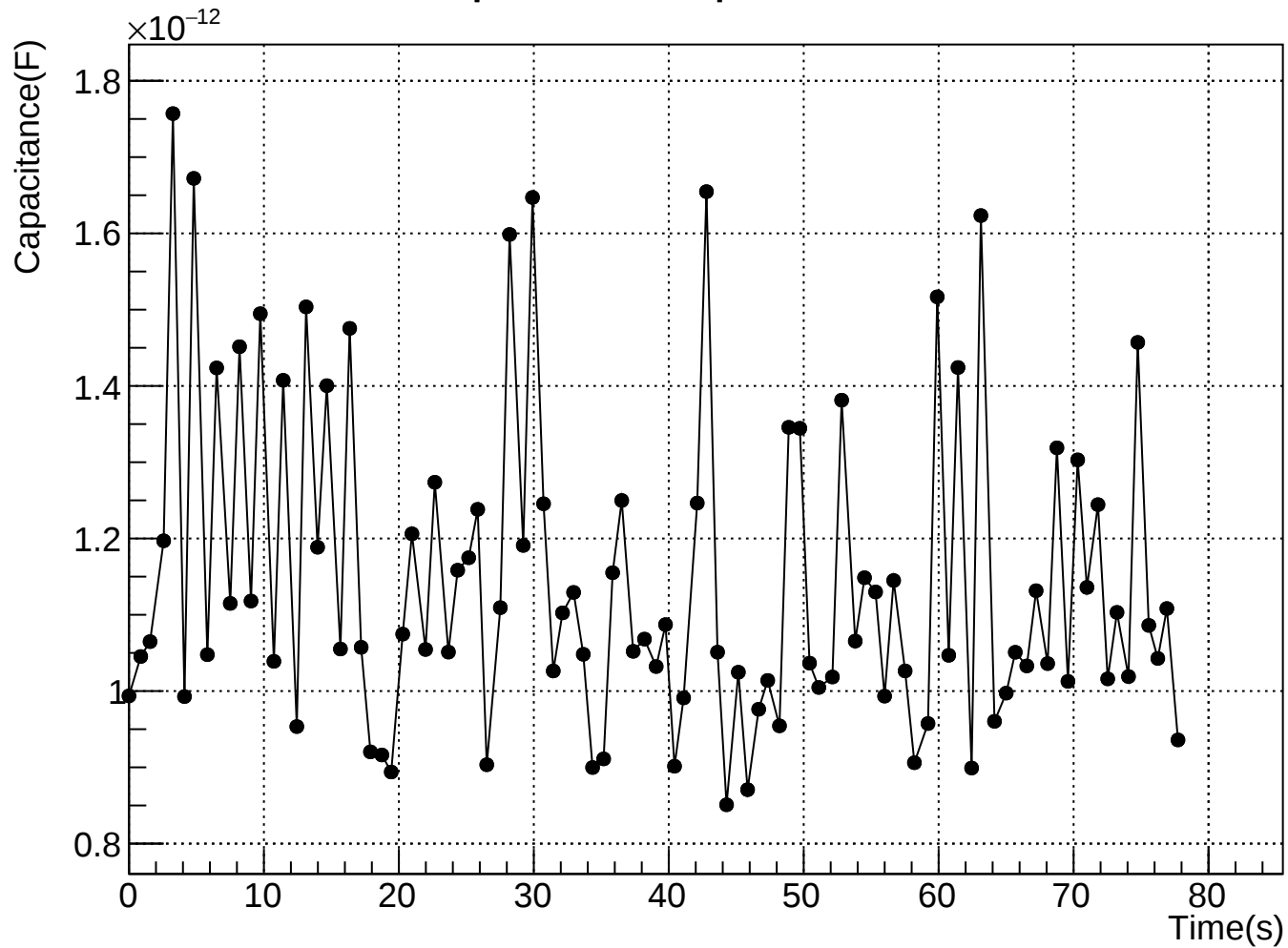
Sample101 Chip5 Channel072



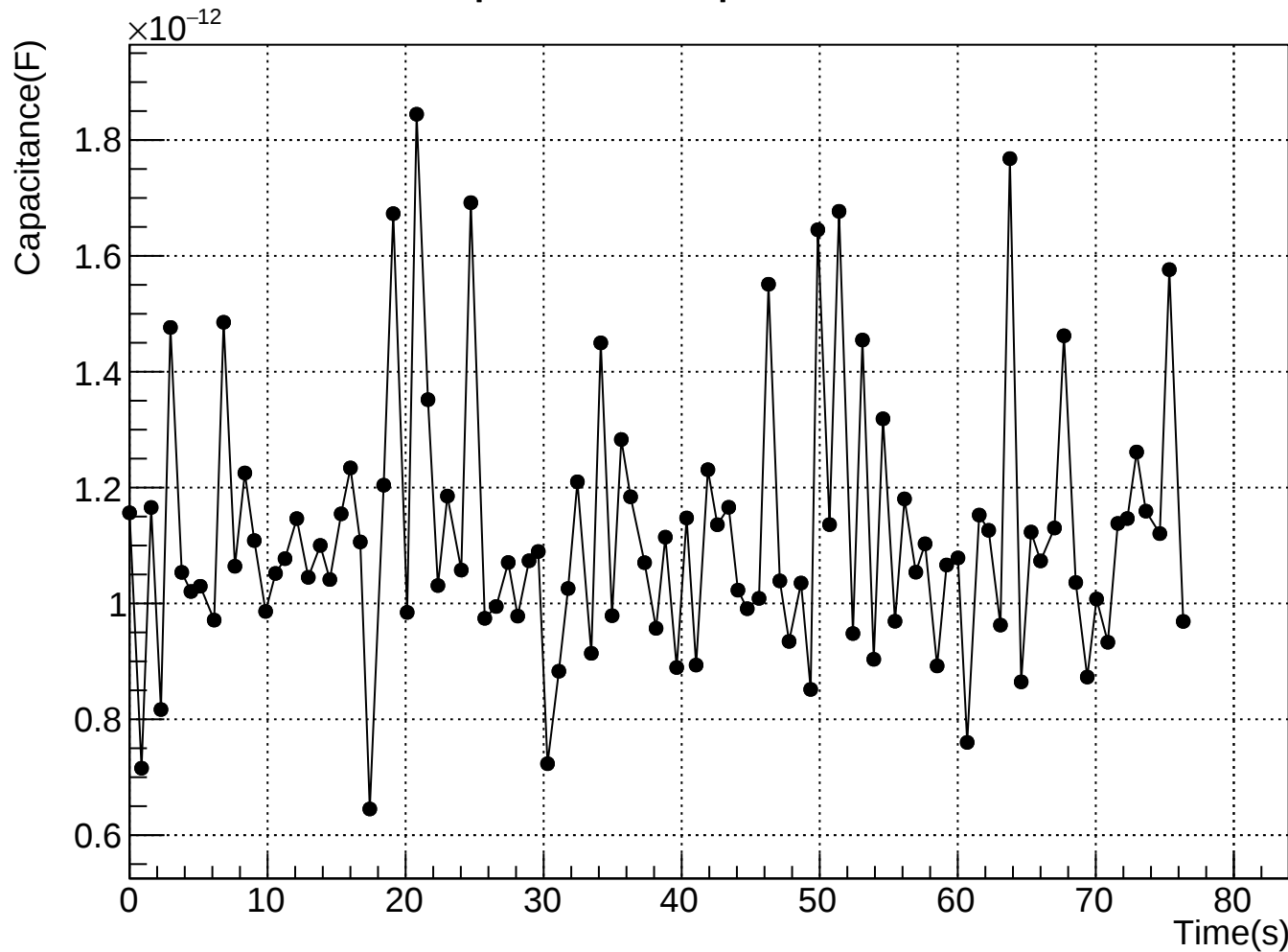
Sample101 Chip5 Channel073



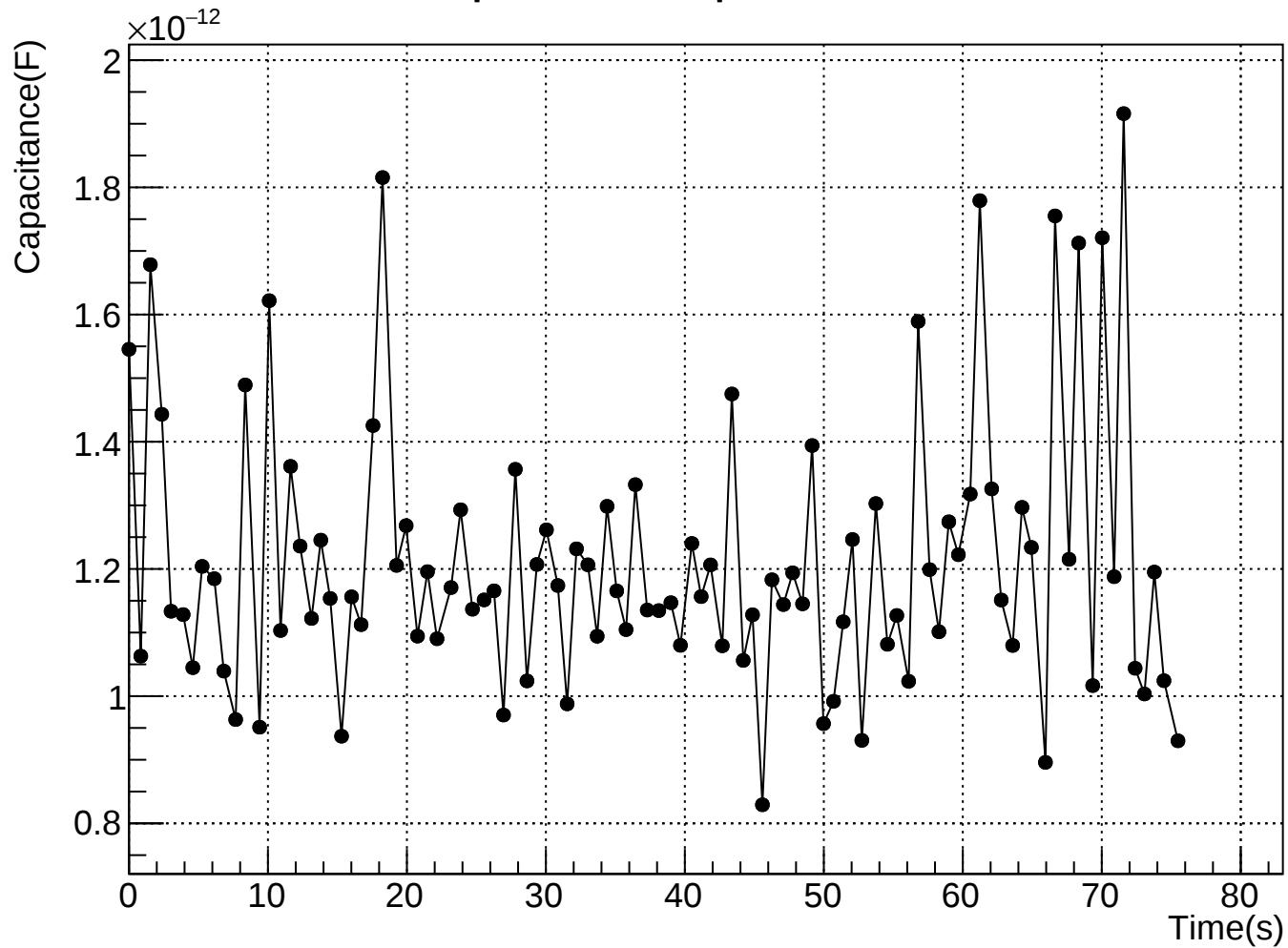
Sample101 Chip5 Channel074



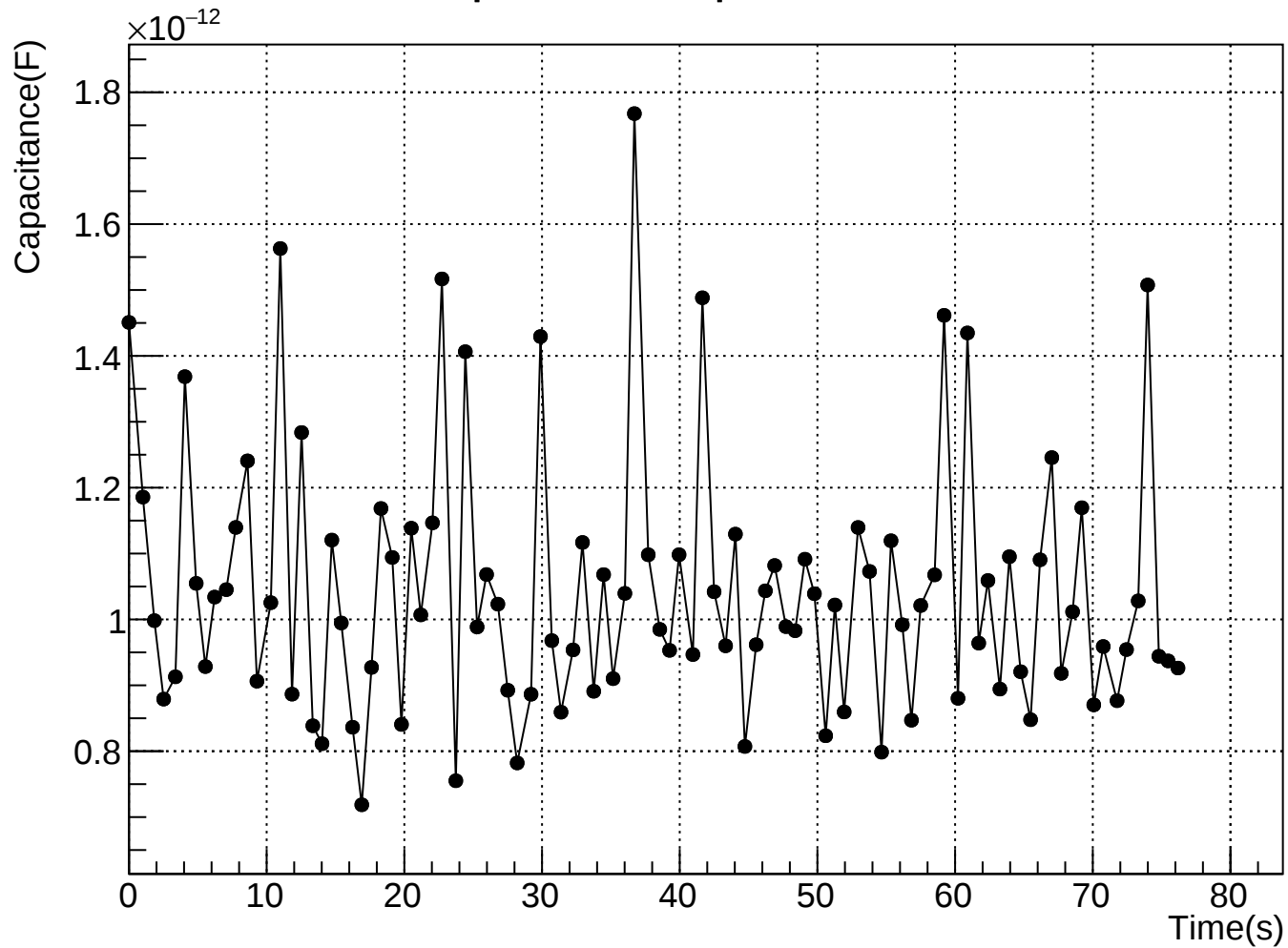
Sample101 Chip5 Channel075



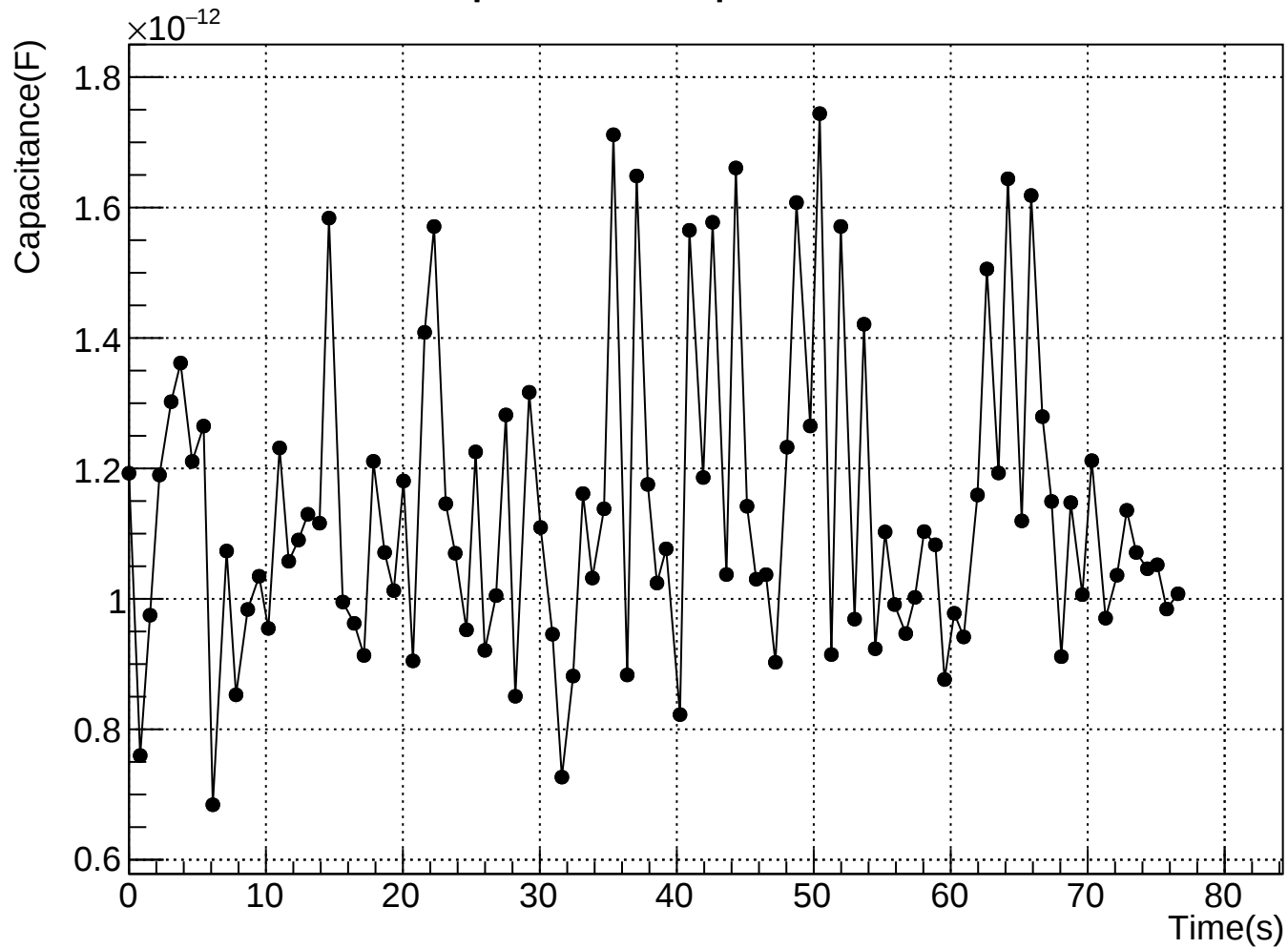
Sample101 Chip5 Channel076



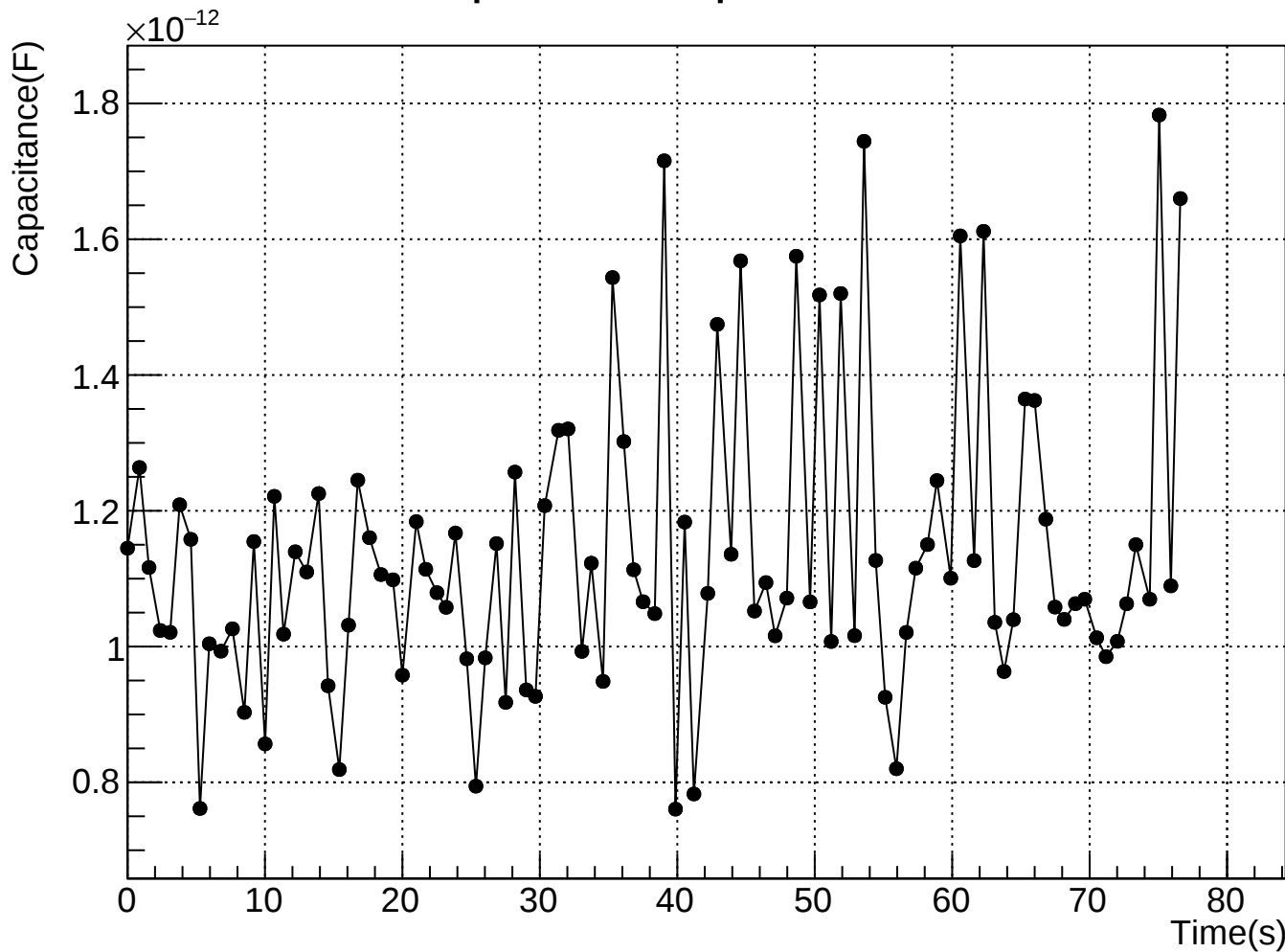
Sample101 Chip5 Channel077



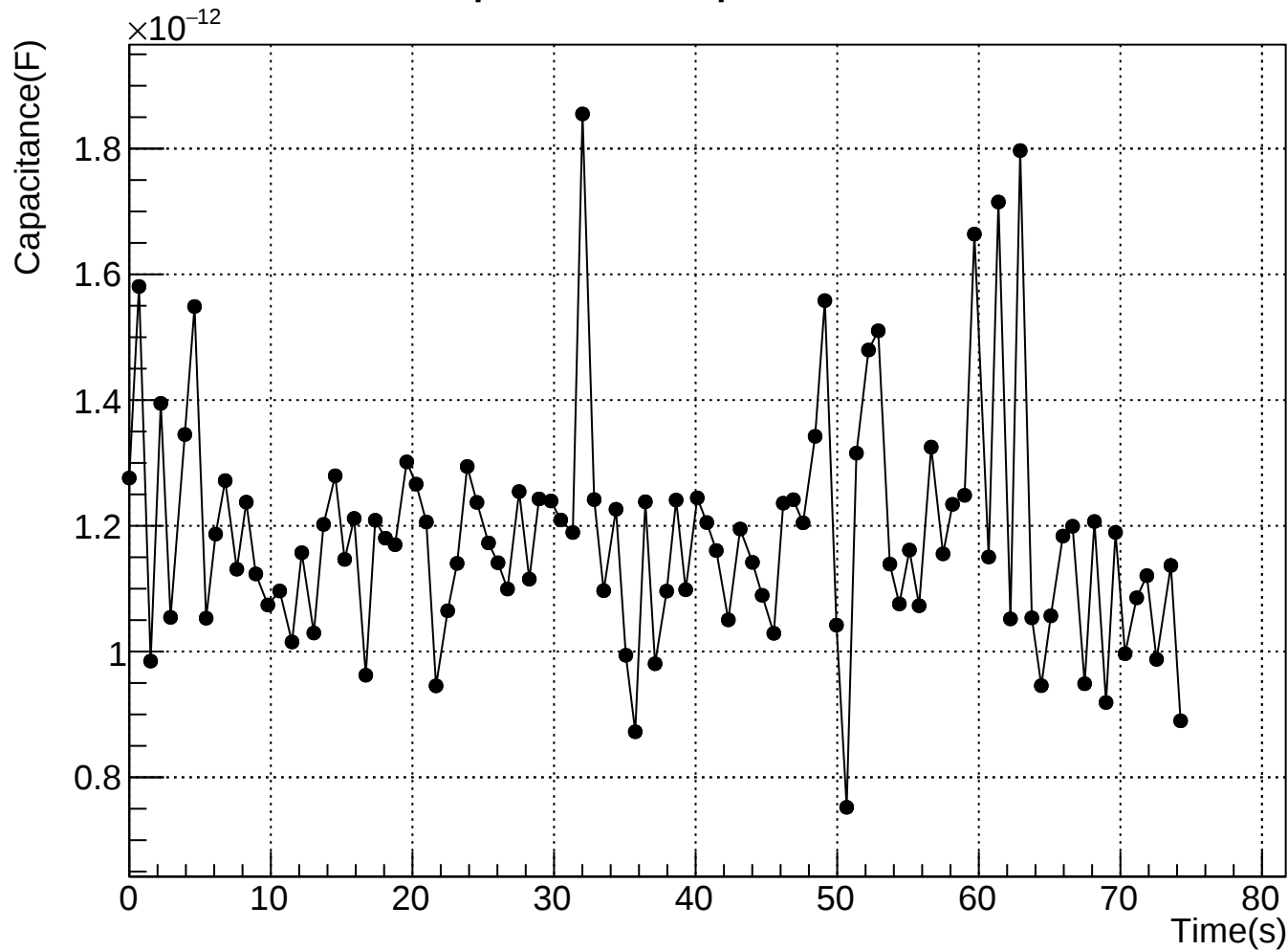
Sample101 Chip5 Channel078



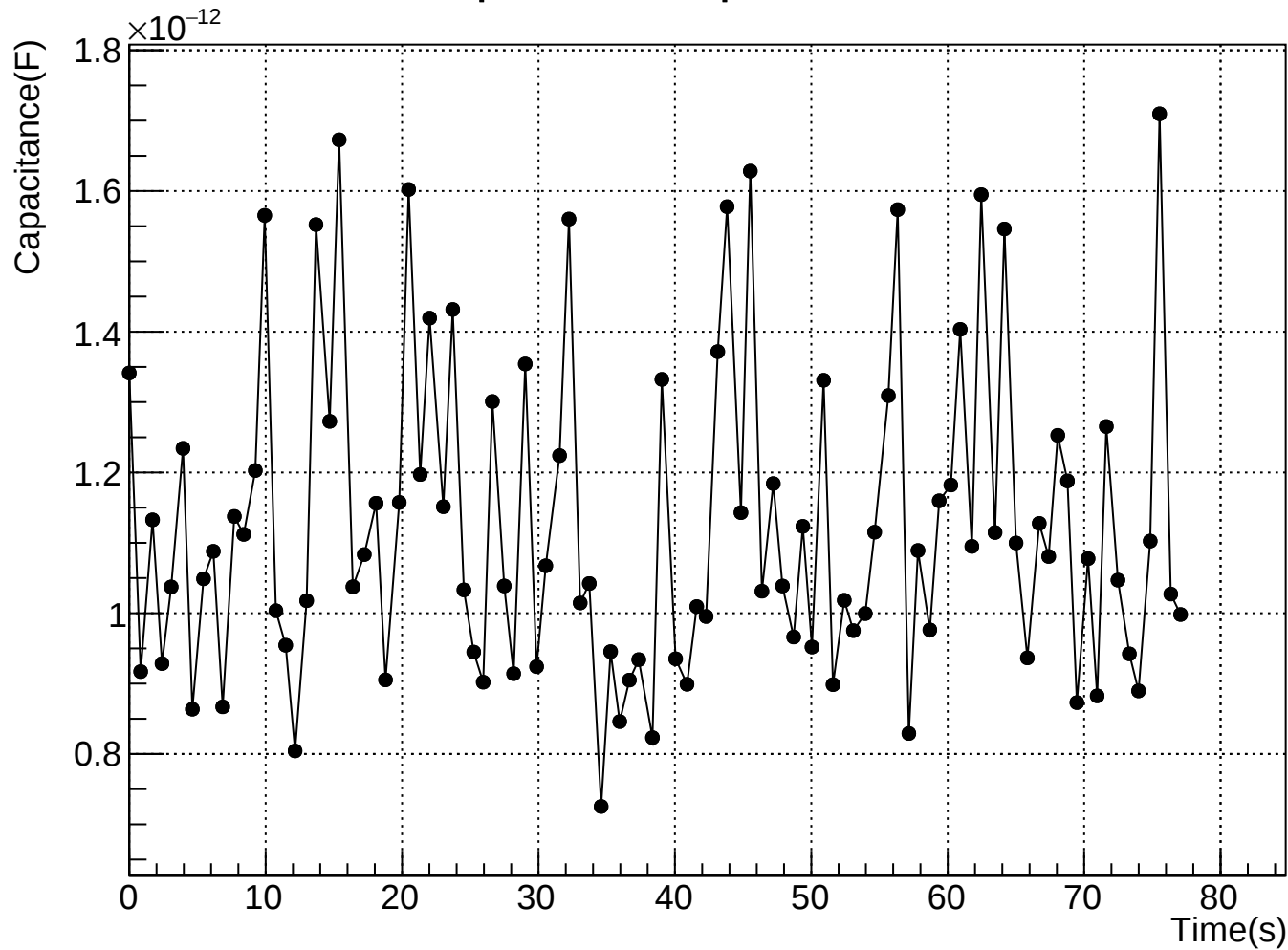
Sample101 Chip5 Channel079



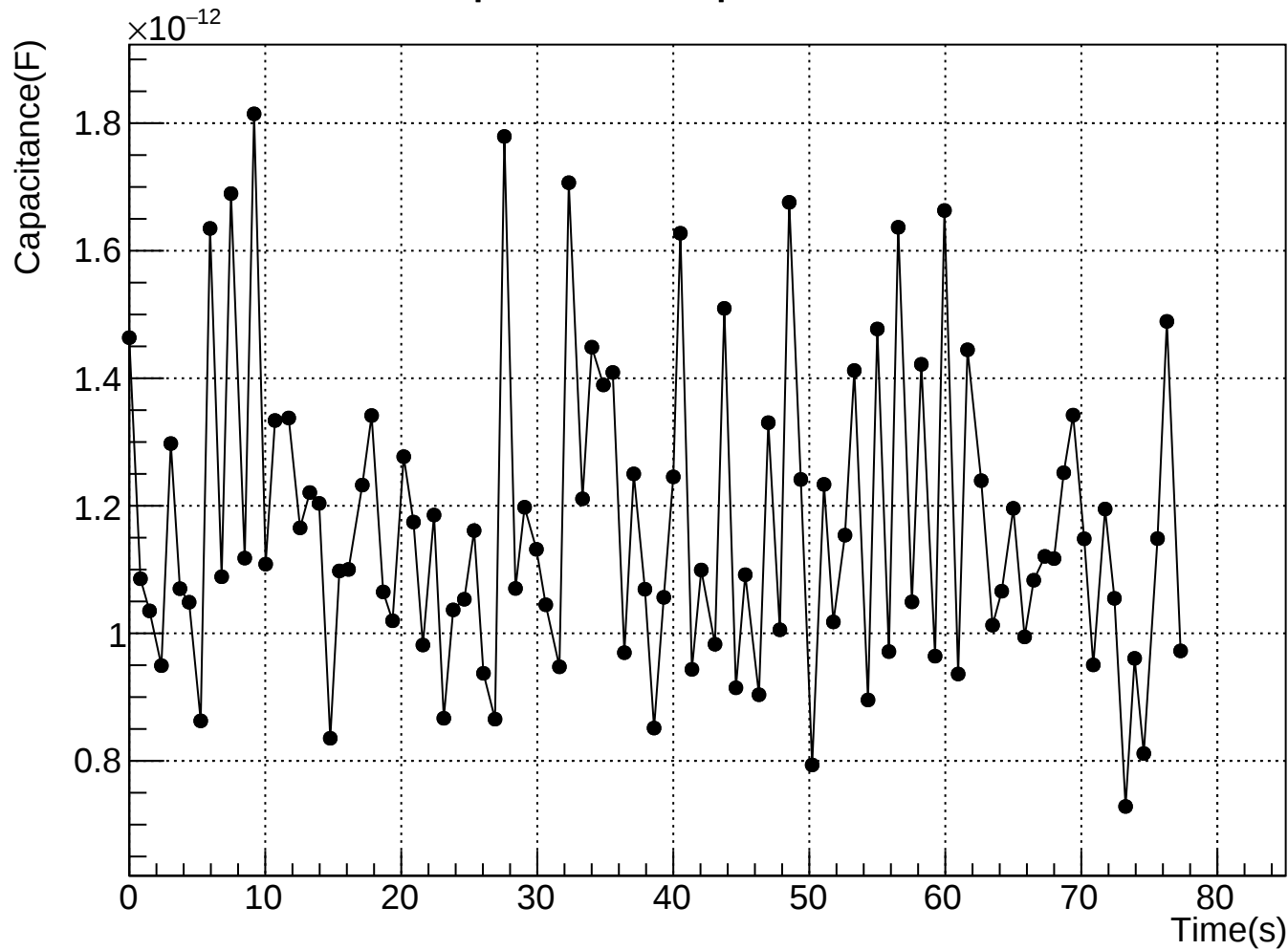
Sample101 Chip5 Channel080



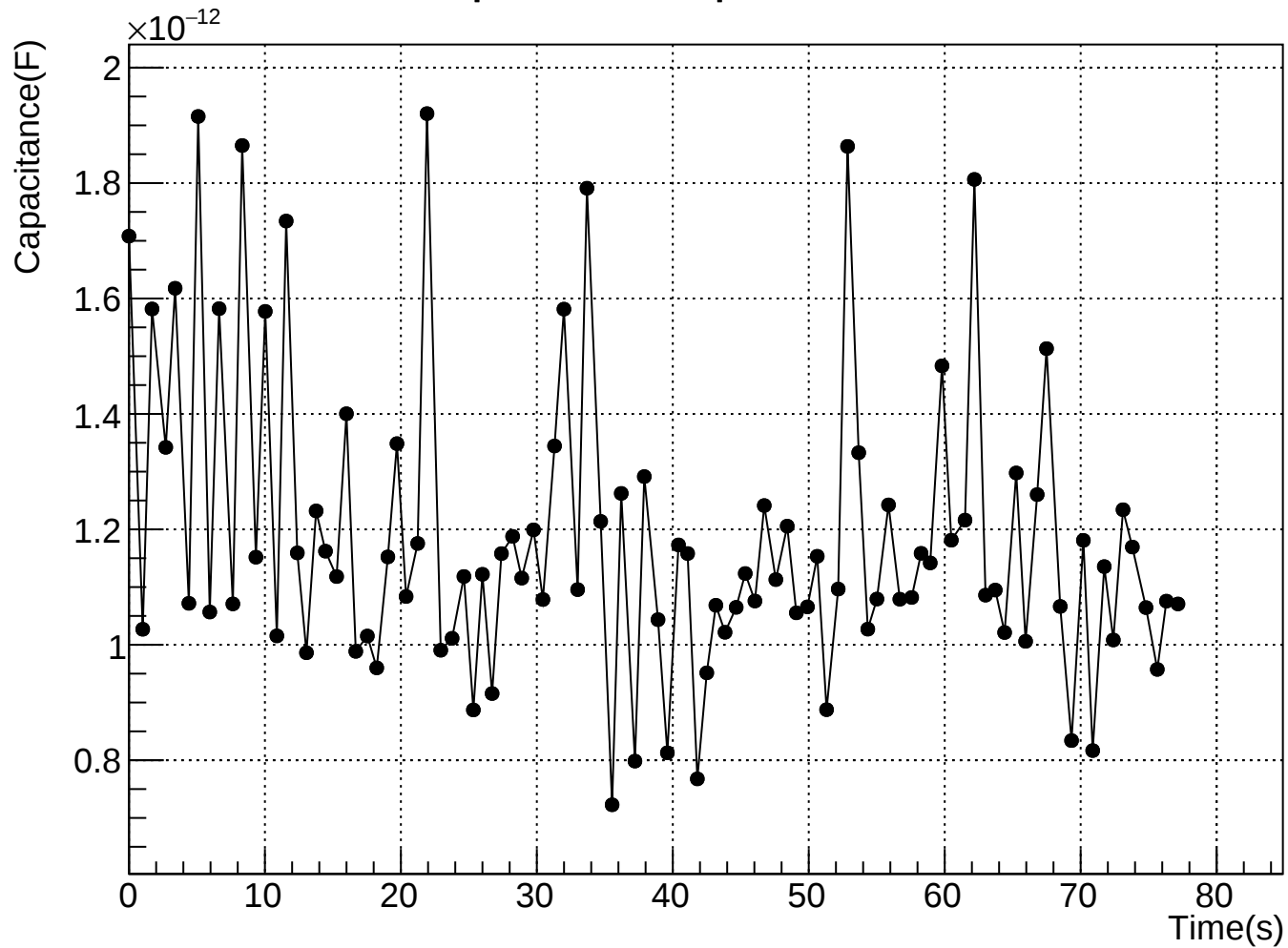
Sample101 Chip5 Channel081



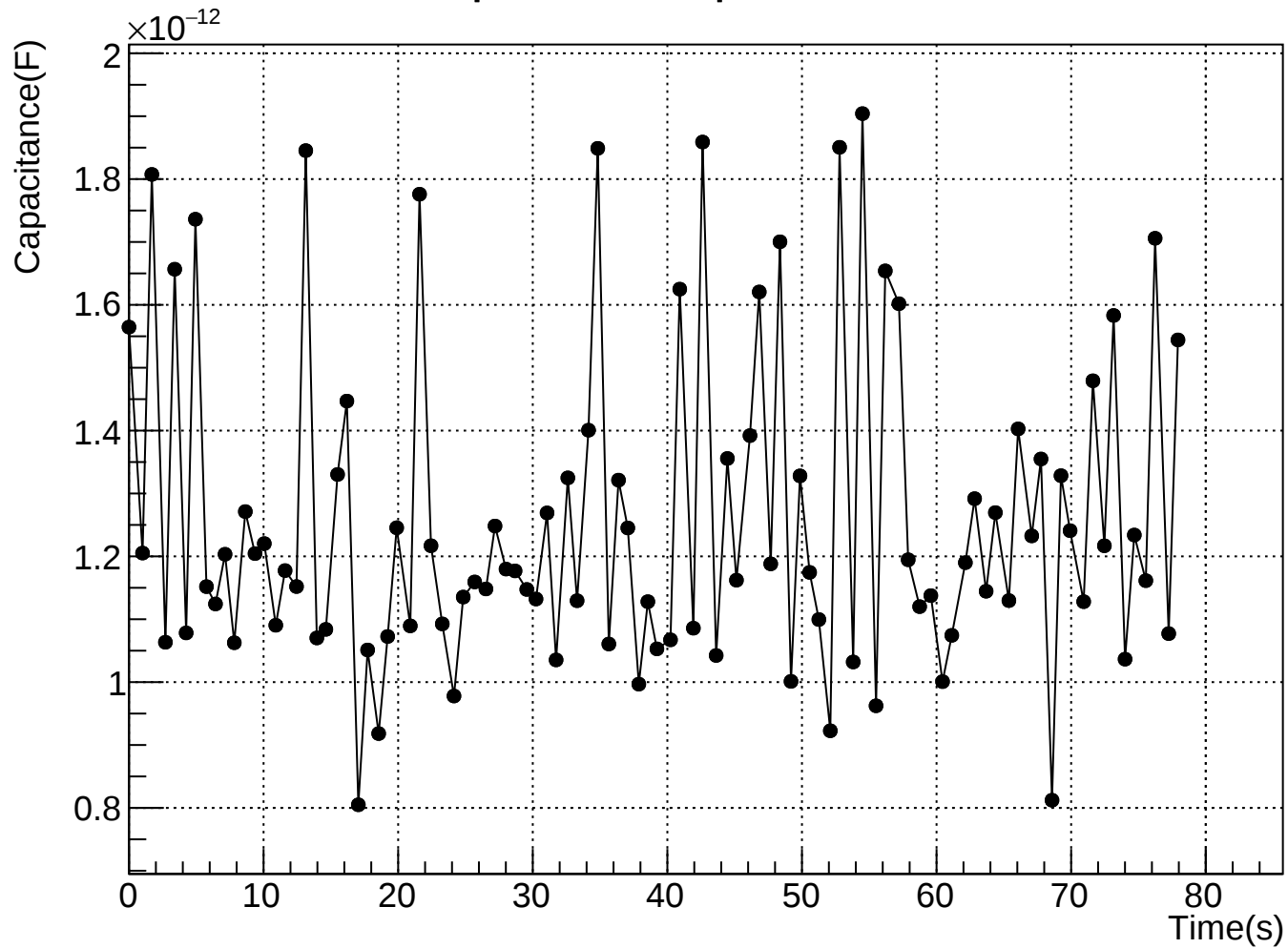
Sample101 Chip5 Channel082



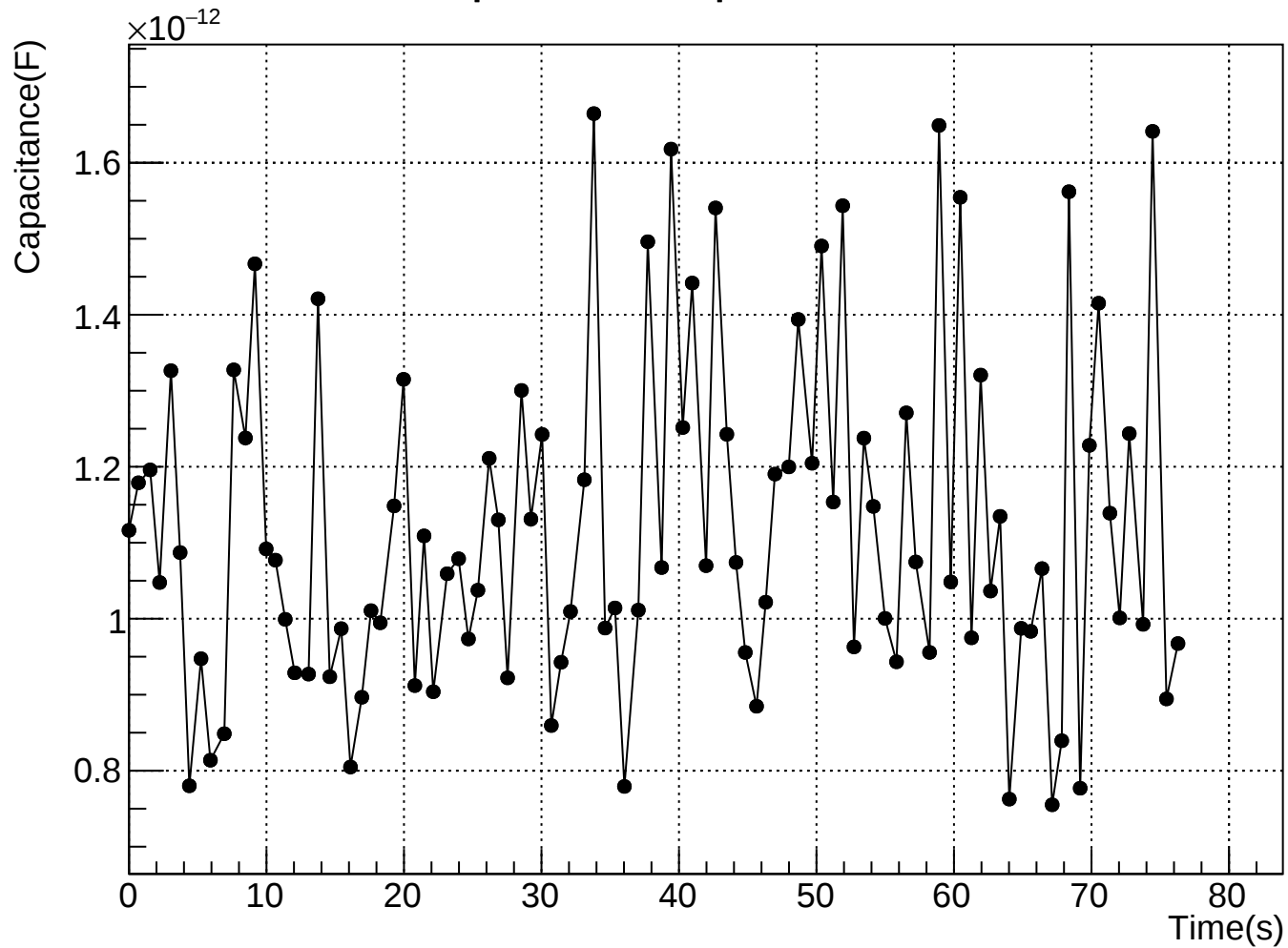
Sample101 Chip5 Channel083



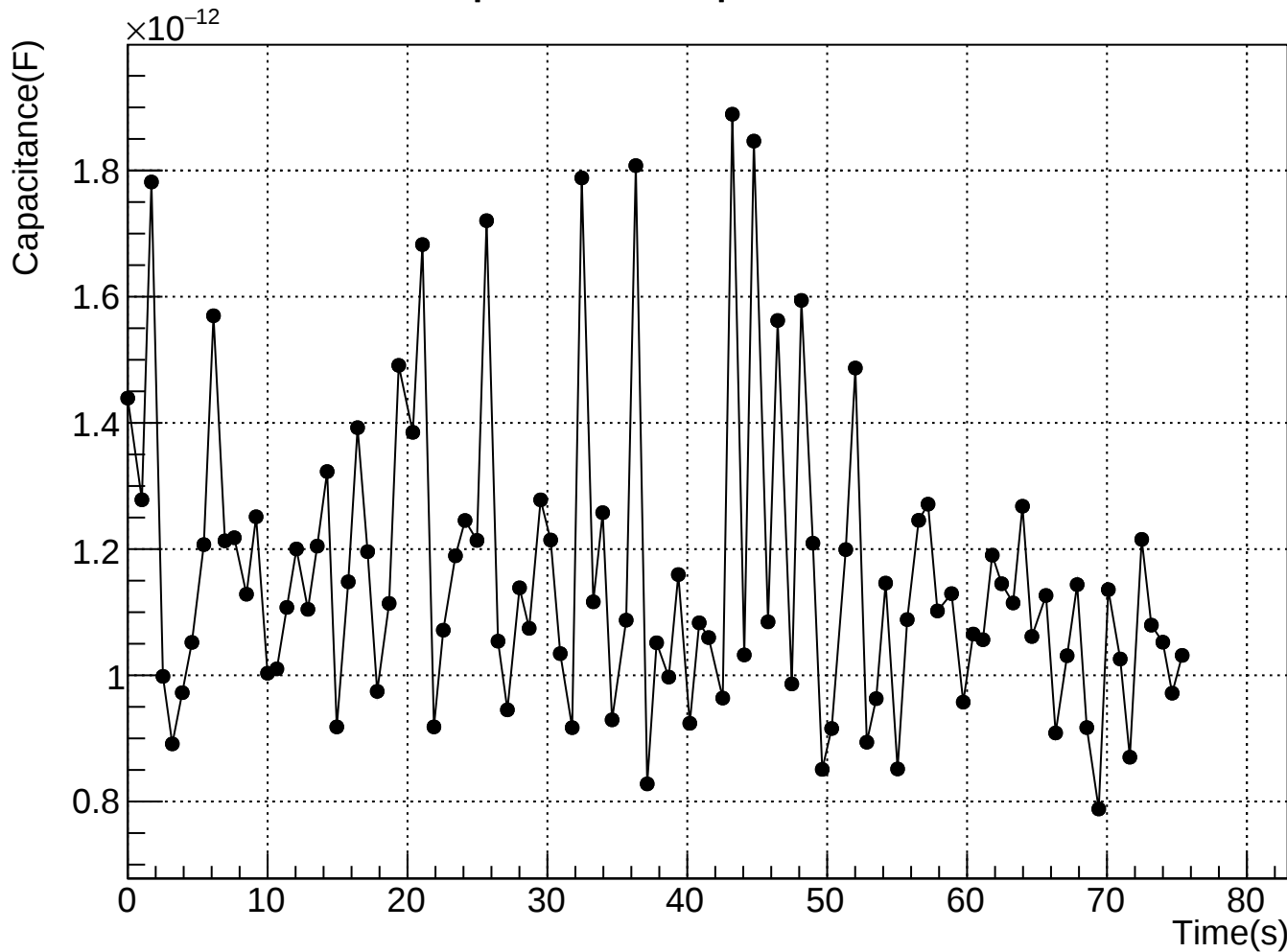
Sample101 Chip5 Channel084



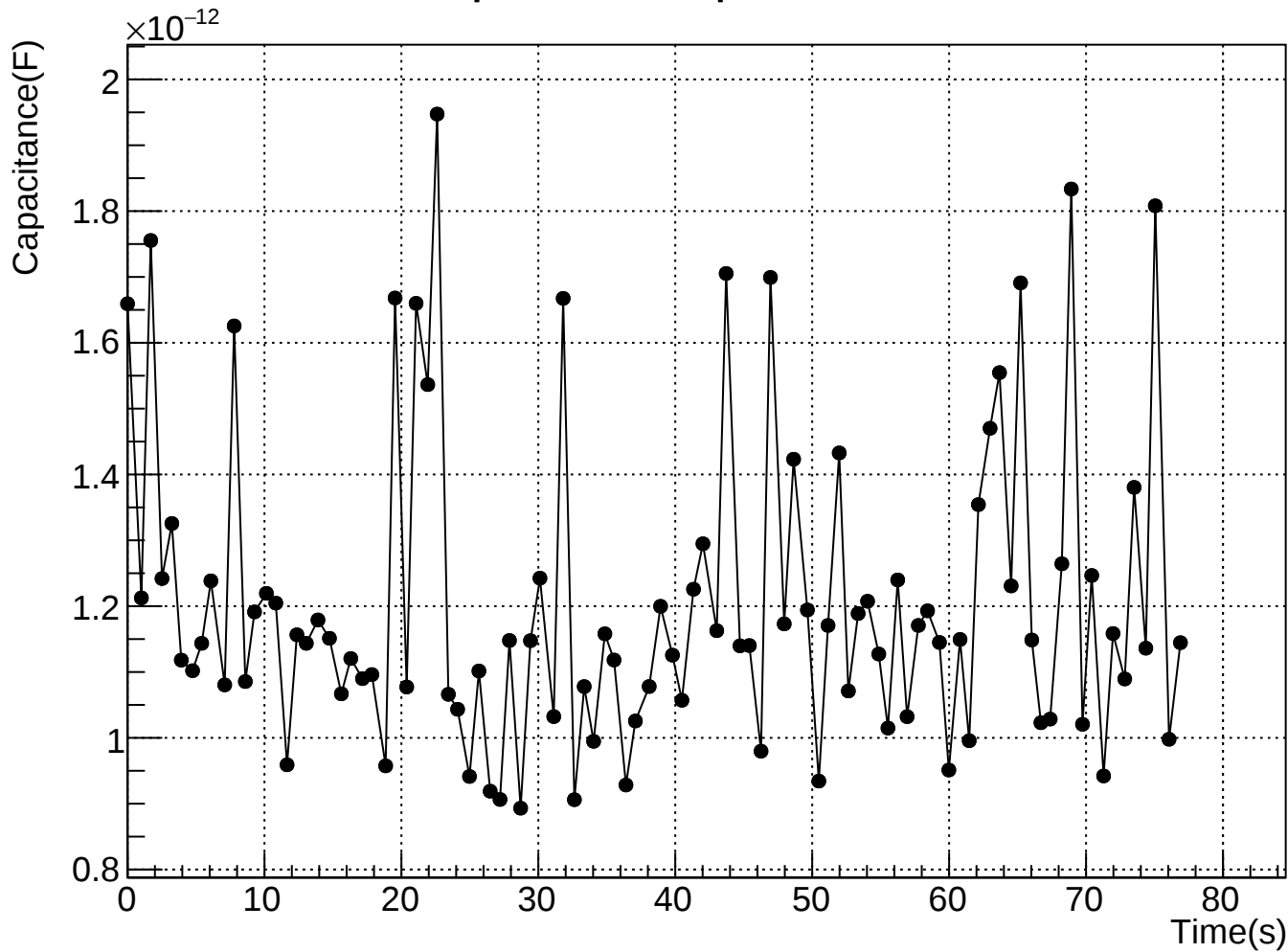
Sample101 Chip5 Channel085



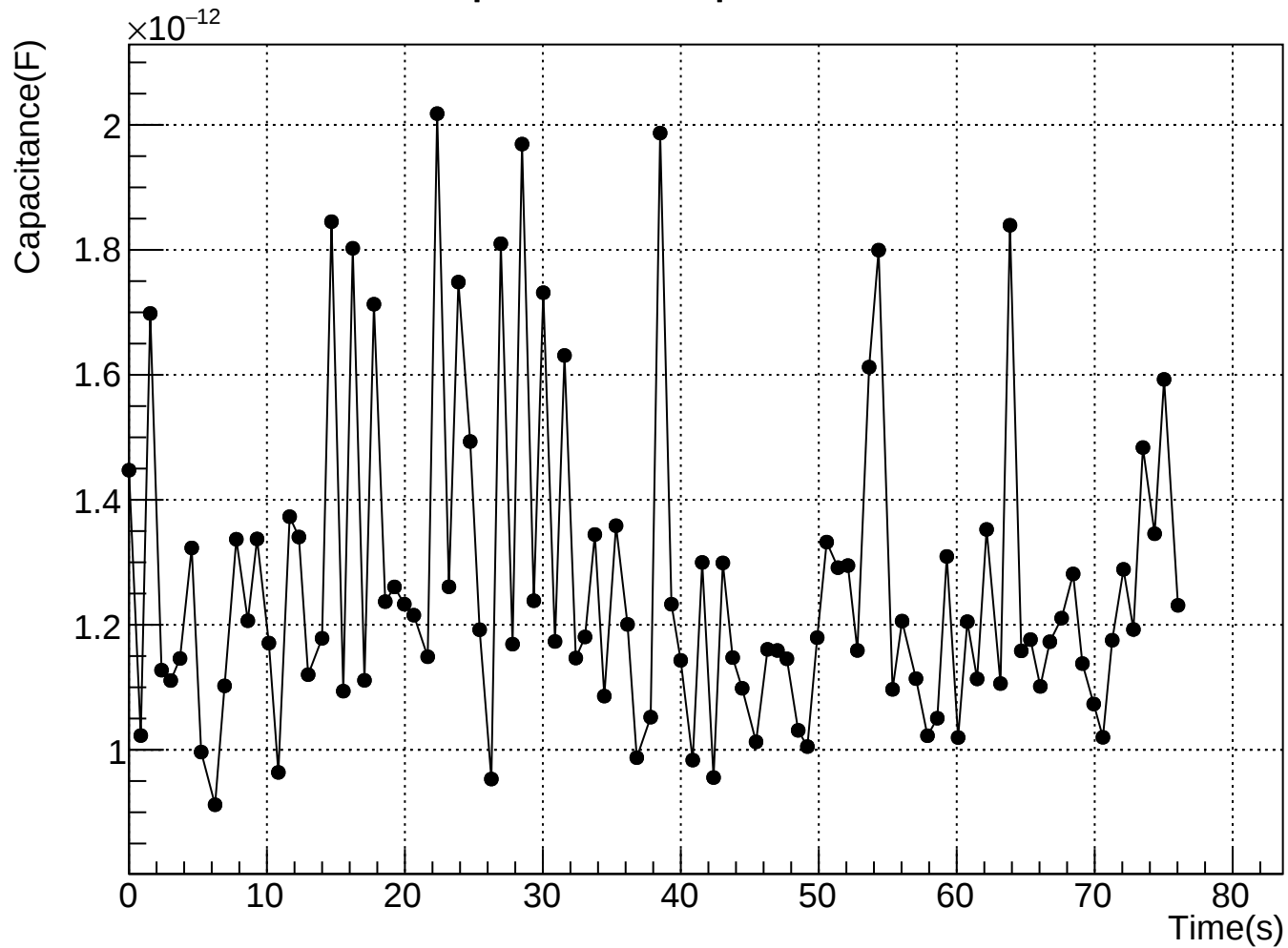
Sample101 Chip5 Channel086



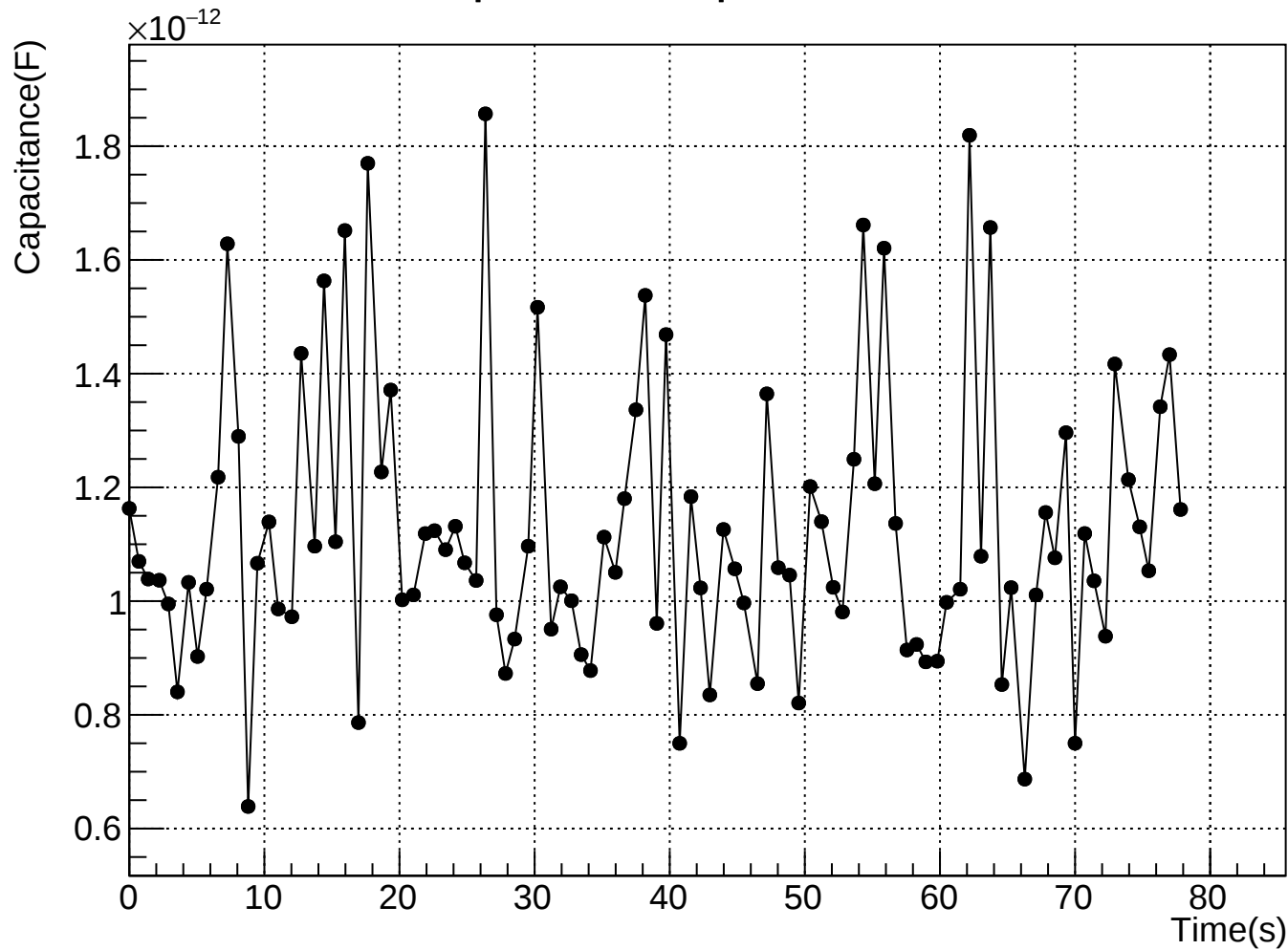
Sample101 Chip5 Channel087



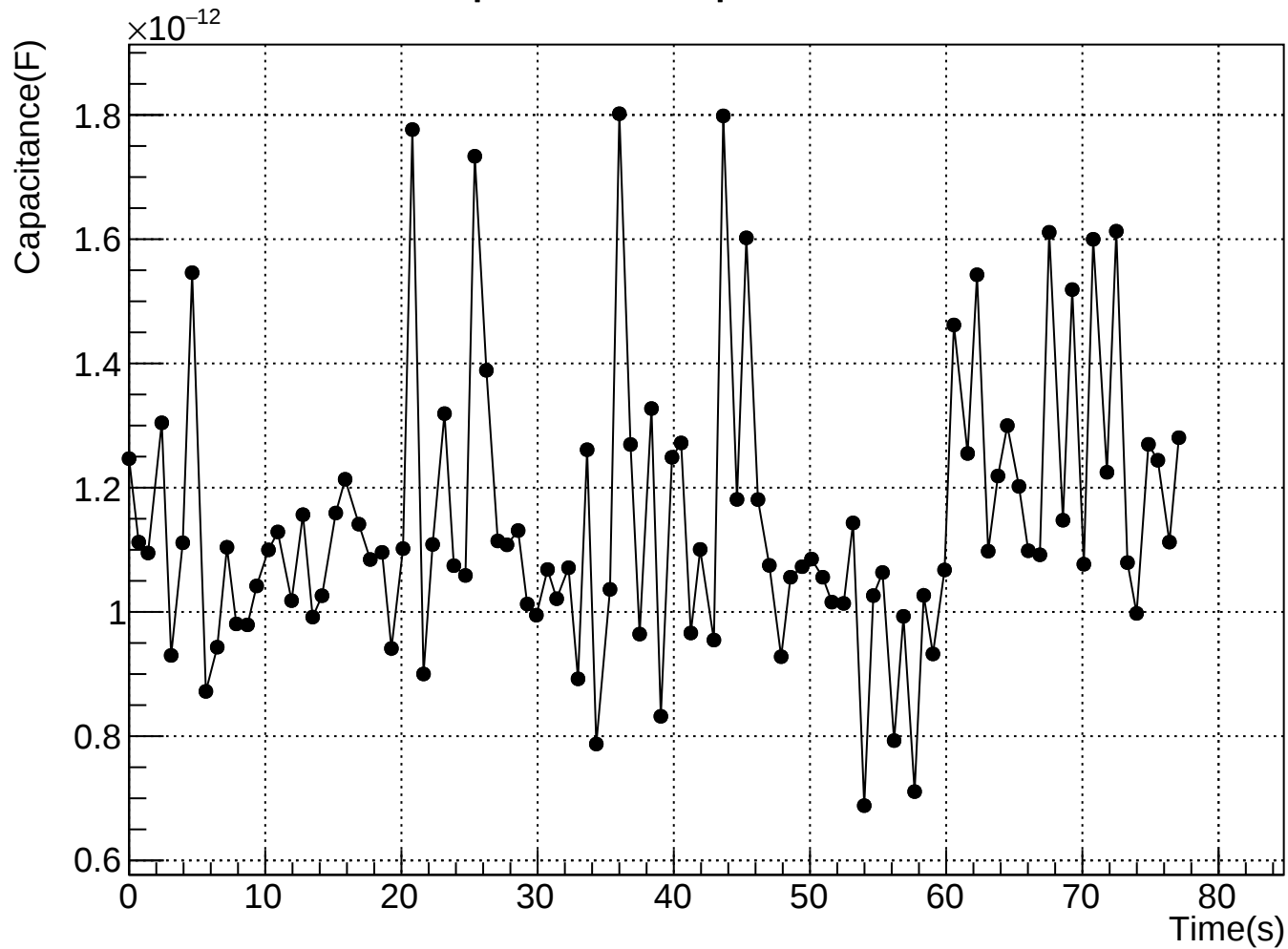
Sample101 Chip5 Channel088



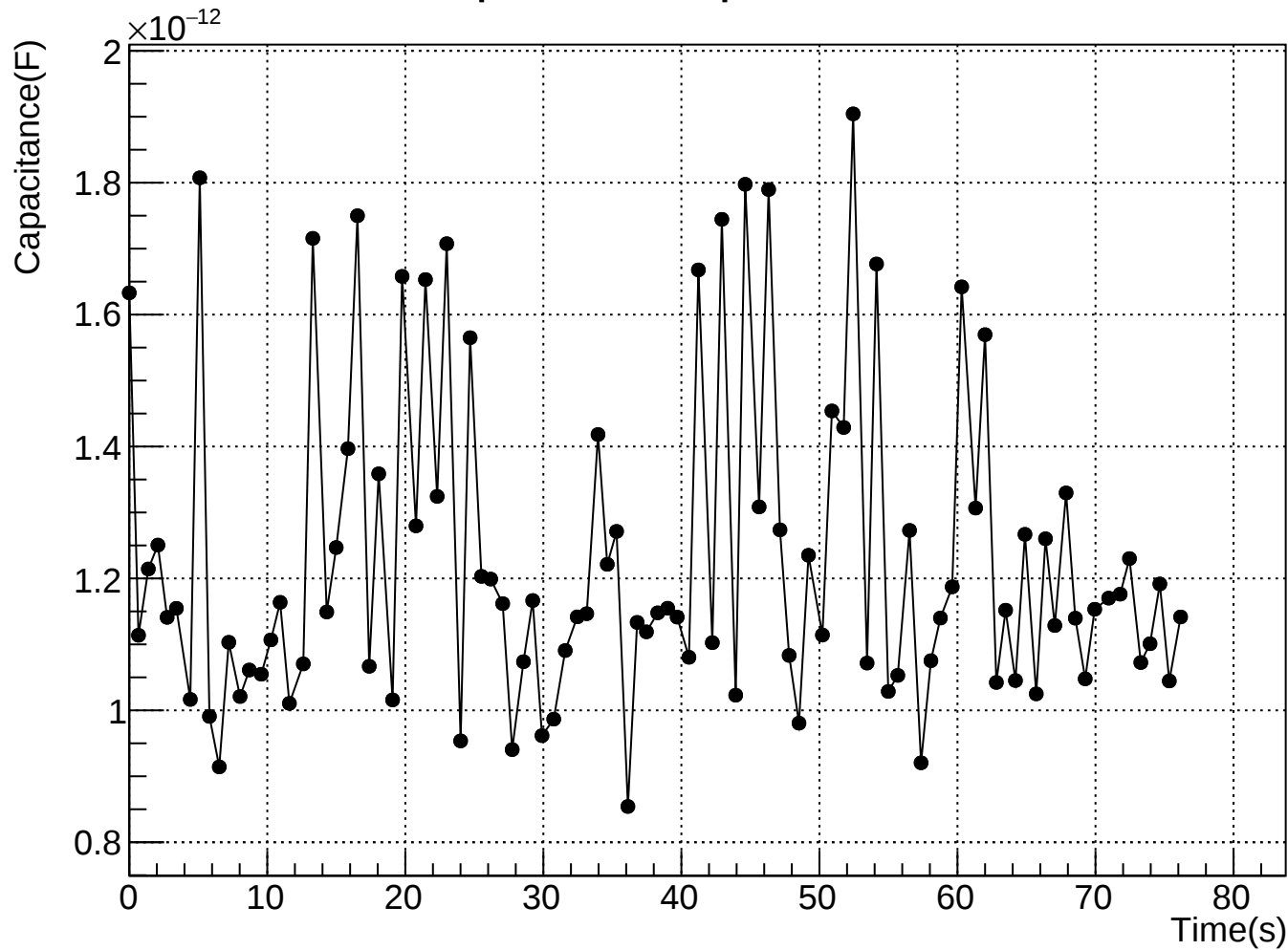
Sample101 Chip5 Channel089



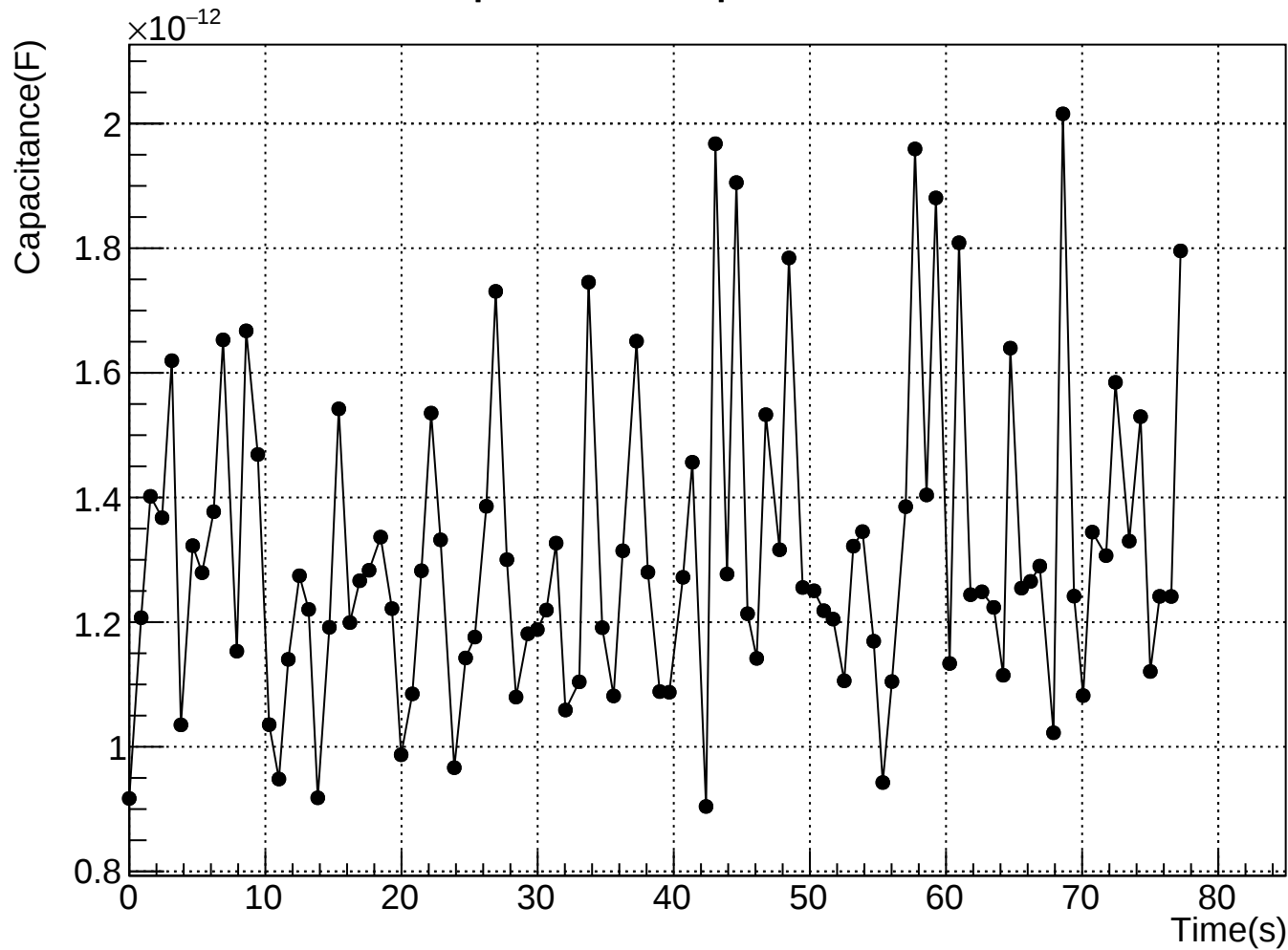
Sample101 Chip5 Channel090



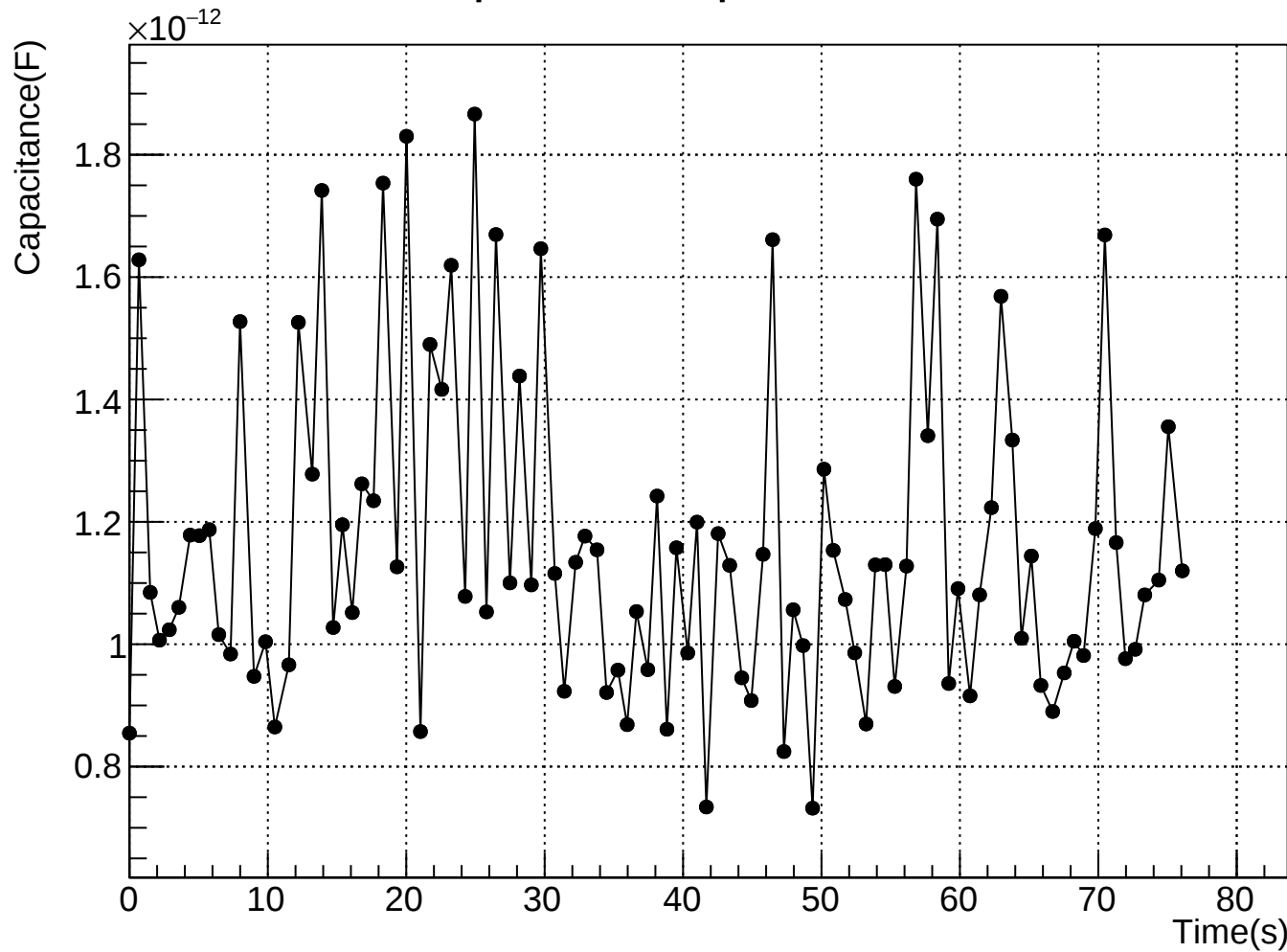
Sample101 Chip5 Channel091



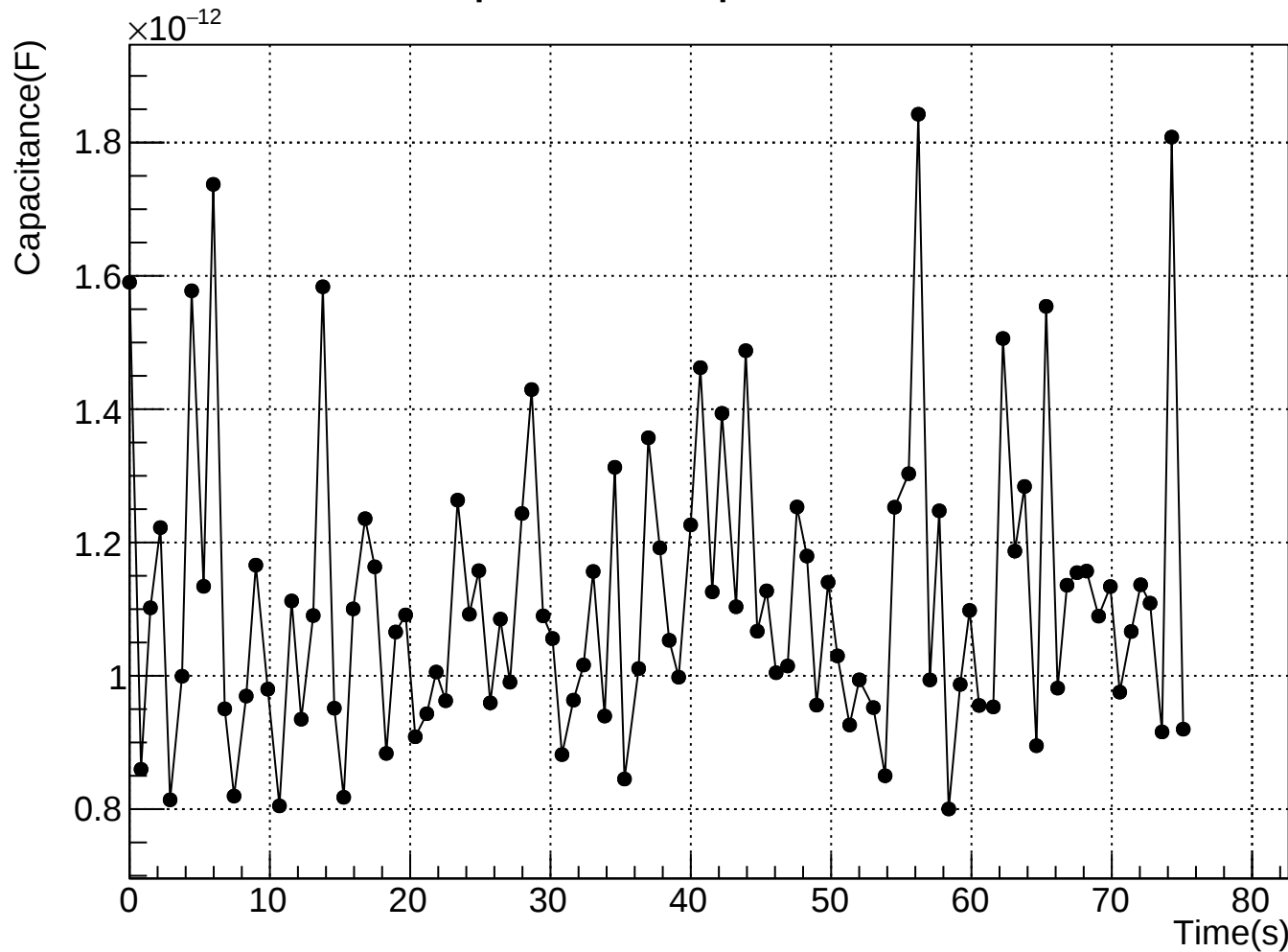
Sample101 Chip5 Channel092



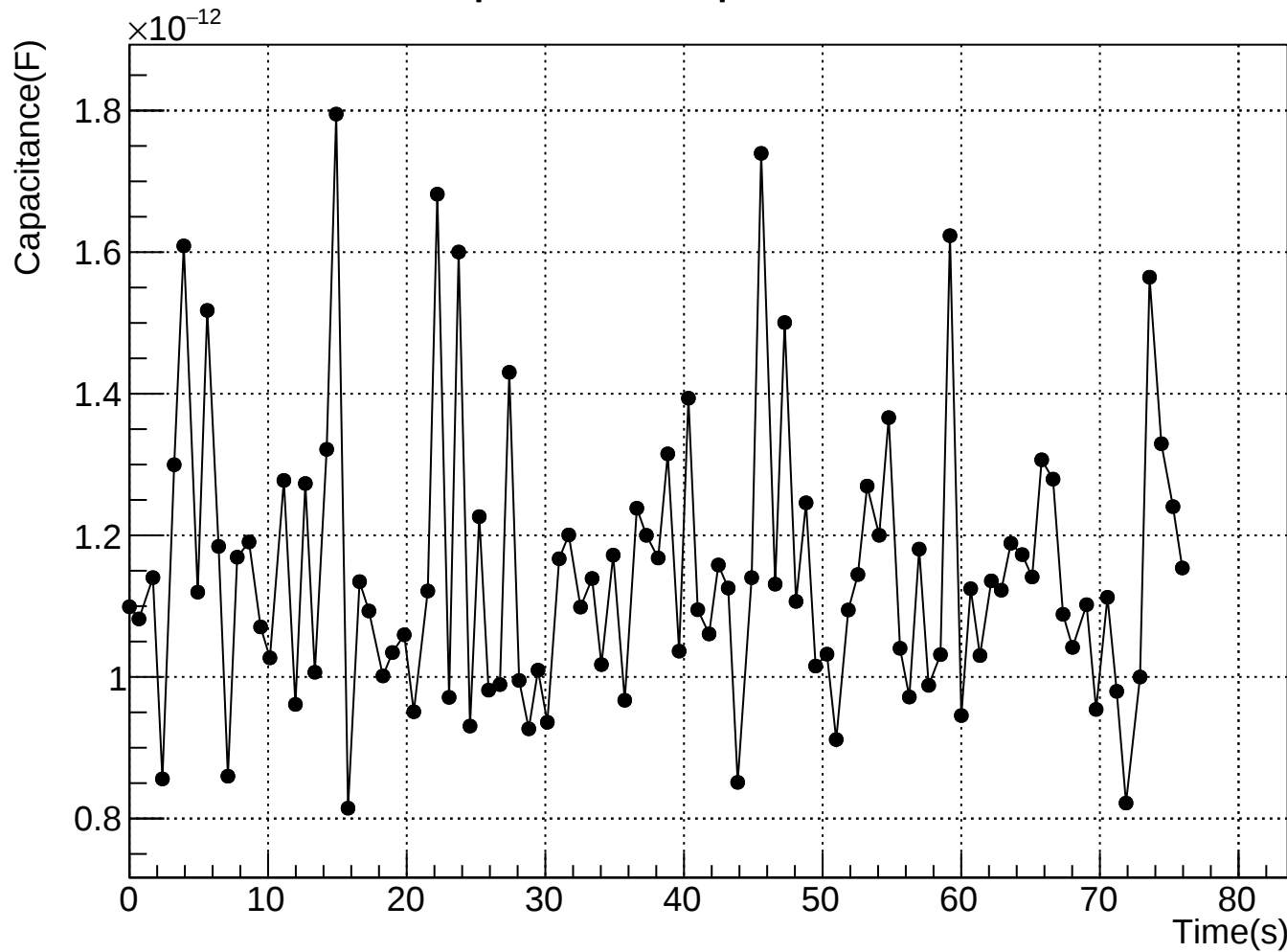
Sample101 Chip5 Channel093



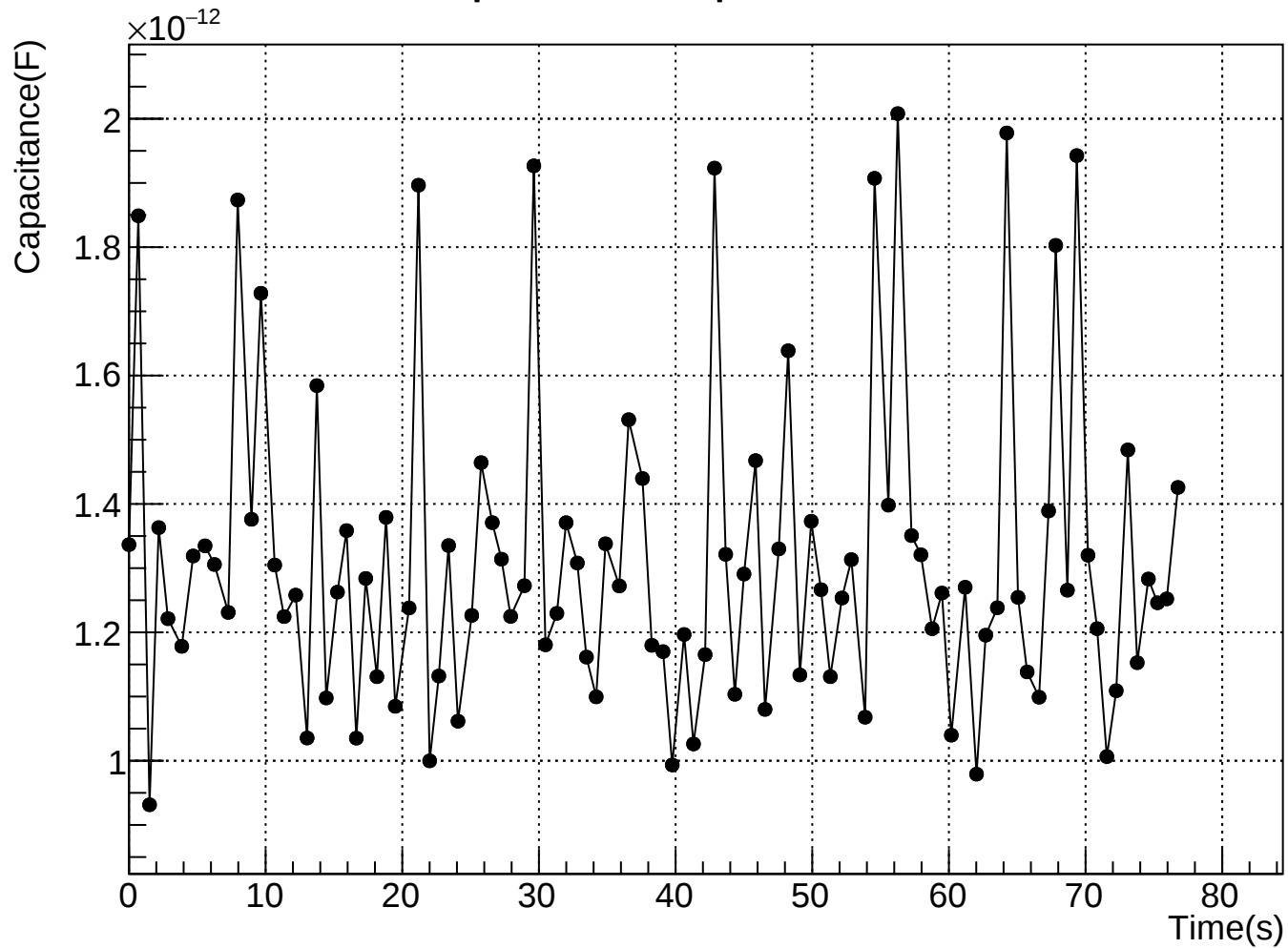
Sample101 Chip5 Channel094



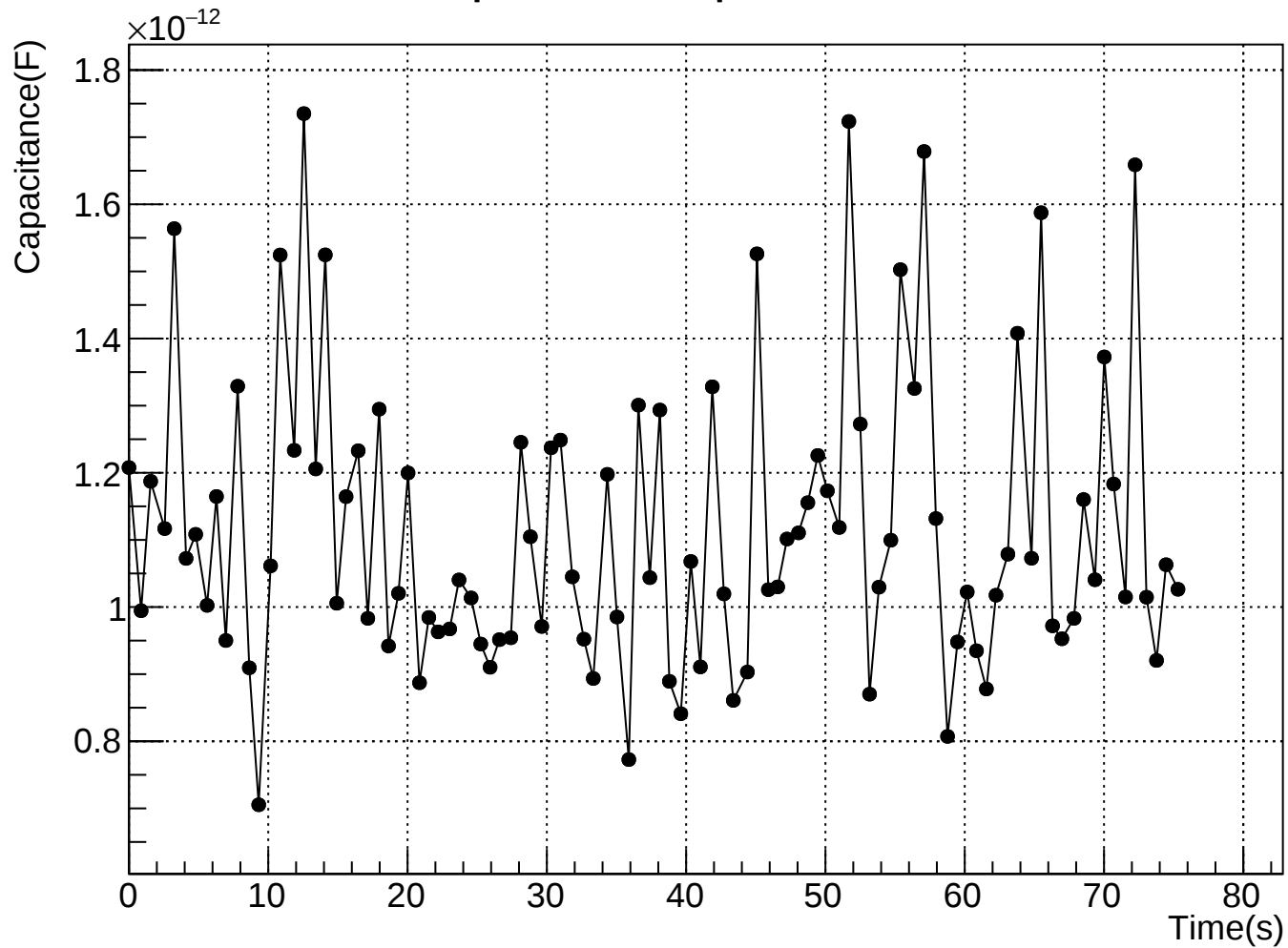
Sample101 Chip5 Channel095



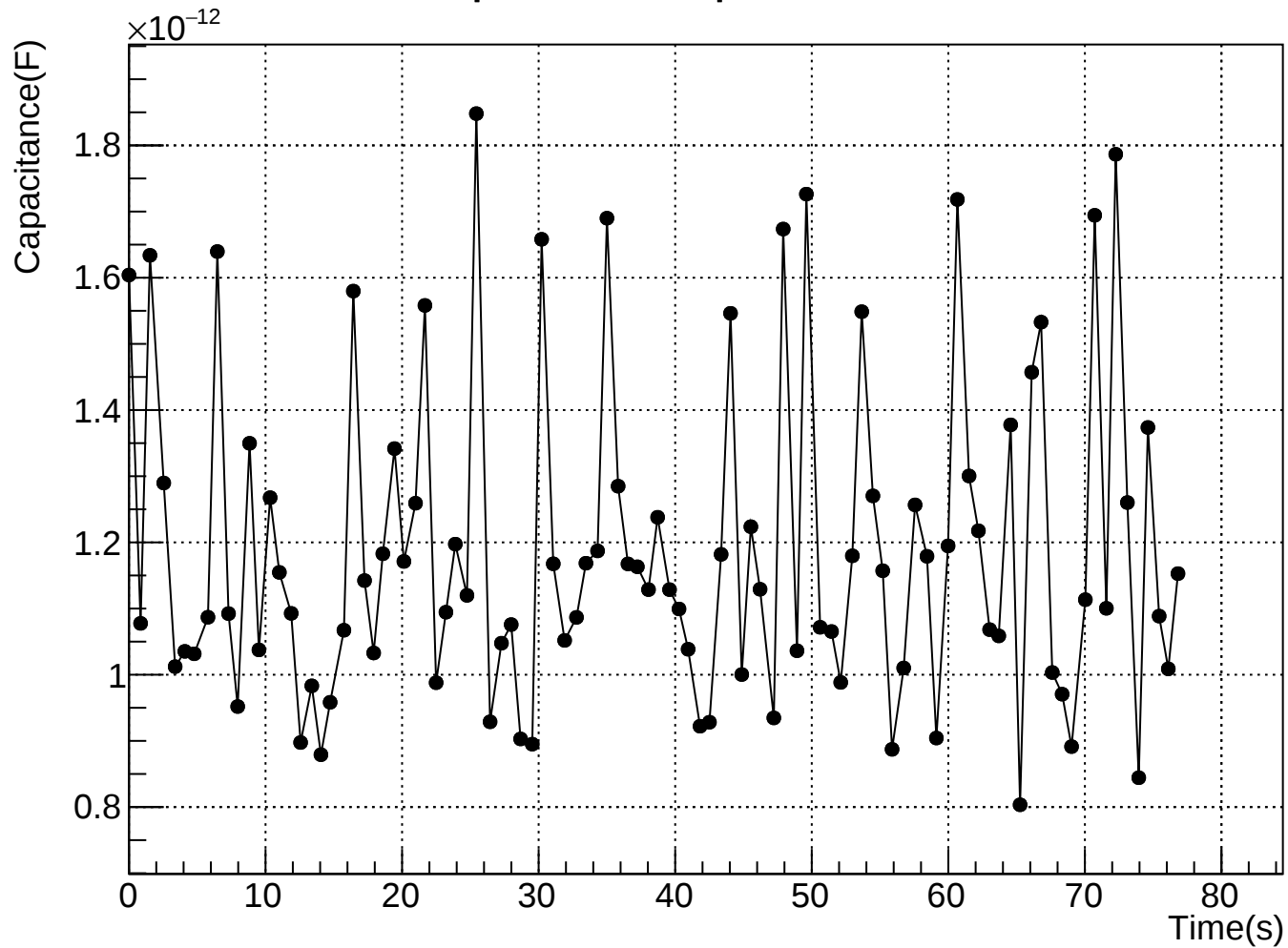
Sample101 Chip5 Channel096



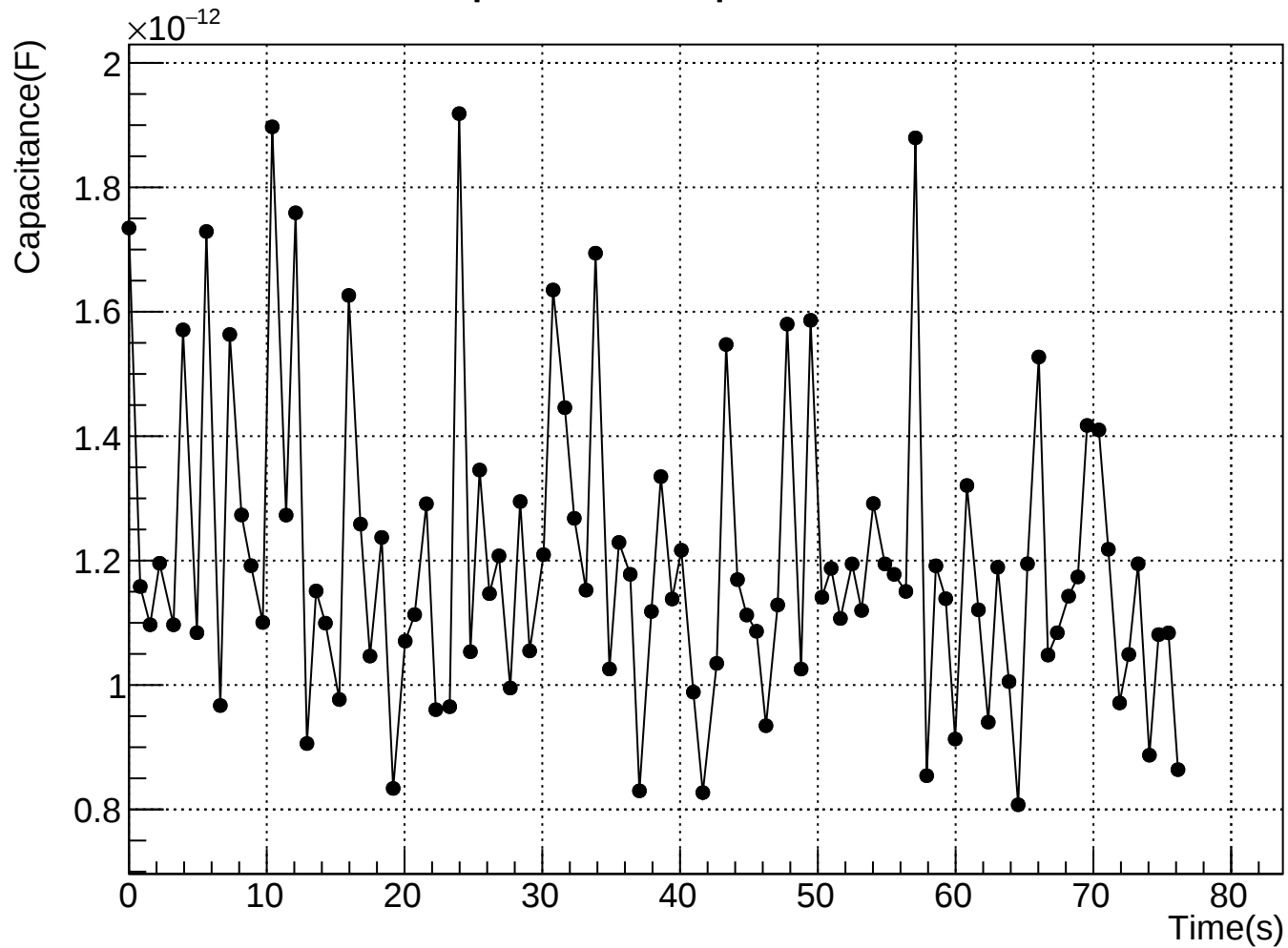
Sample101 Chip5 Channel097



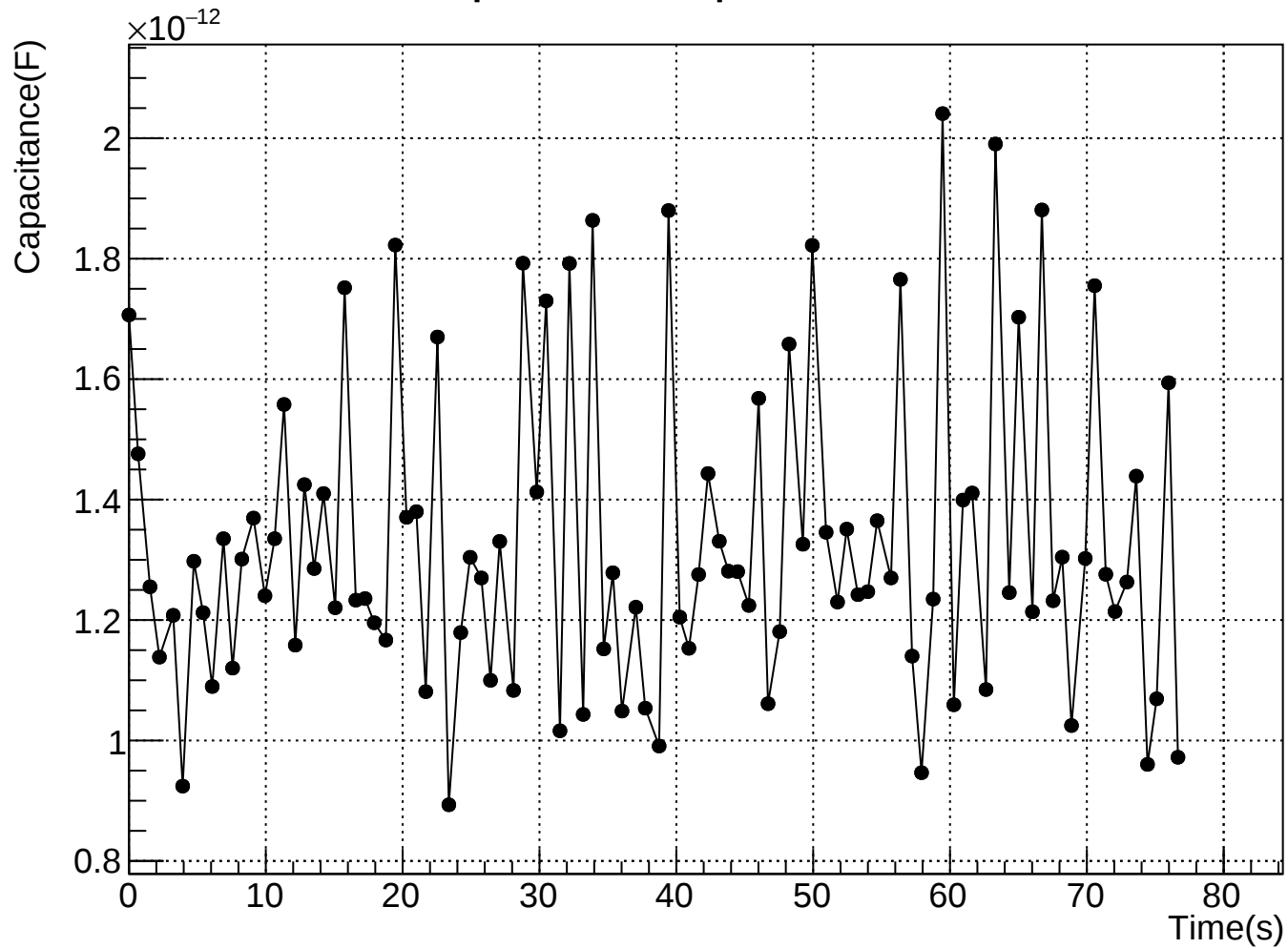
Sample101 Chip5 Channel098



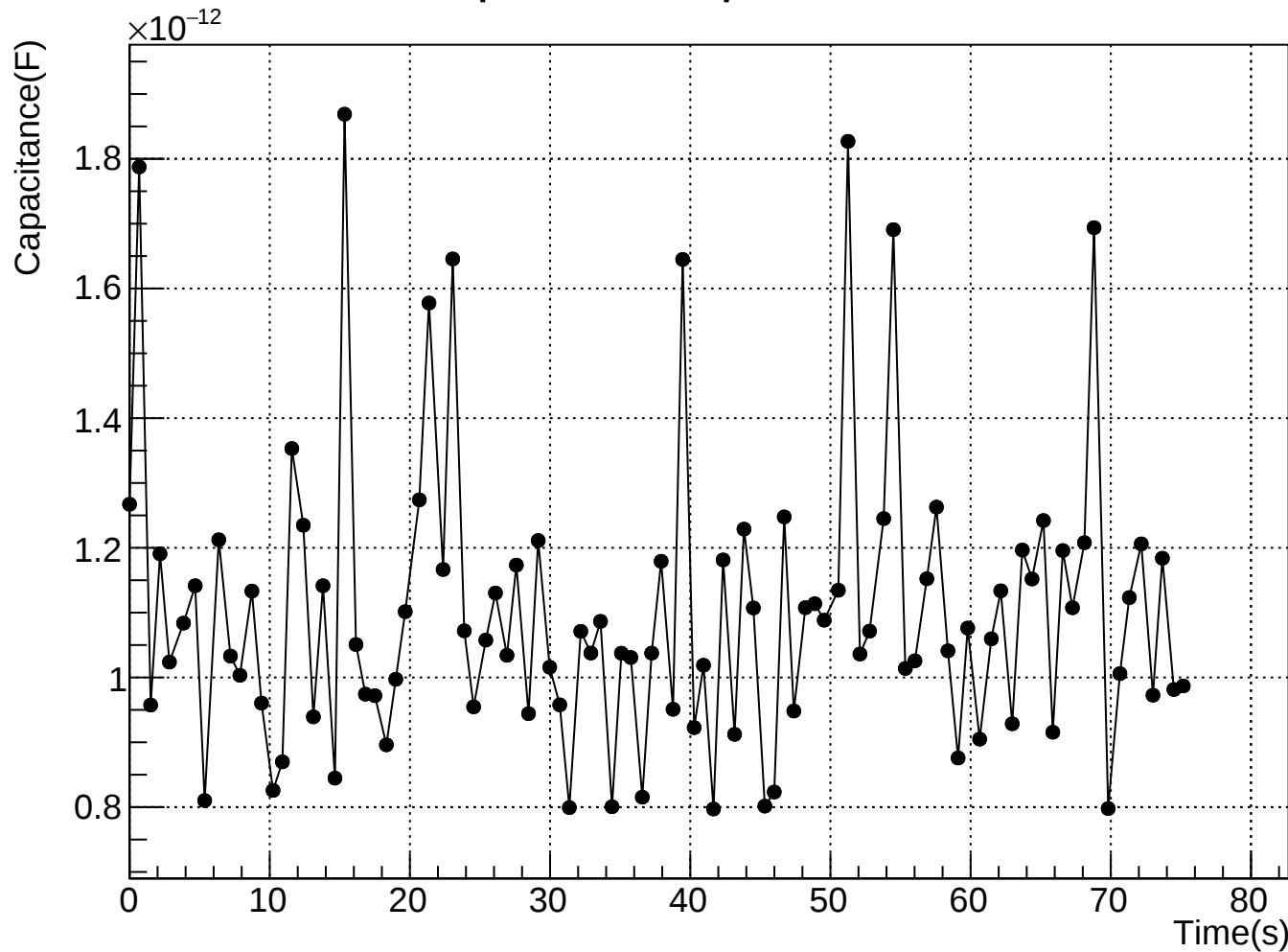
Sample101 Chip5 Channel099



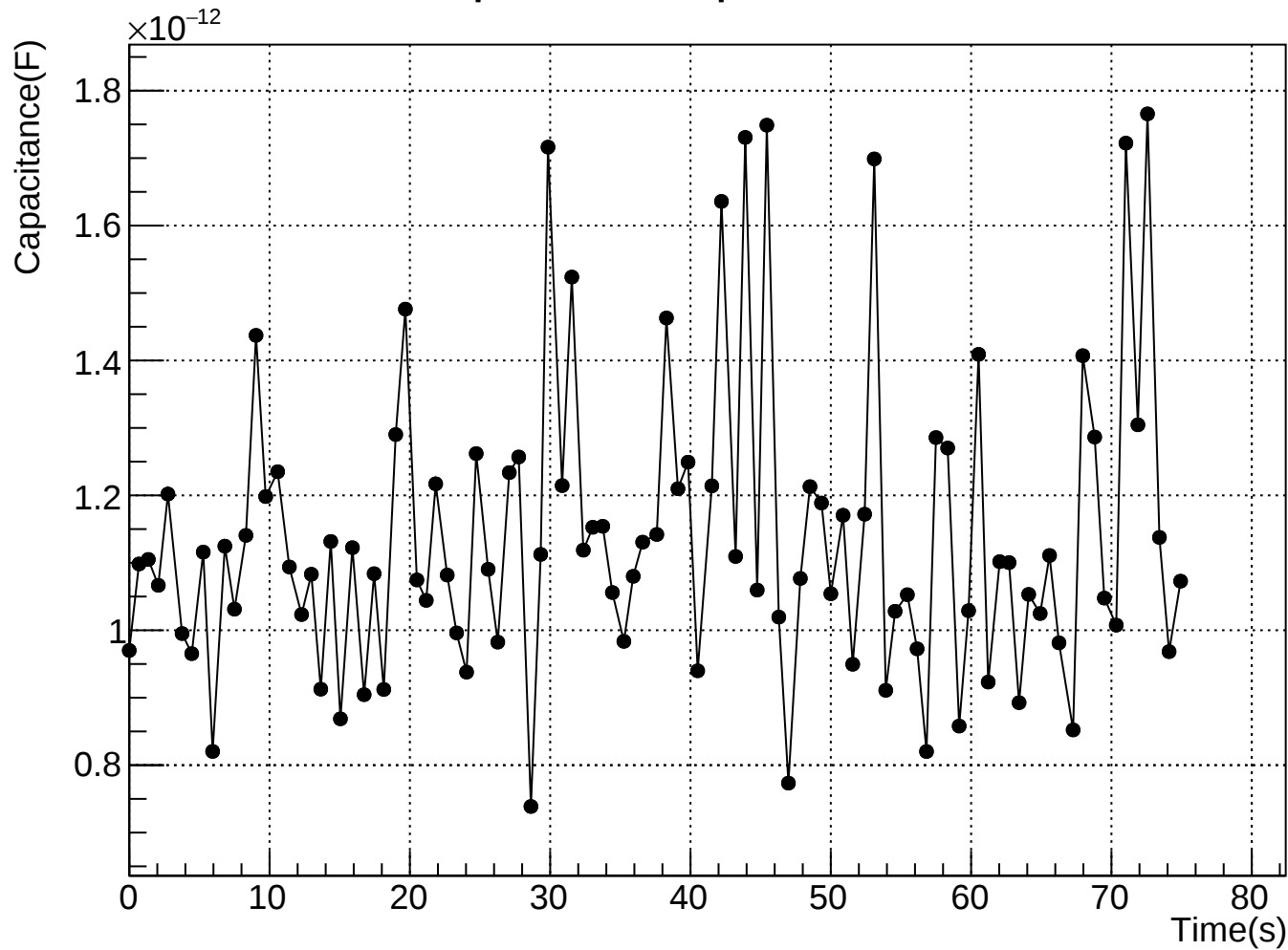
Sample101 Chip5 Channel100



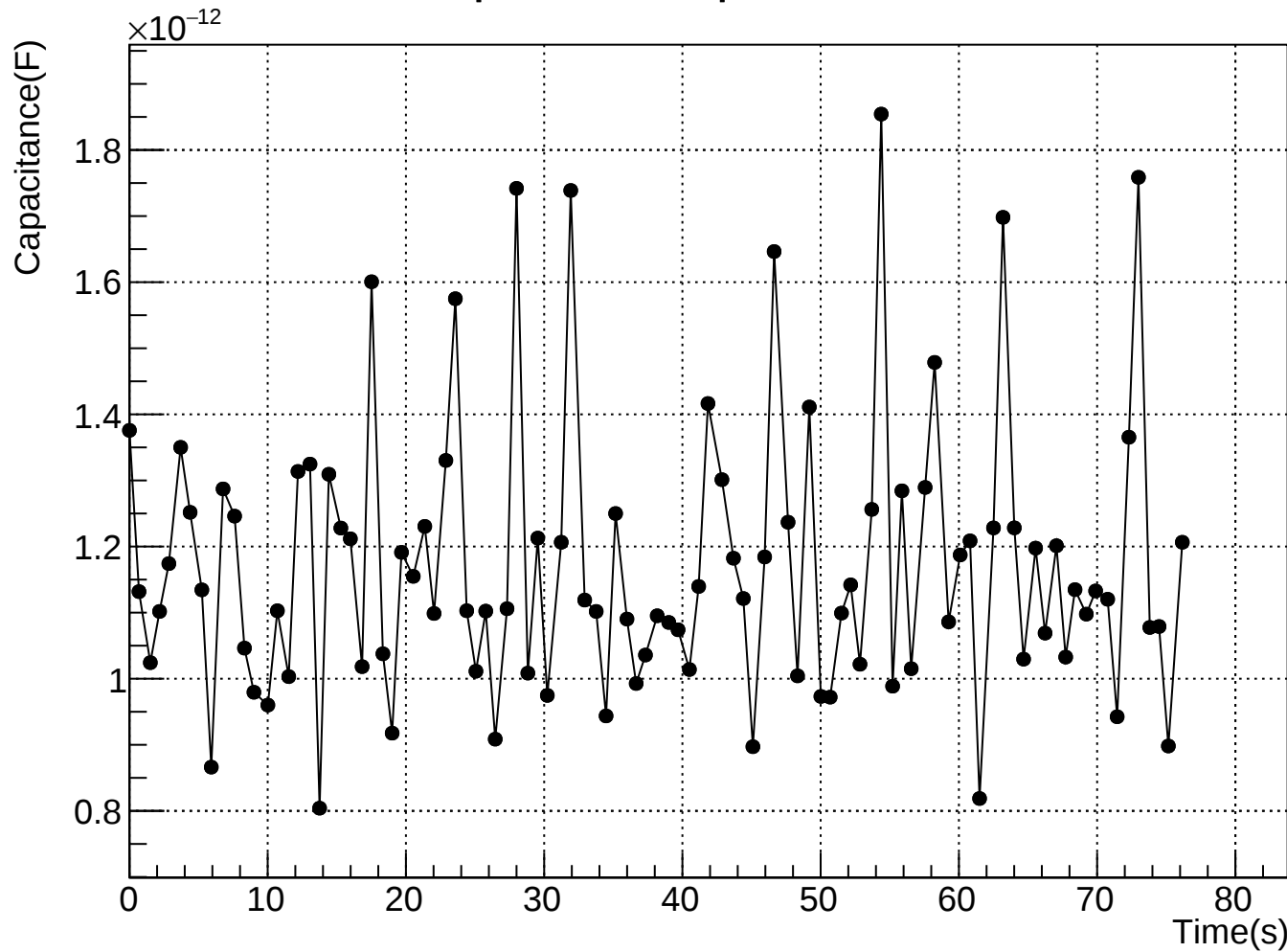
Sample101 Chip5 Channel101



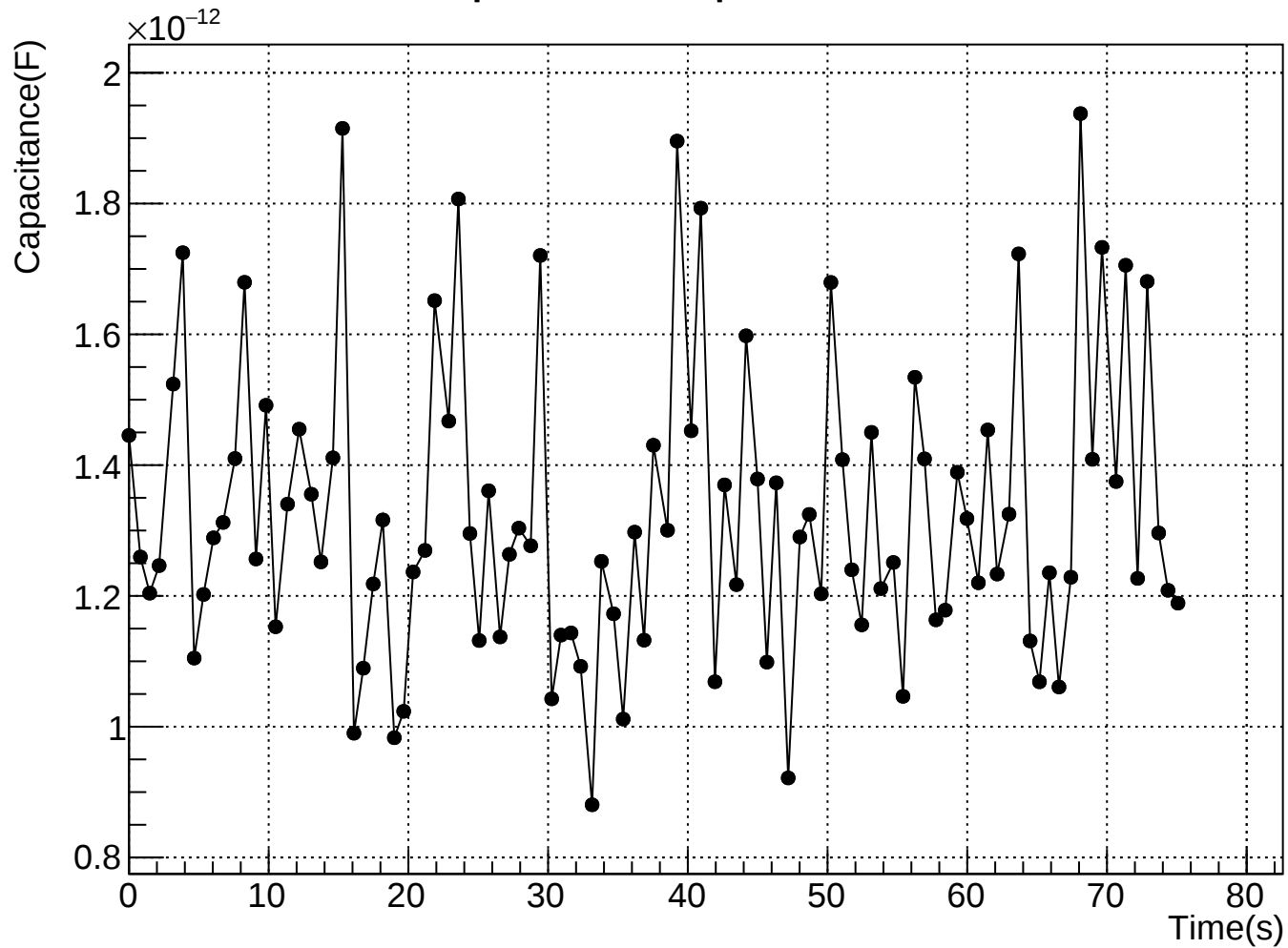
Sample101 Chip5 Channel102



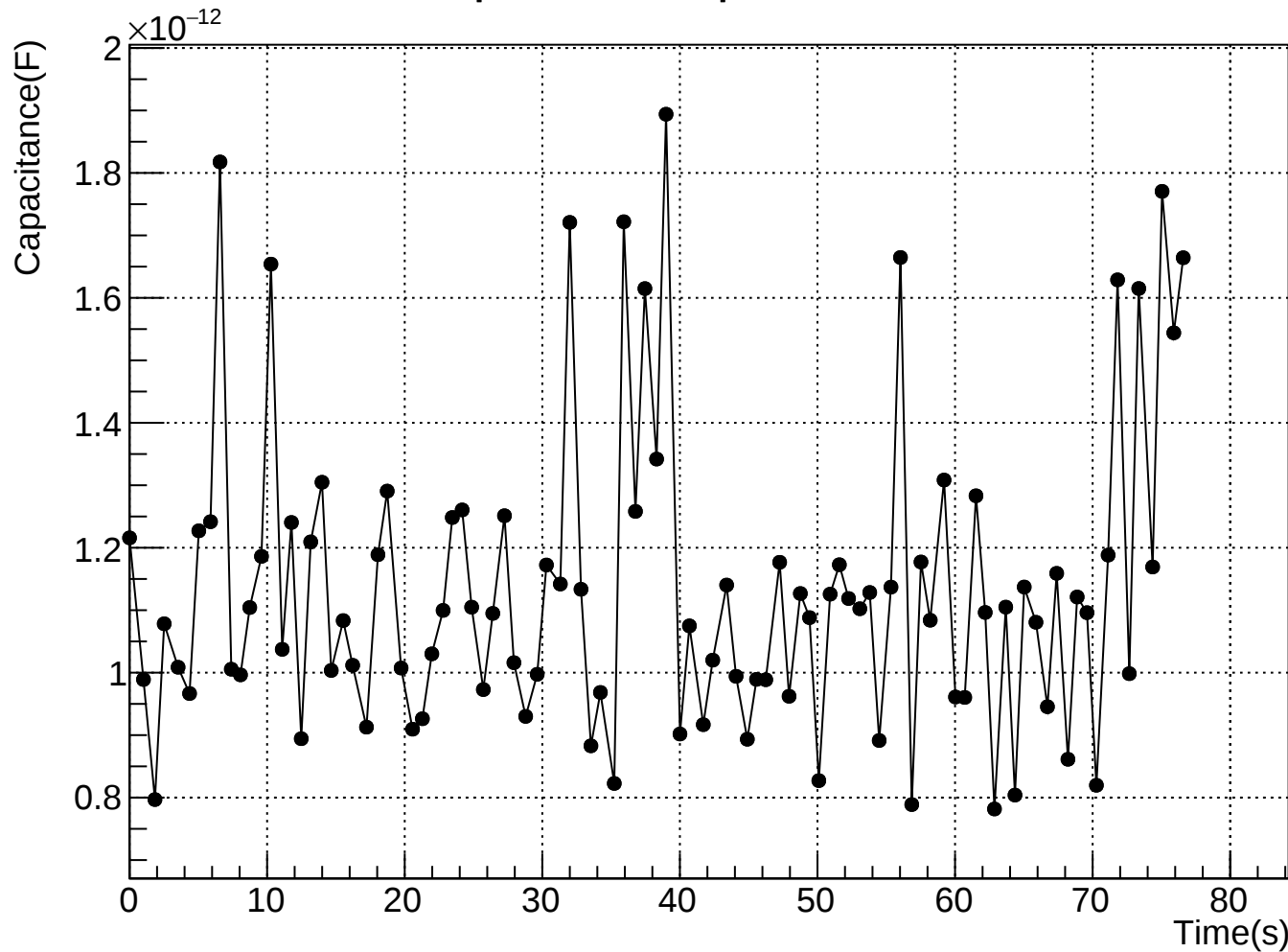
Sample101 Chip5 Channel103



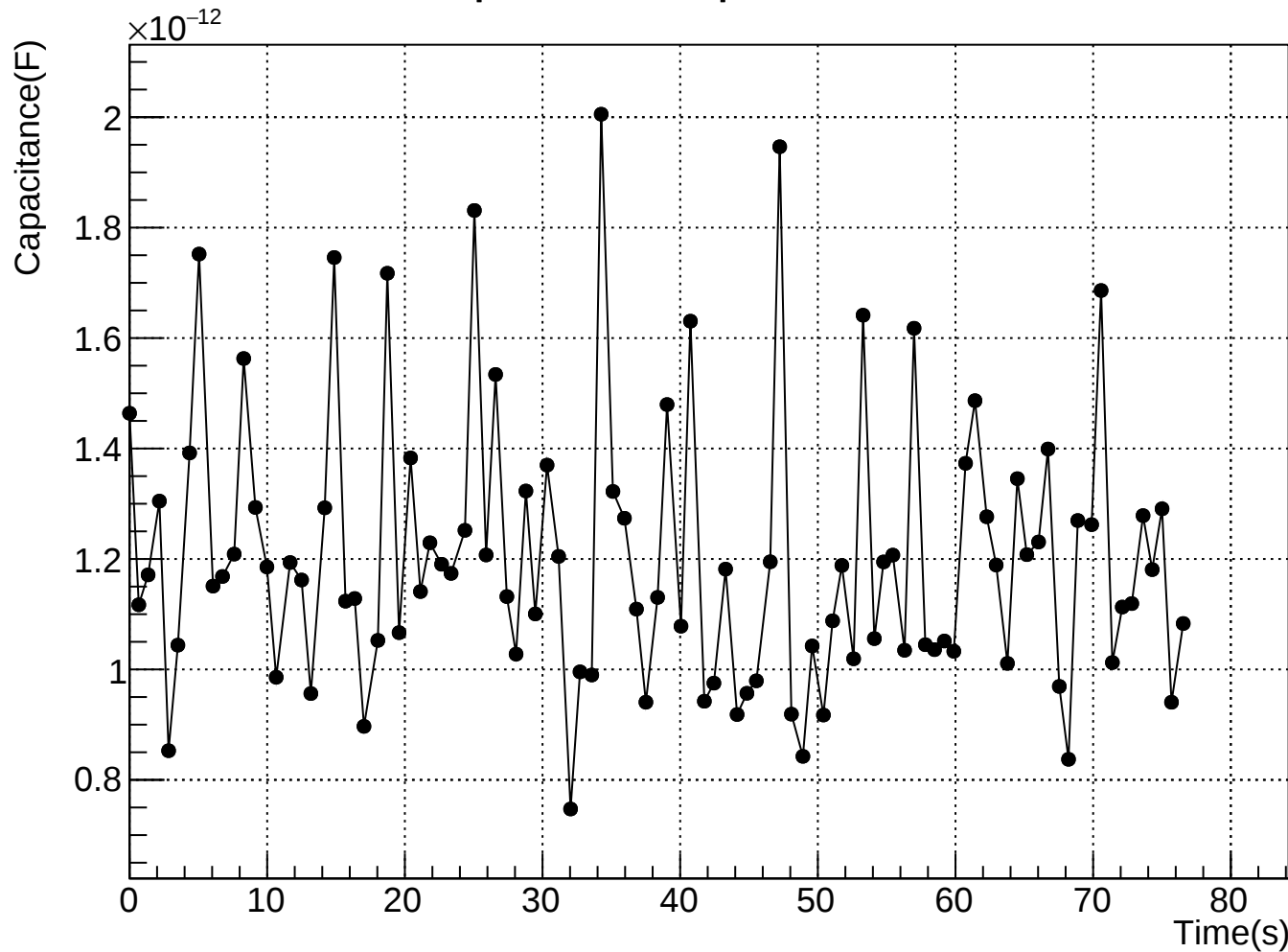
Sample101 Chip5 Channel104



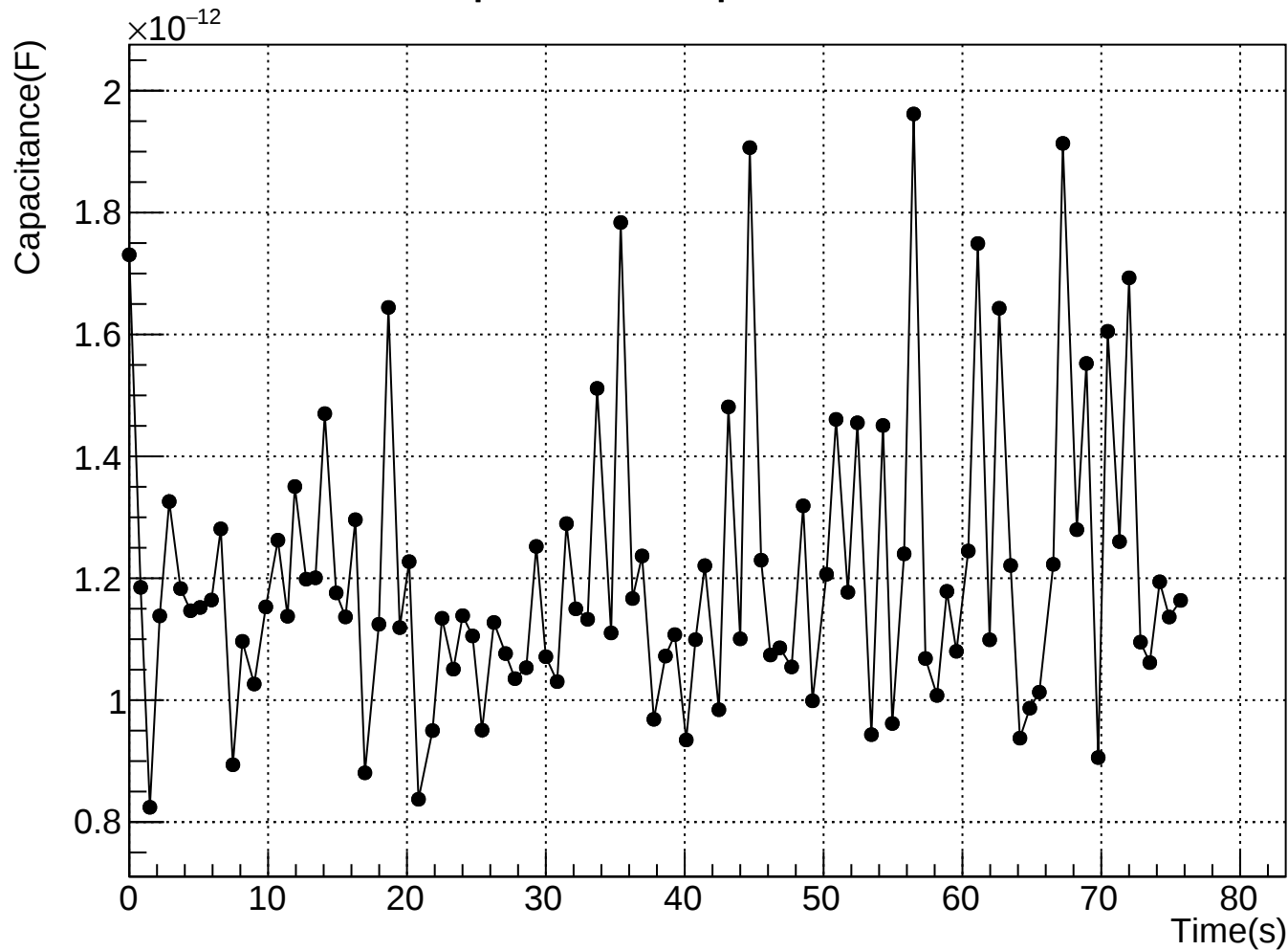
Sample101 Chip5 Channel105



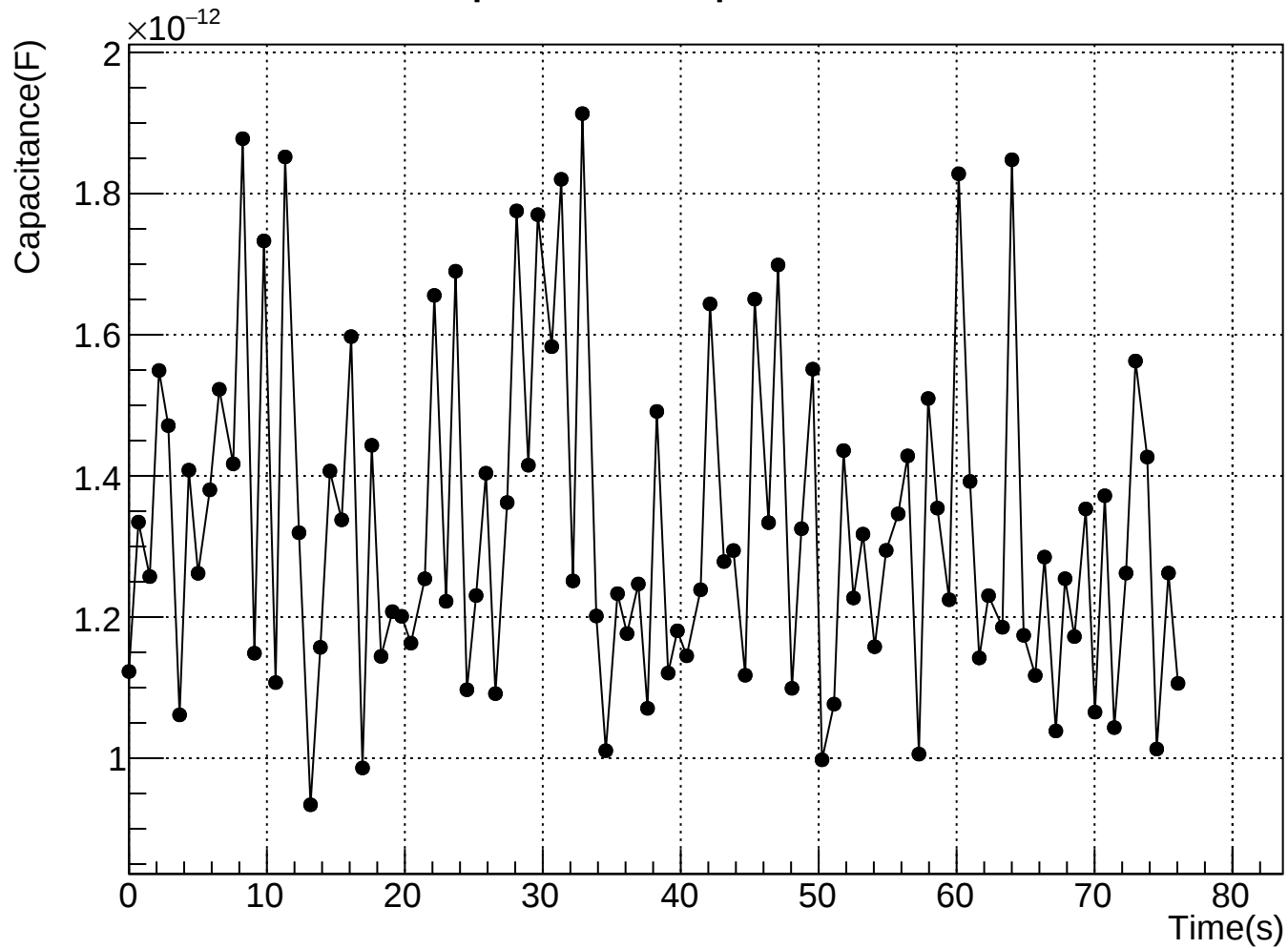
Sample101 Chip5 Channel106



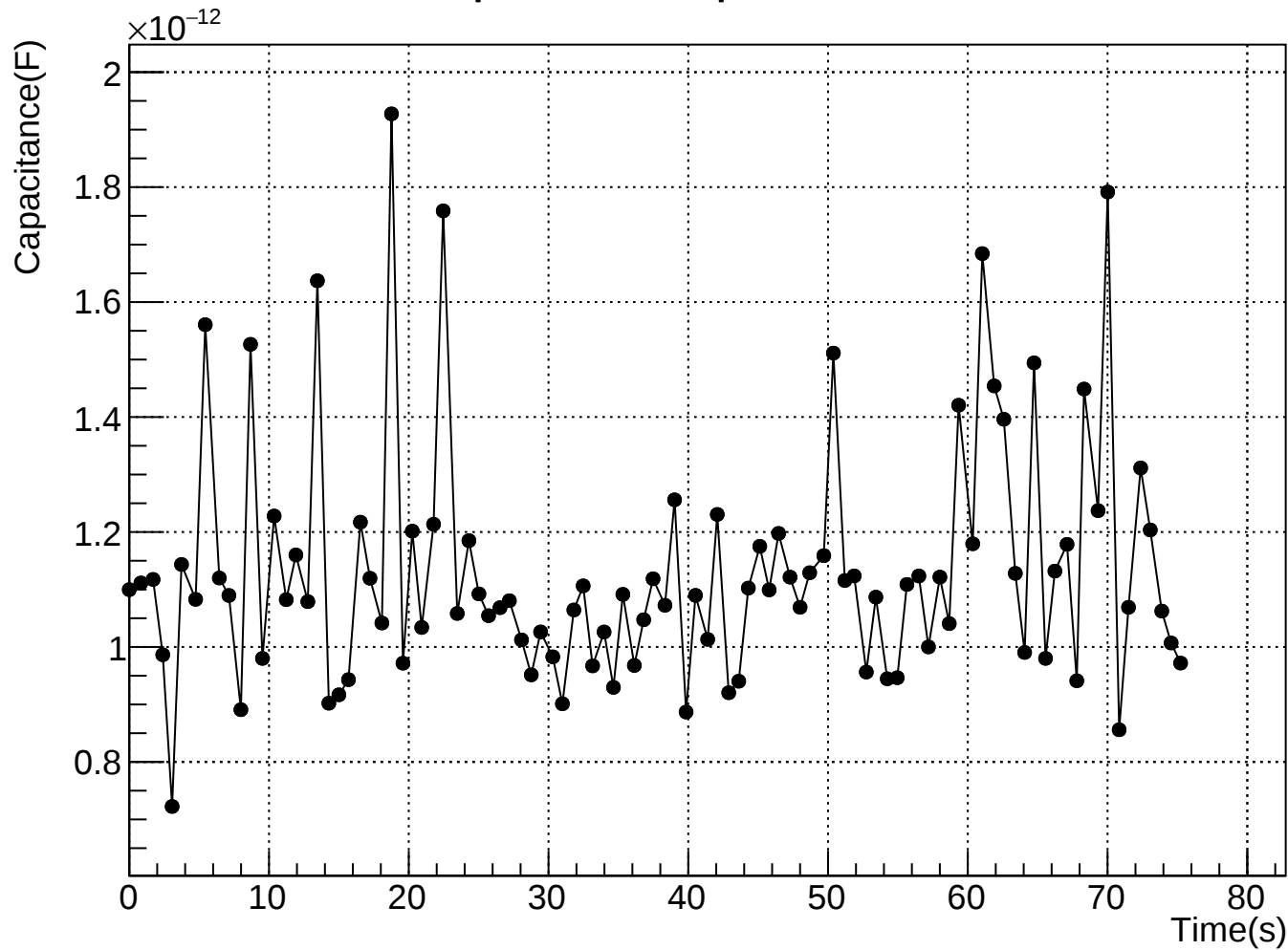
Sample101 Chip5 Channel107



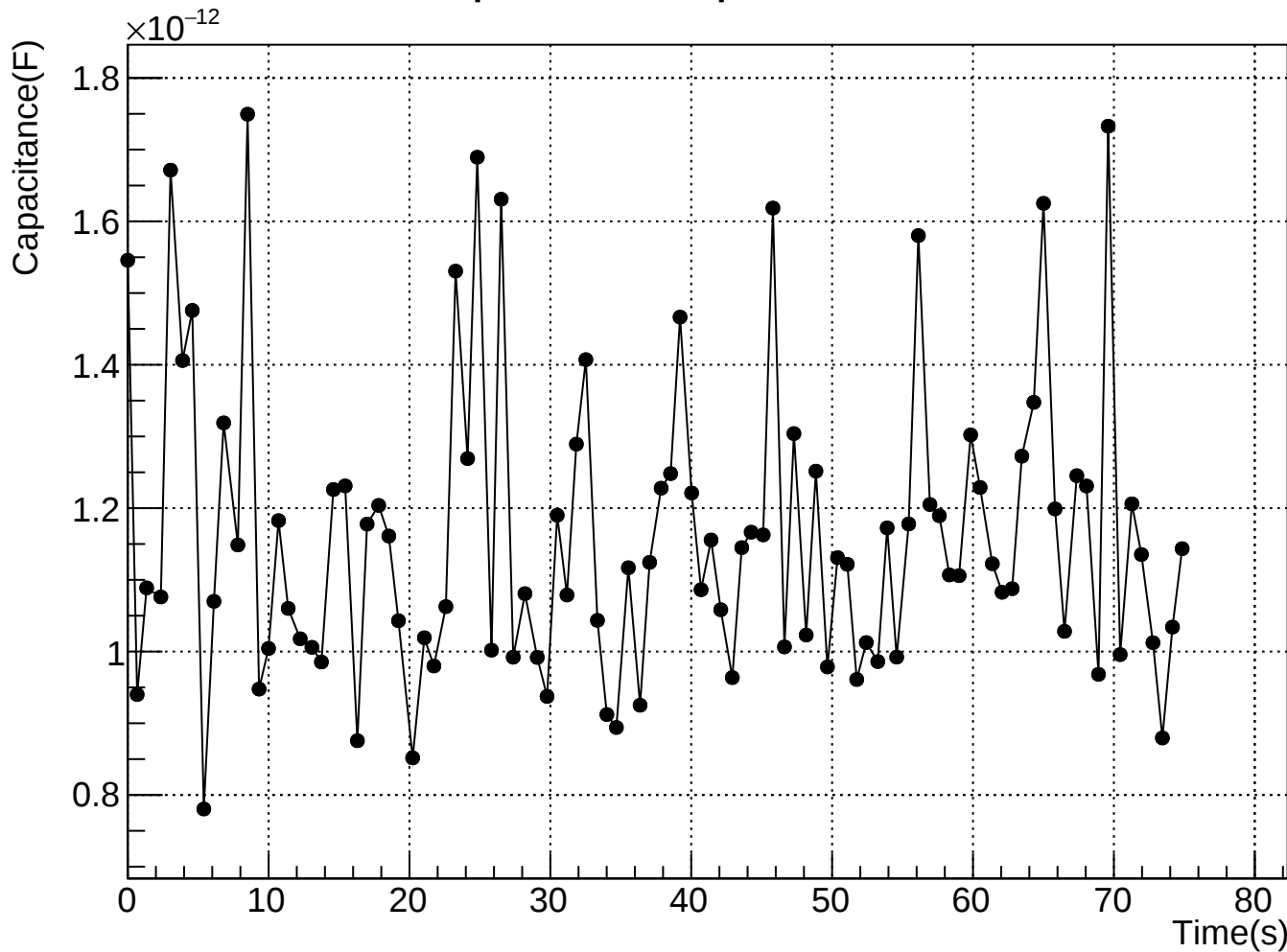
Sample101 Chip5 Channel108



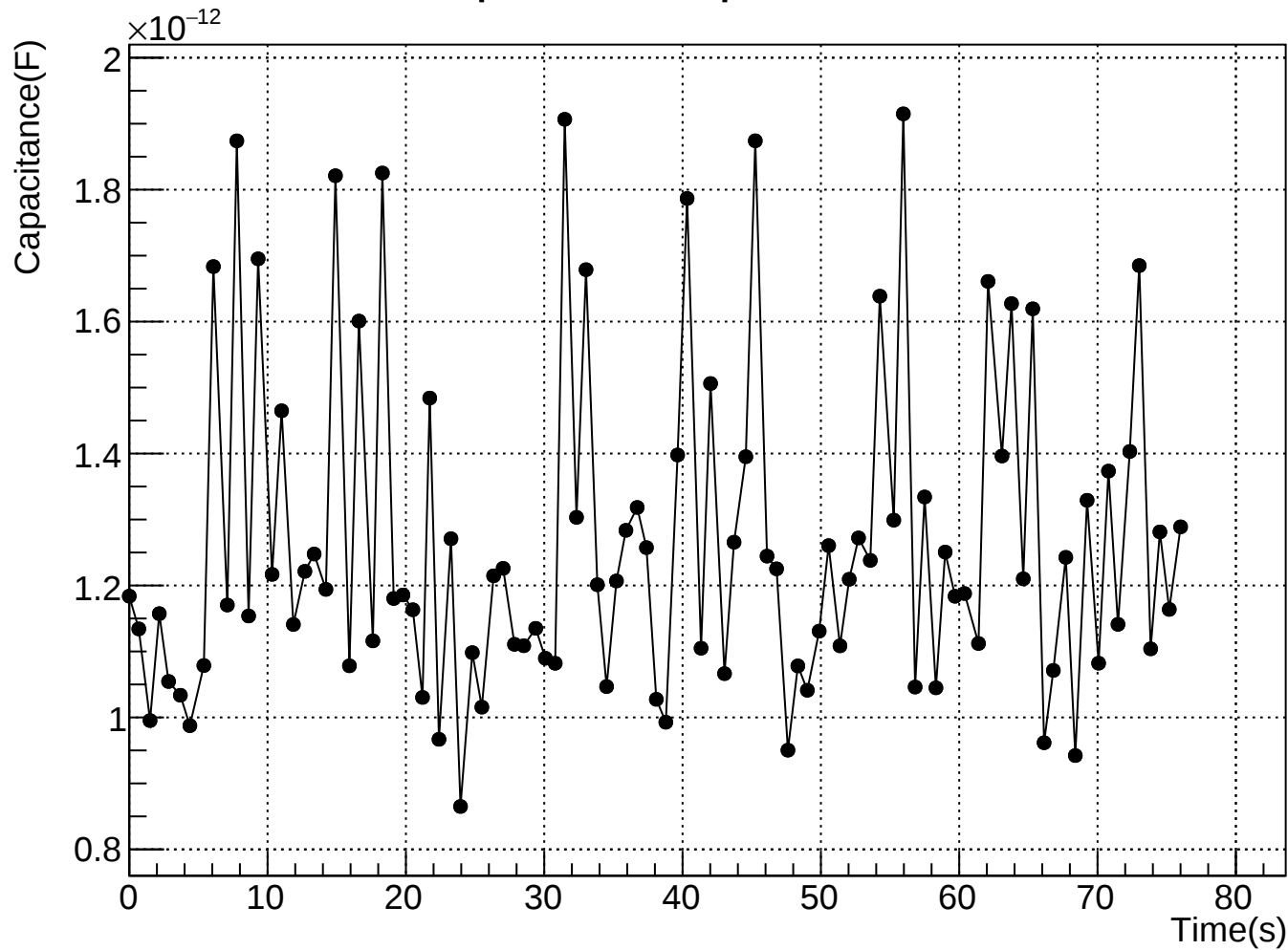
Sample101 Chip5 Channel109



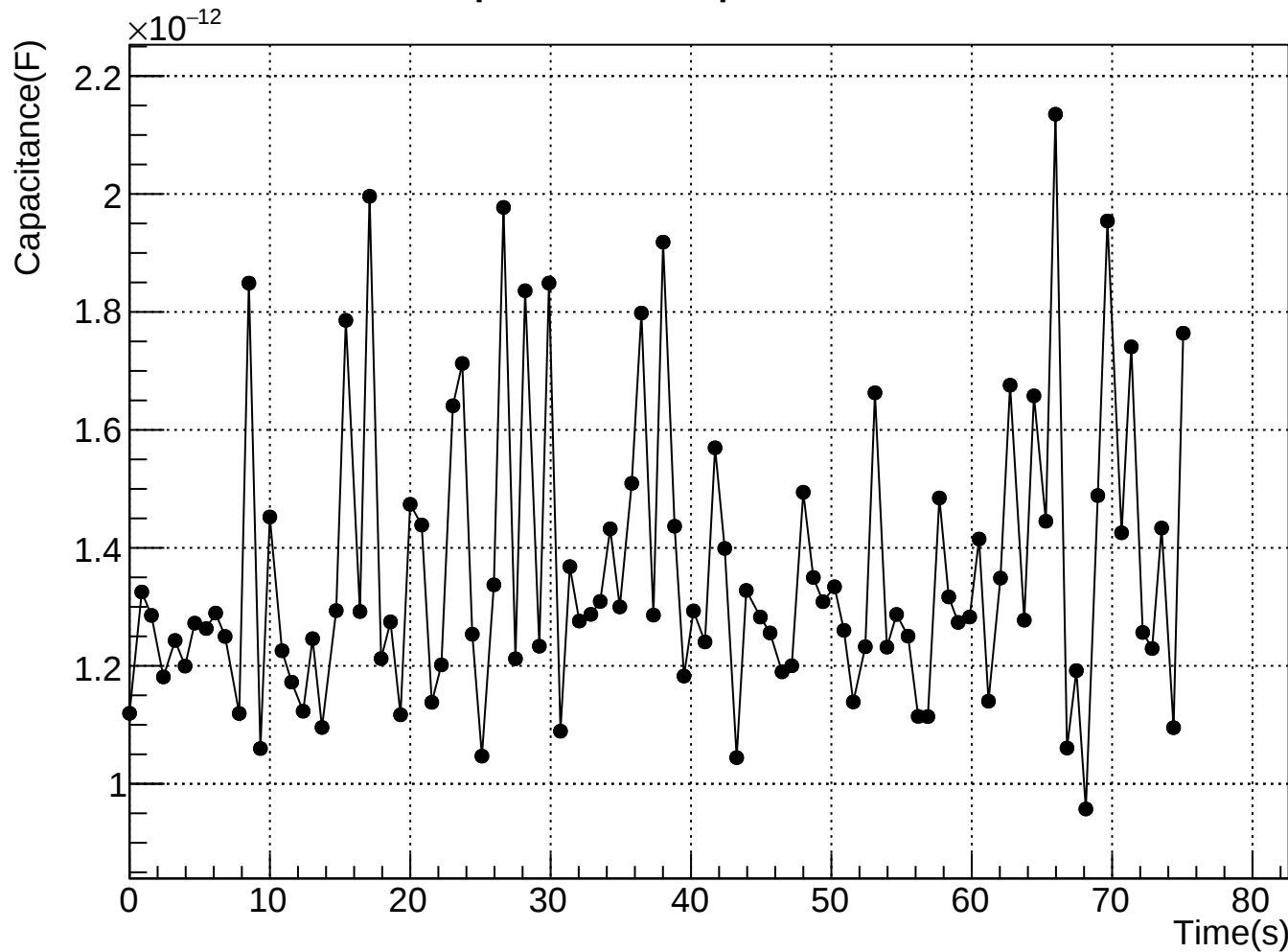
Sample101 Chip5 Channel110



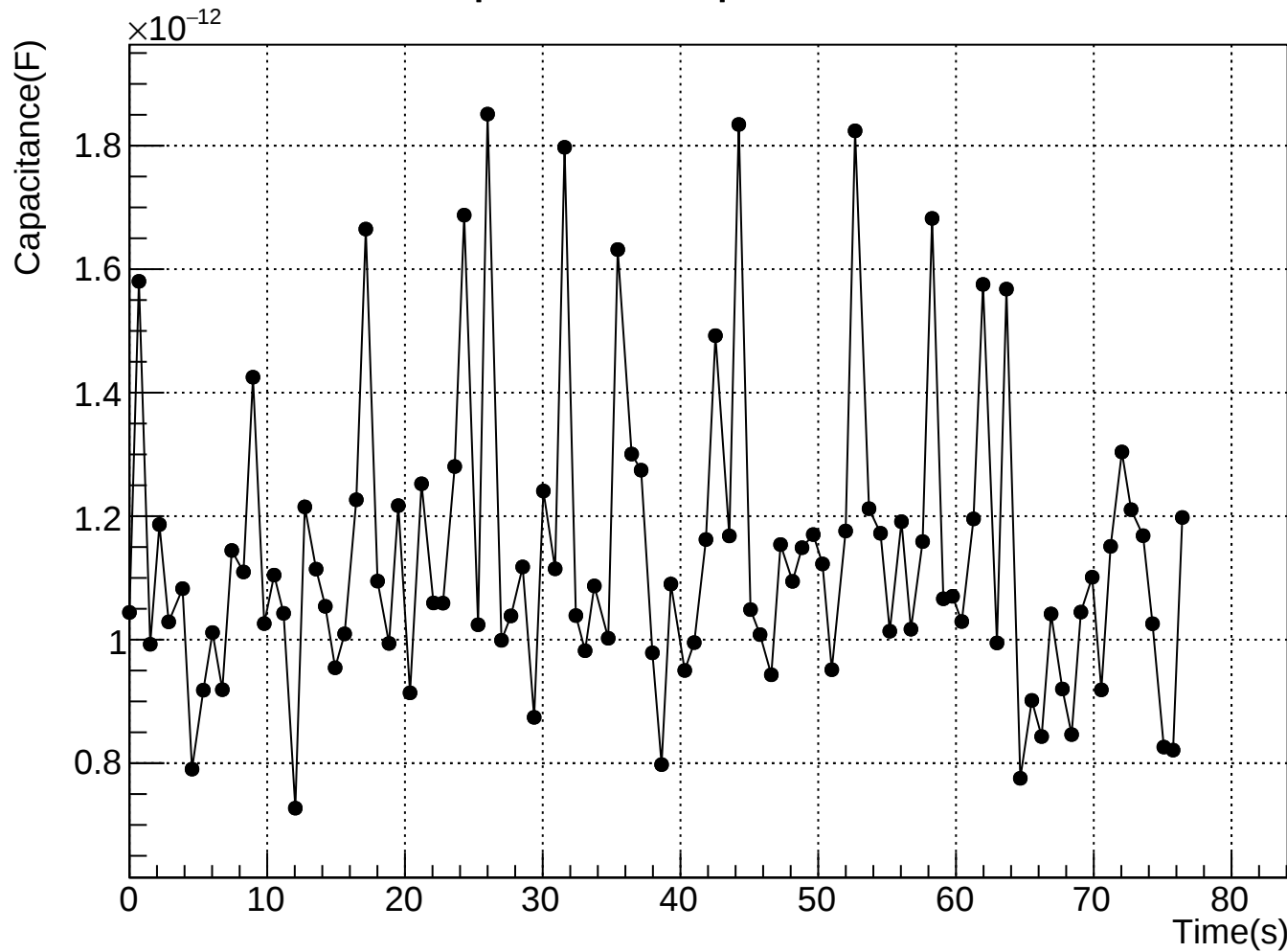
Sample101 Chip5 Channel111



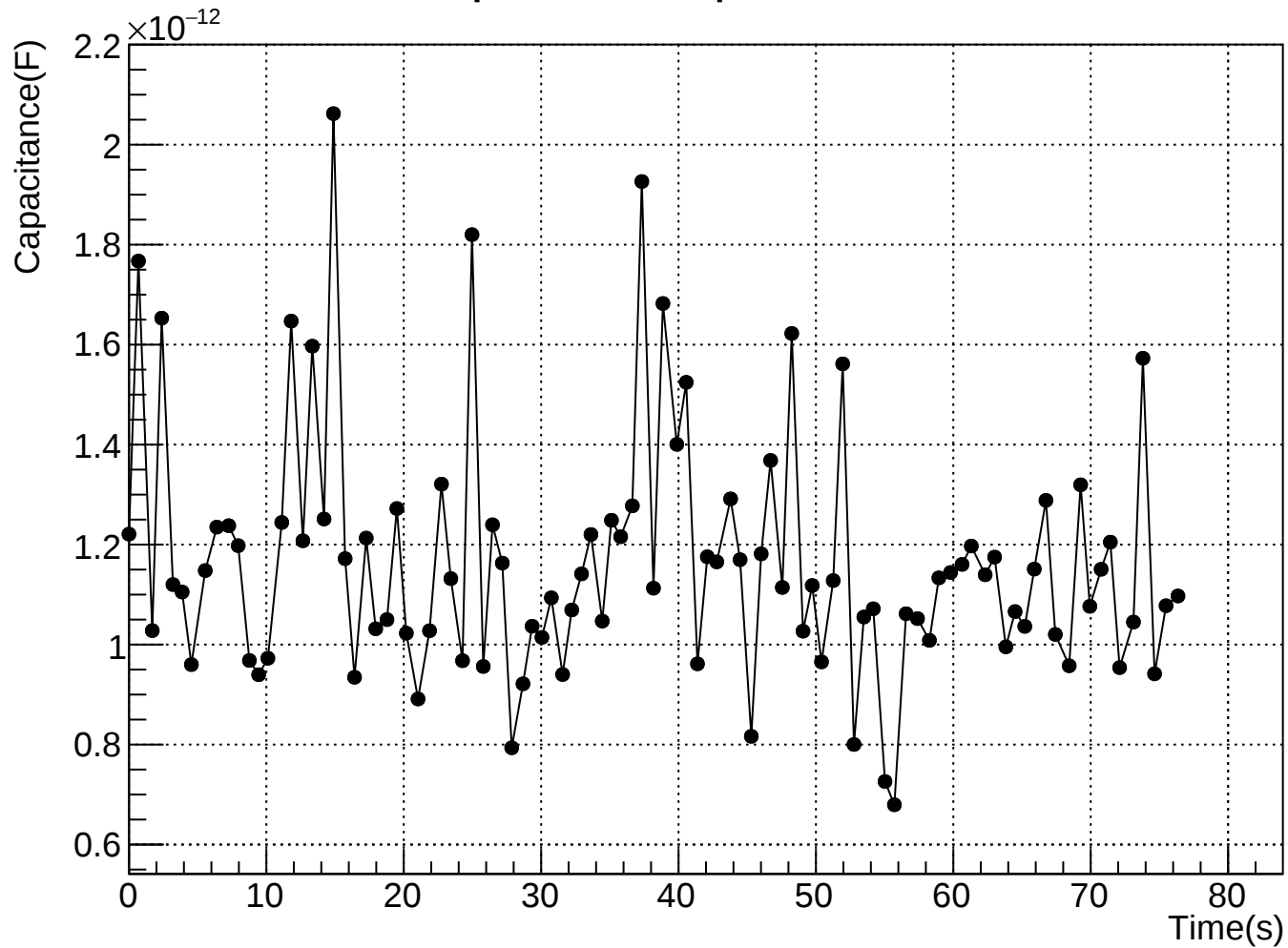
Sample101 Chip5 Channel112



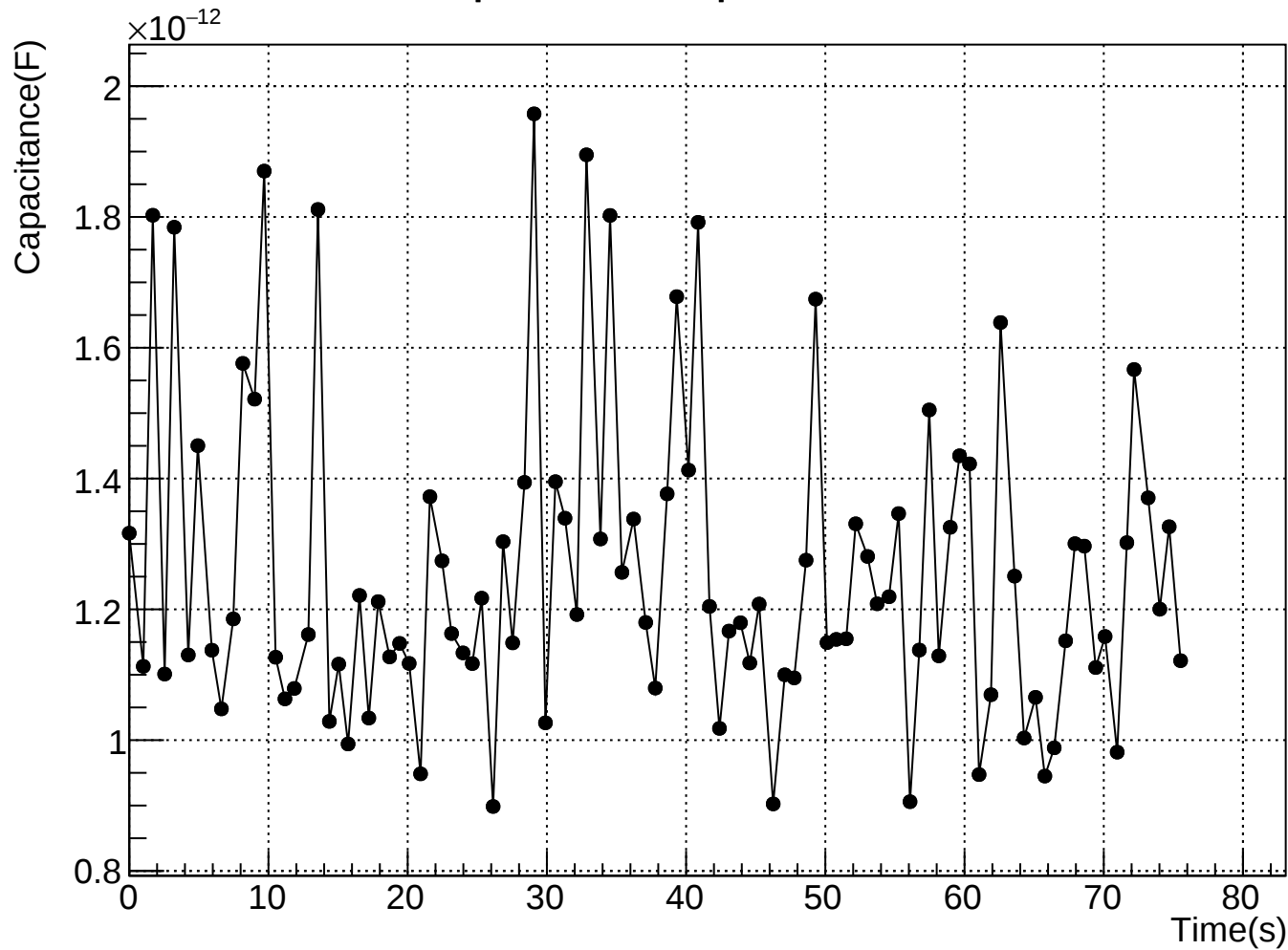
Sample101 Chip5 Channel113



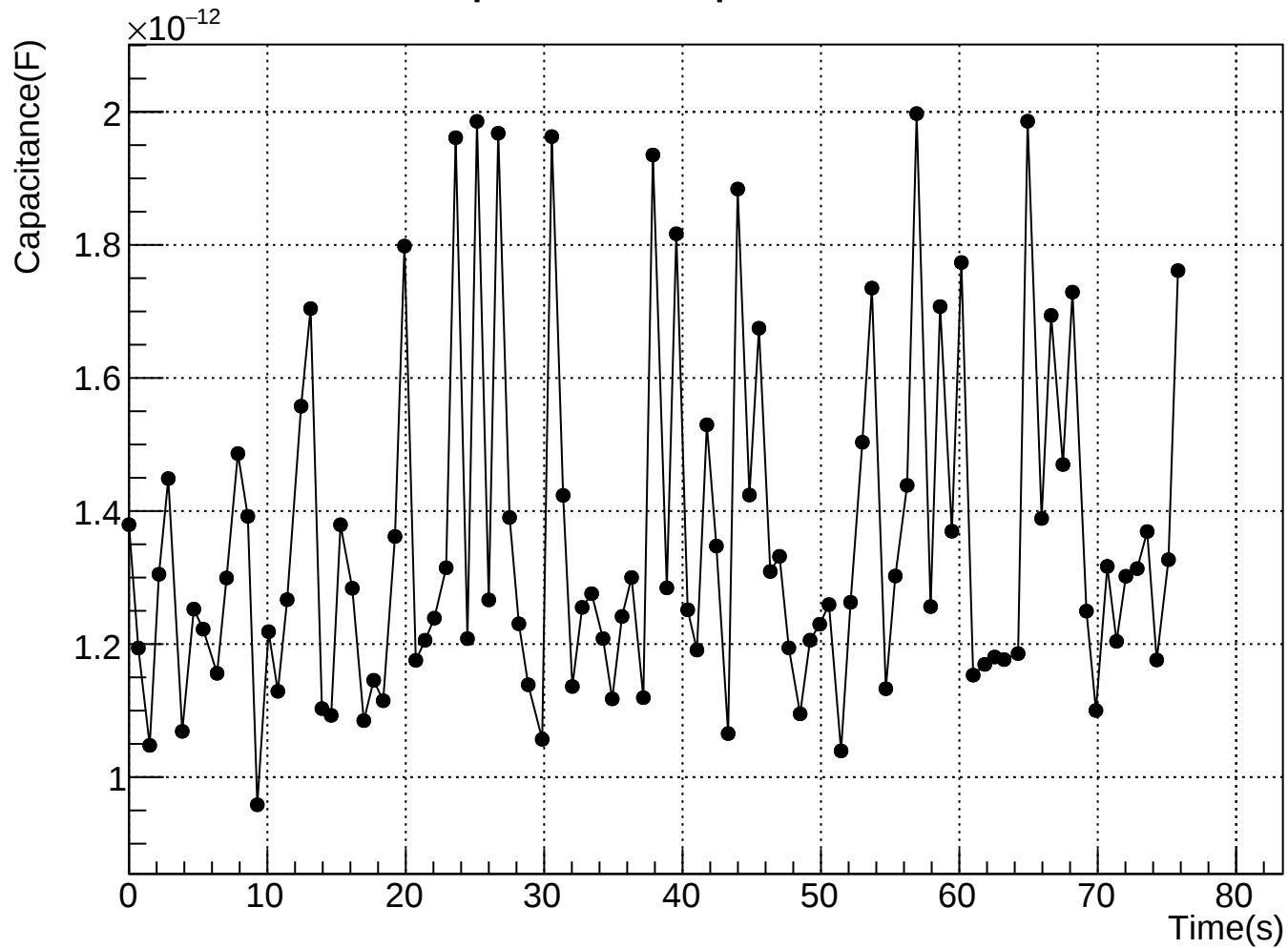
Sample101 Chip5 Channel114



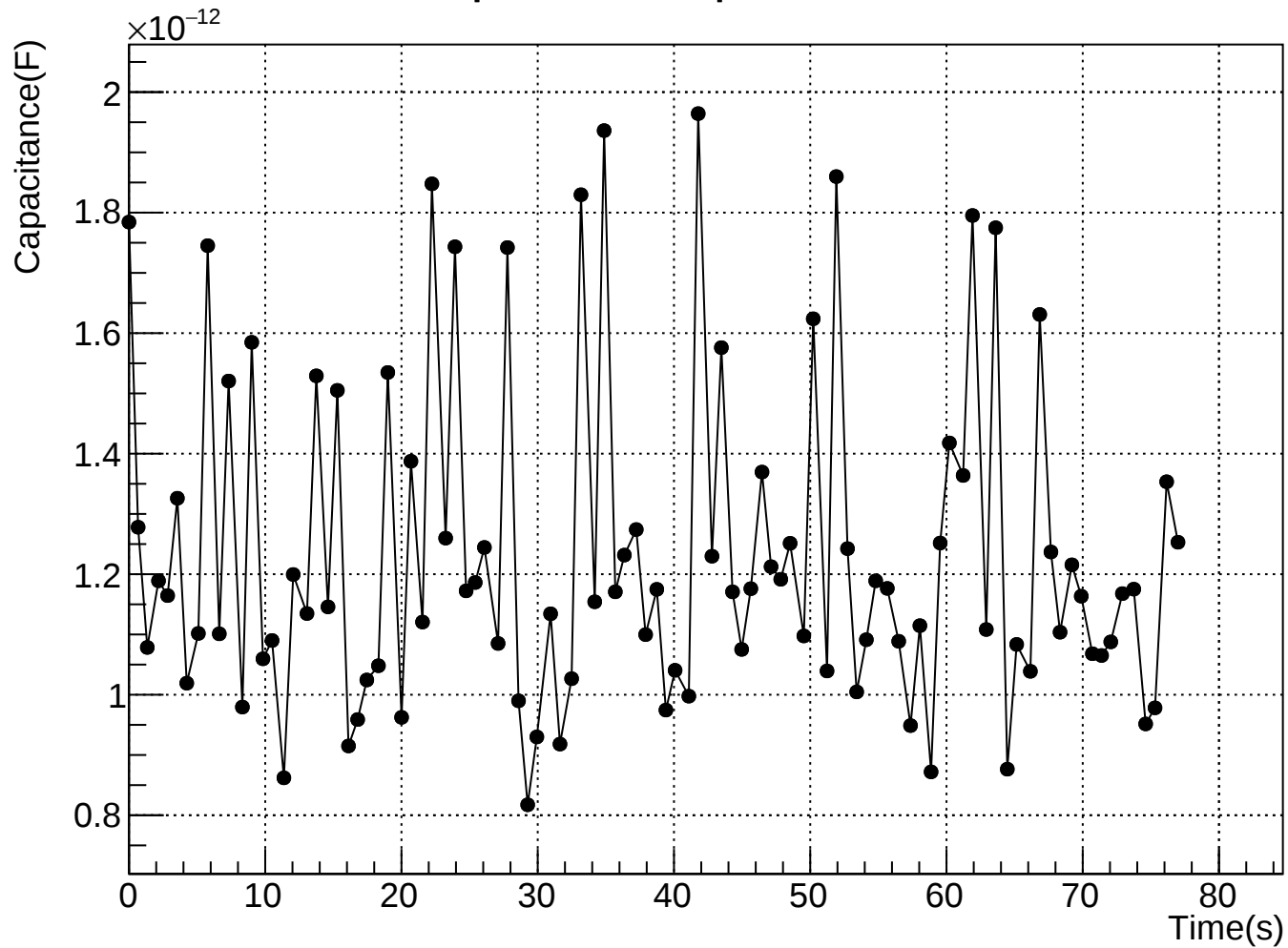
Sample101 Chip5 Channel115



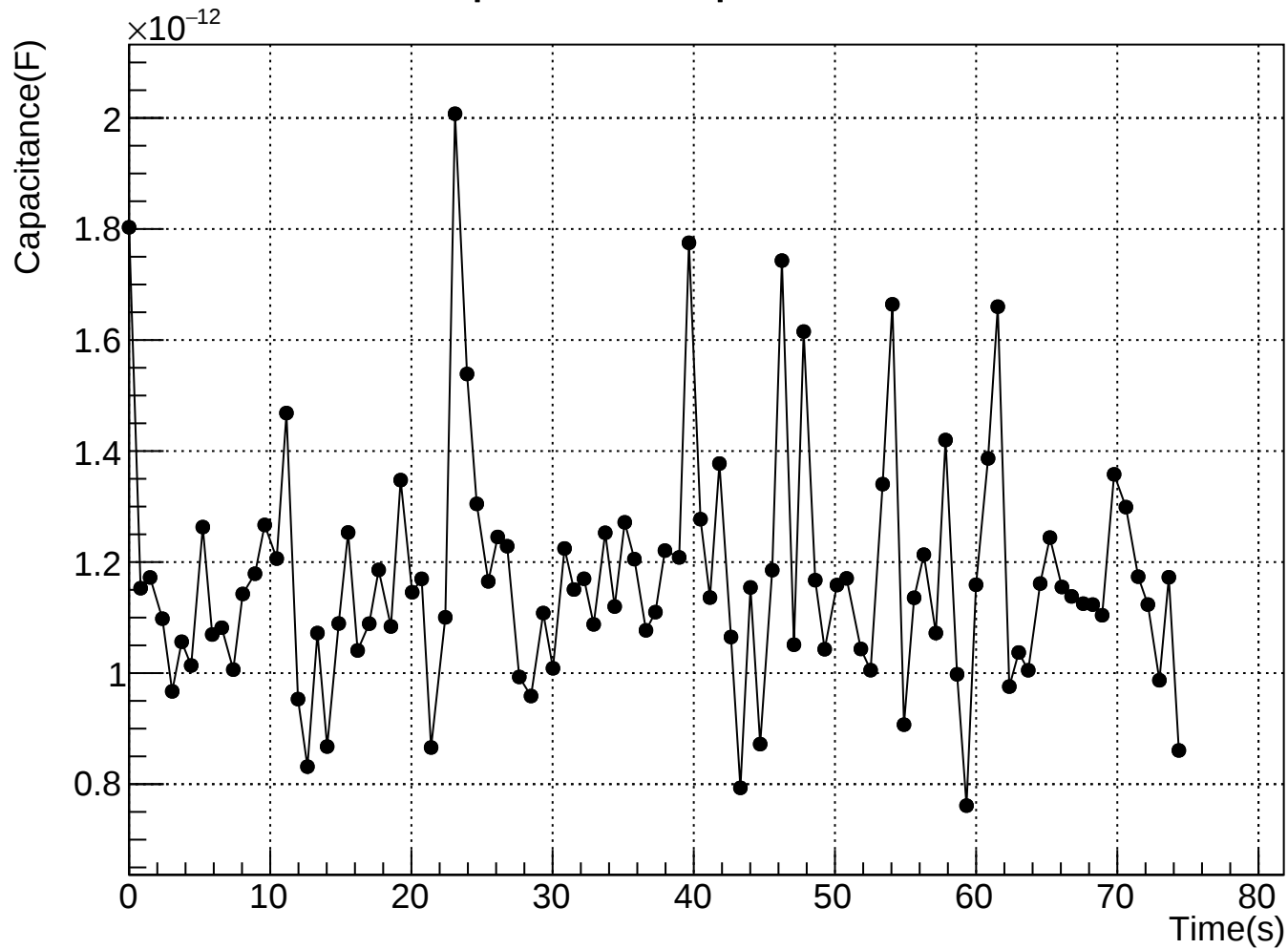
Sample101 Chip5 Channel116



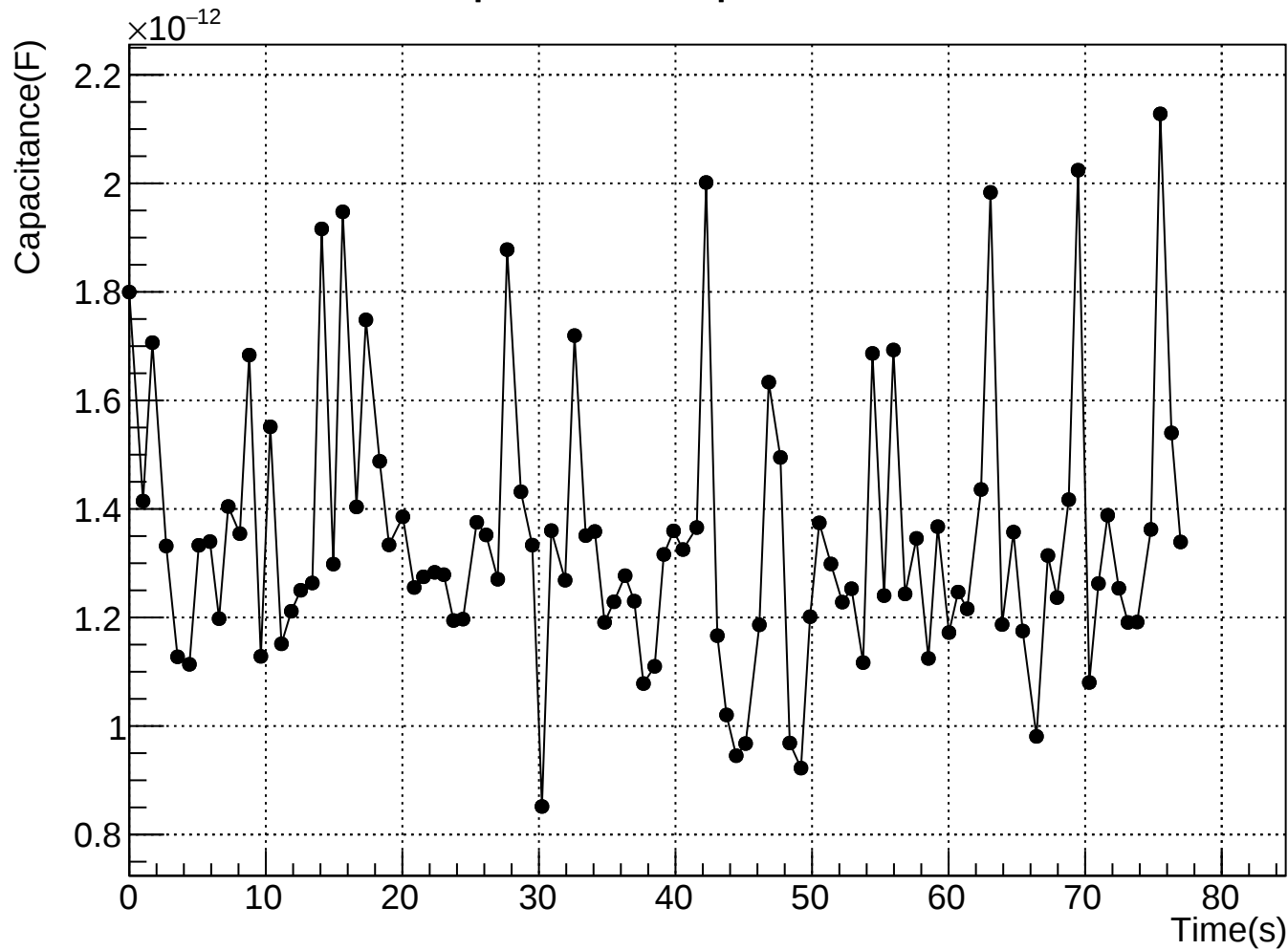
Sample101 Chip5 Channel117



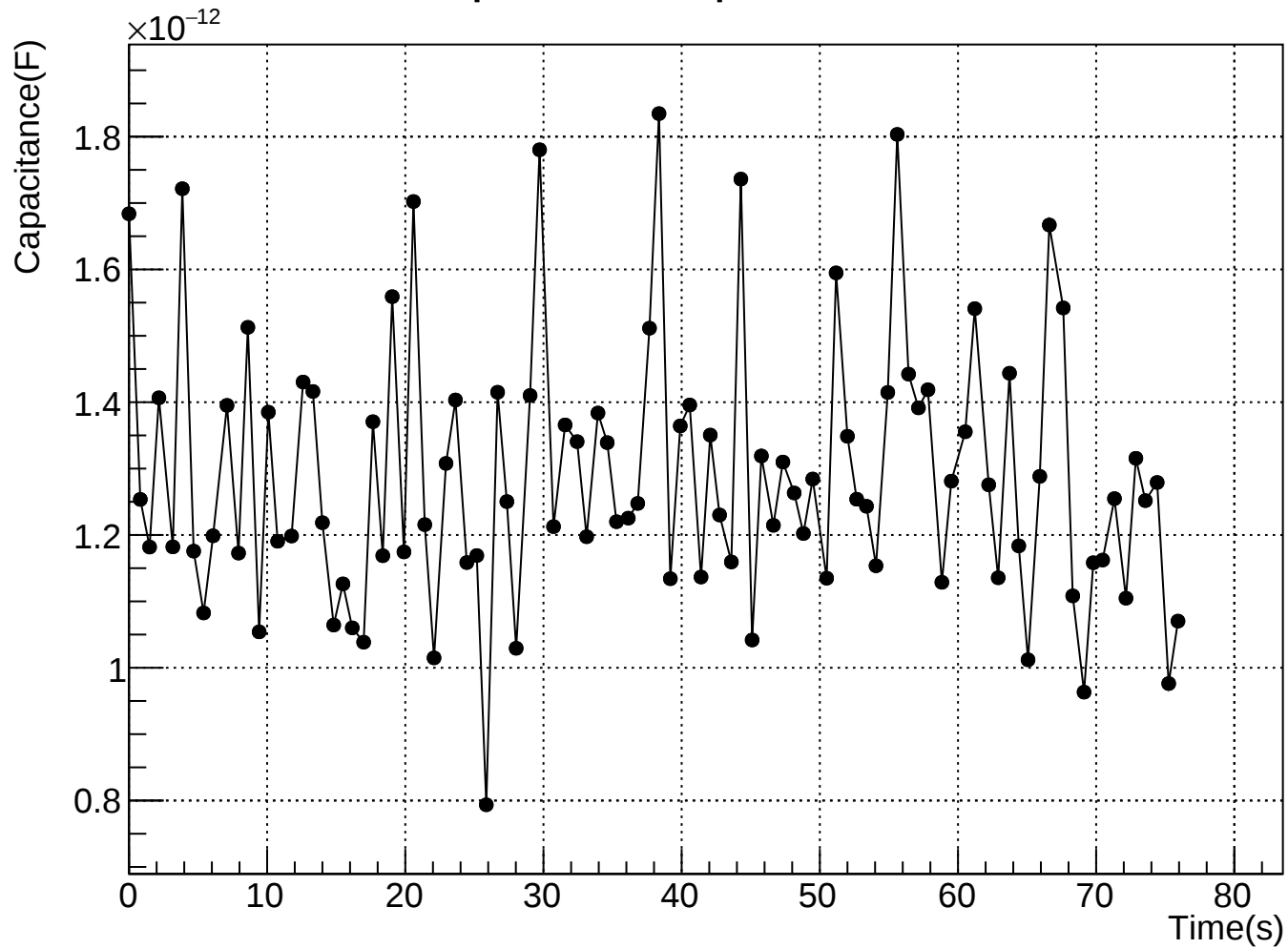
Sample101 Chip5 Channel118



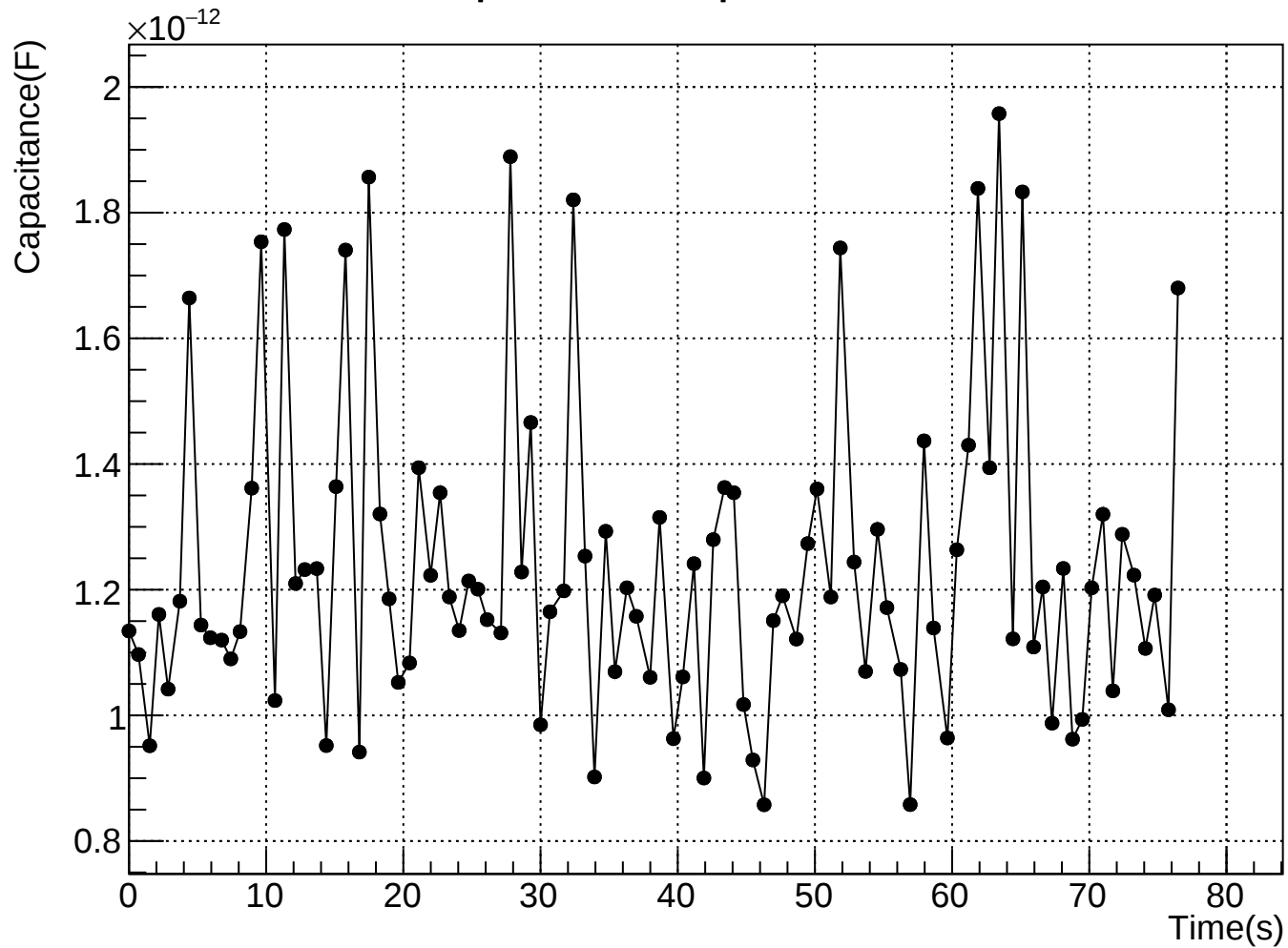
Sample101 Chip5 Channel119



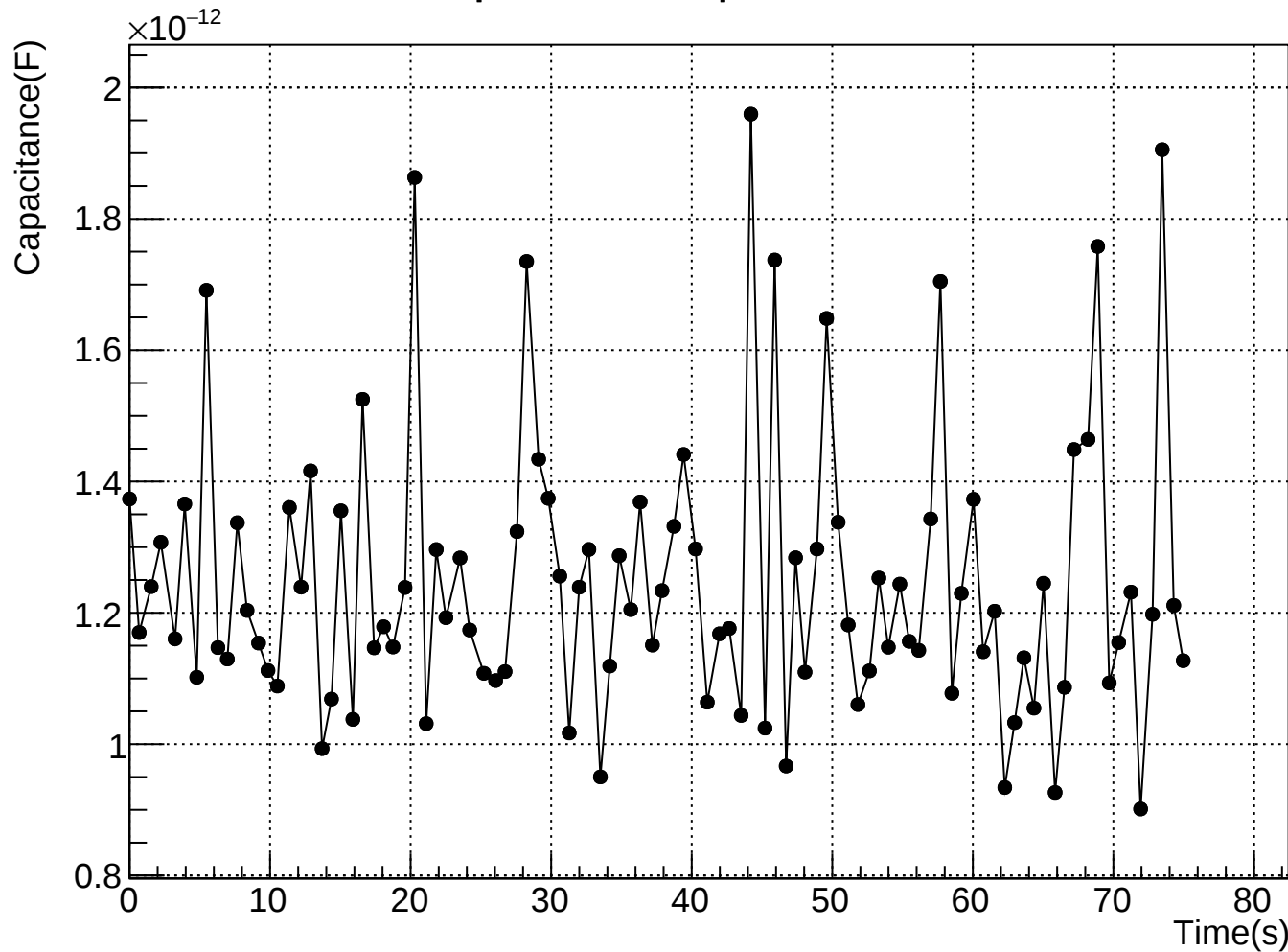
Sample101 Chip5 Channel120



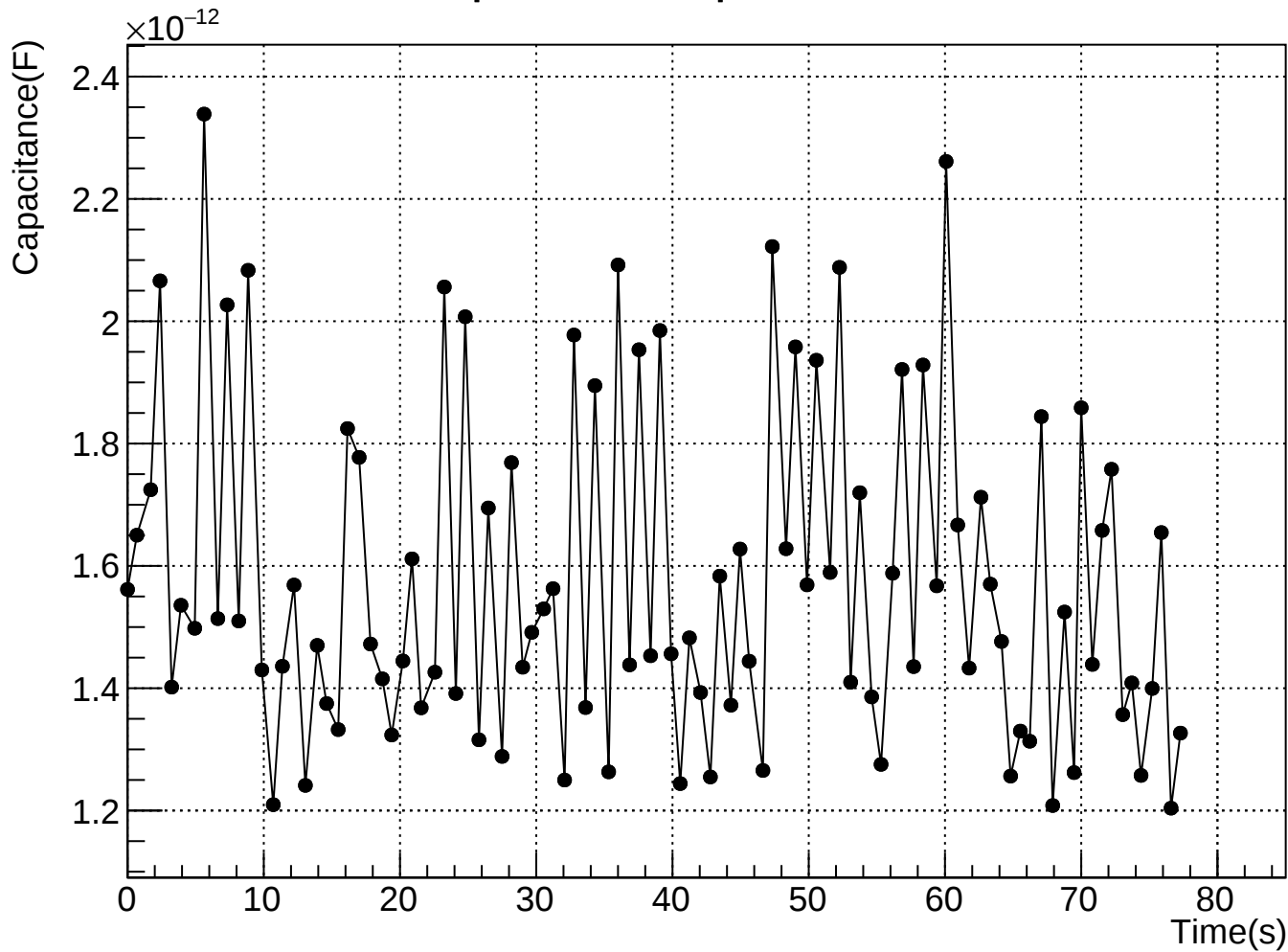
Sample101 Chip5 Channel121



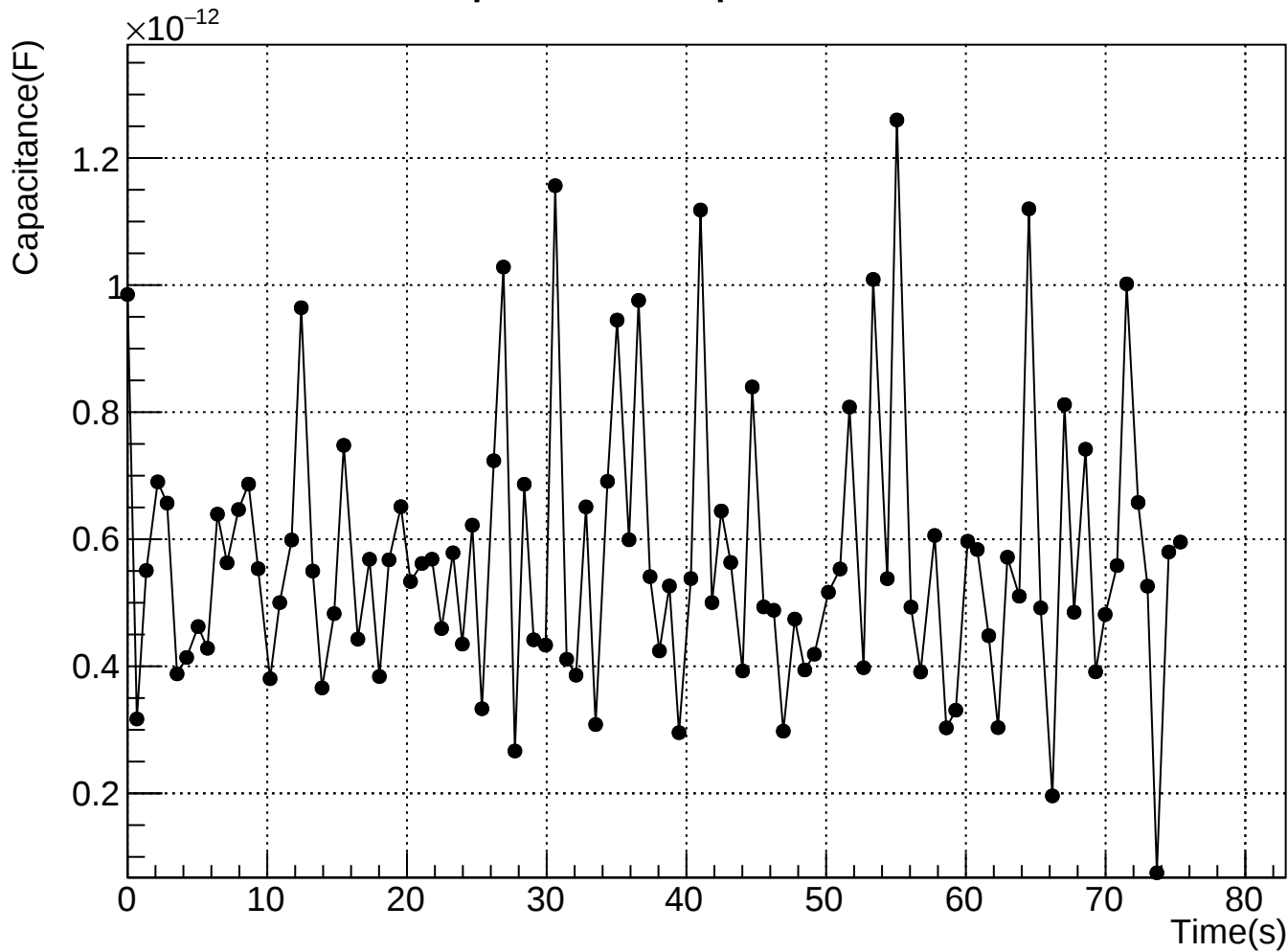
Sample101 Chip5 Channel122



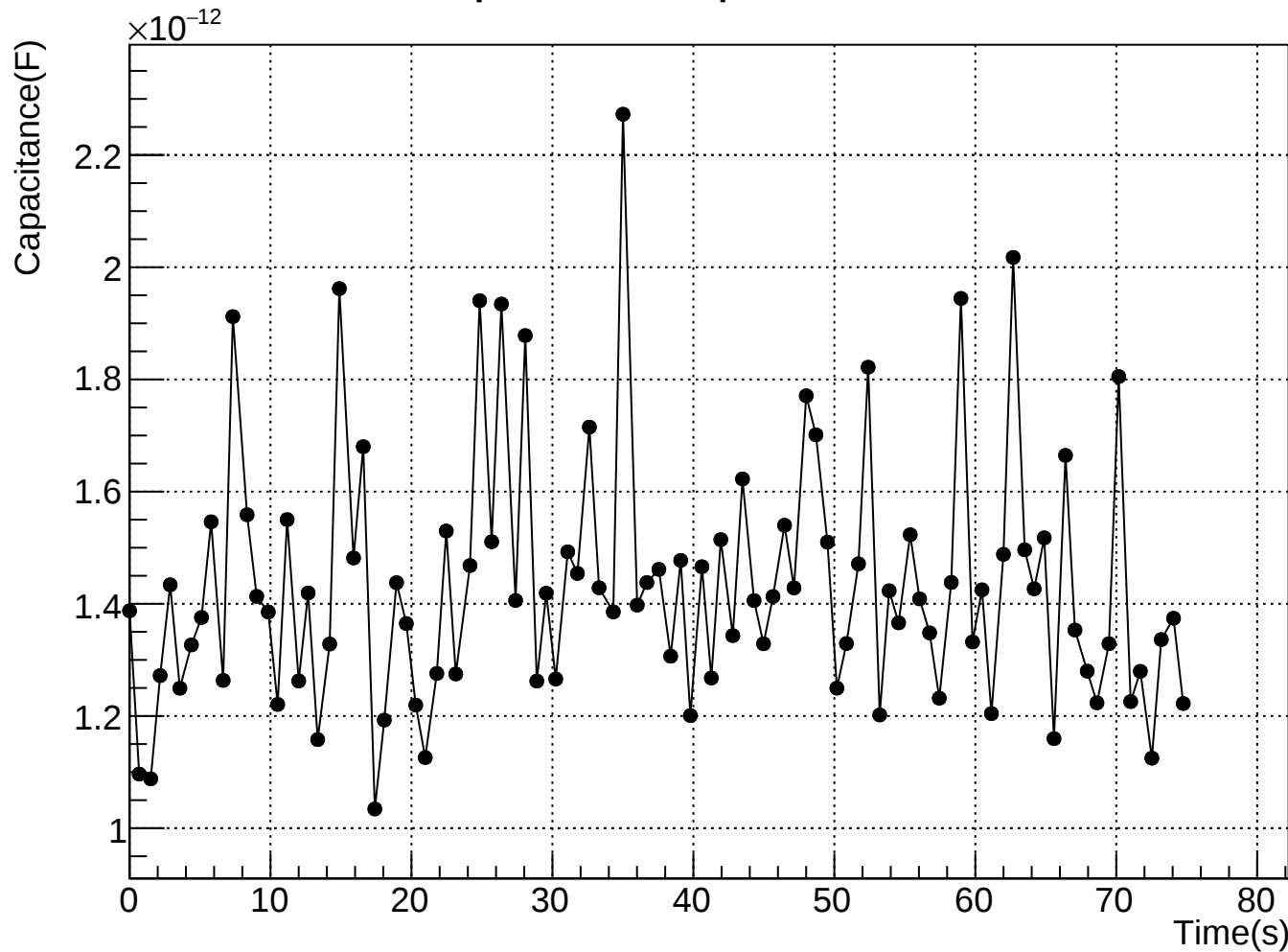
Sample101 Chip5 Channel123



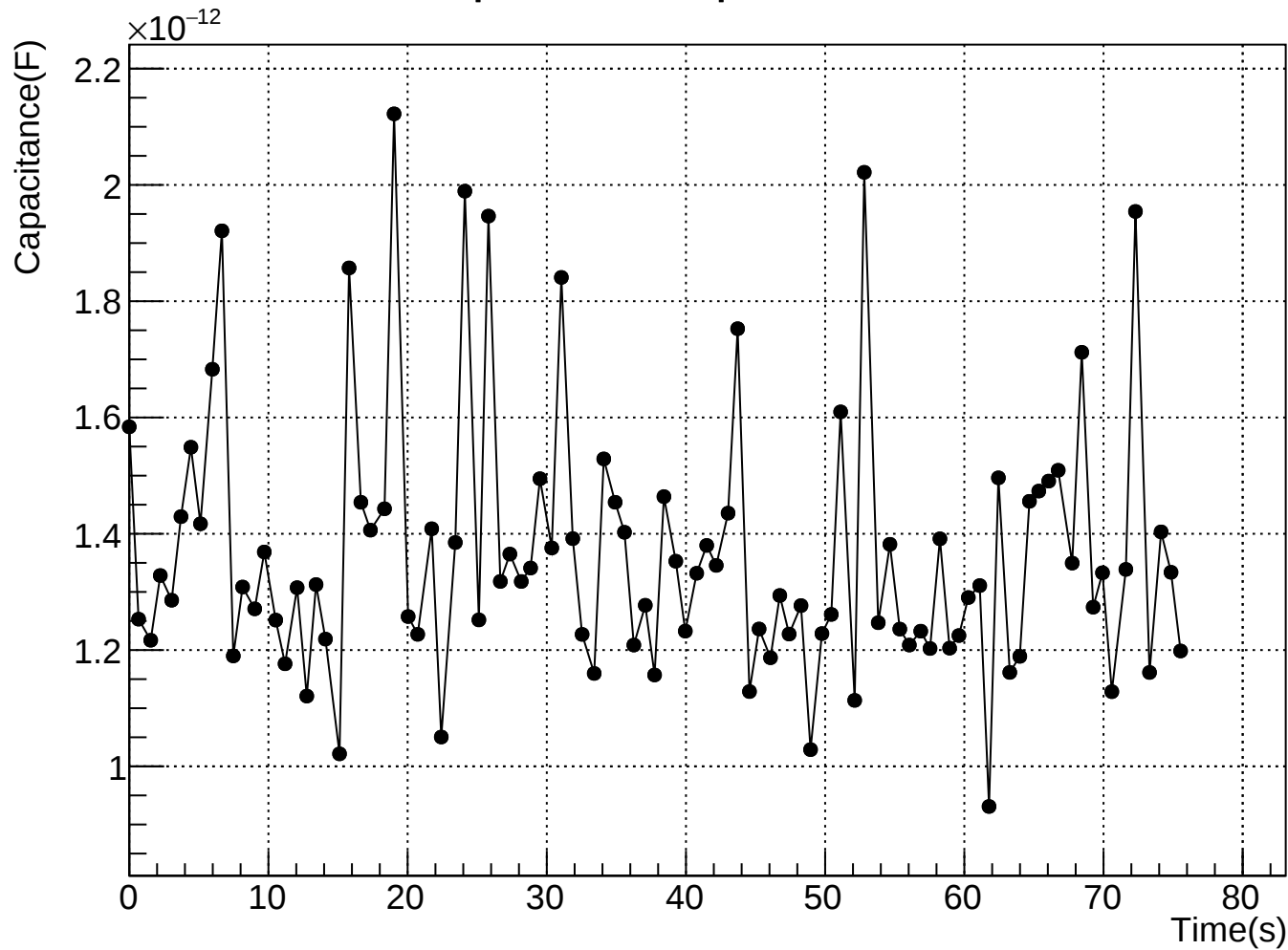
Sample101 Chip5 Channel124



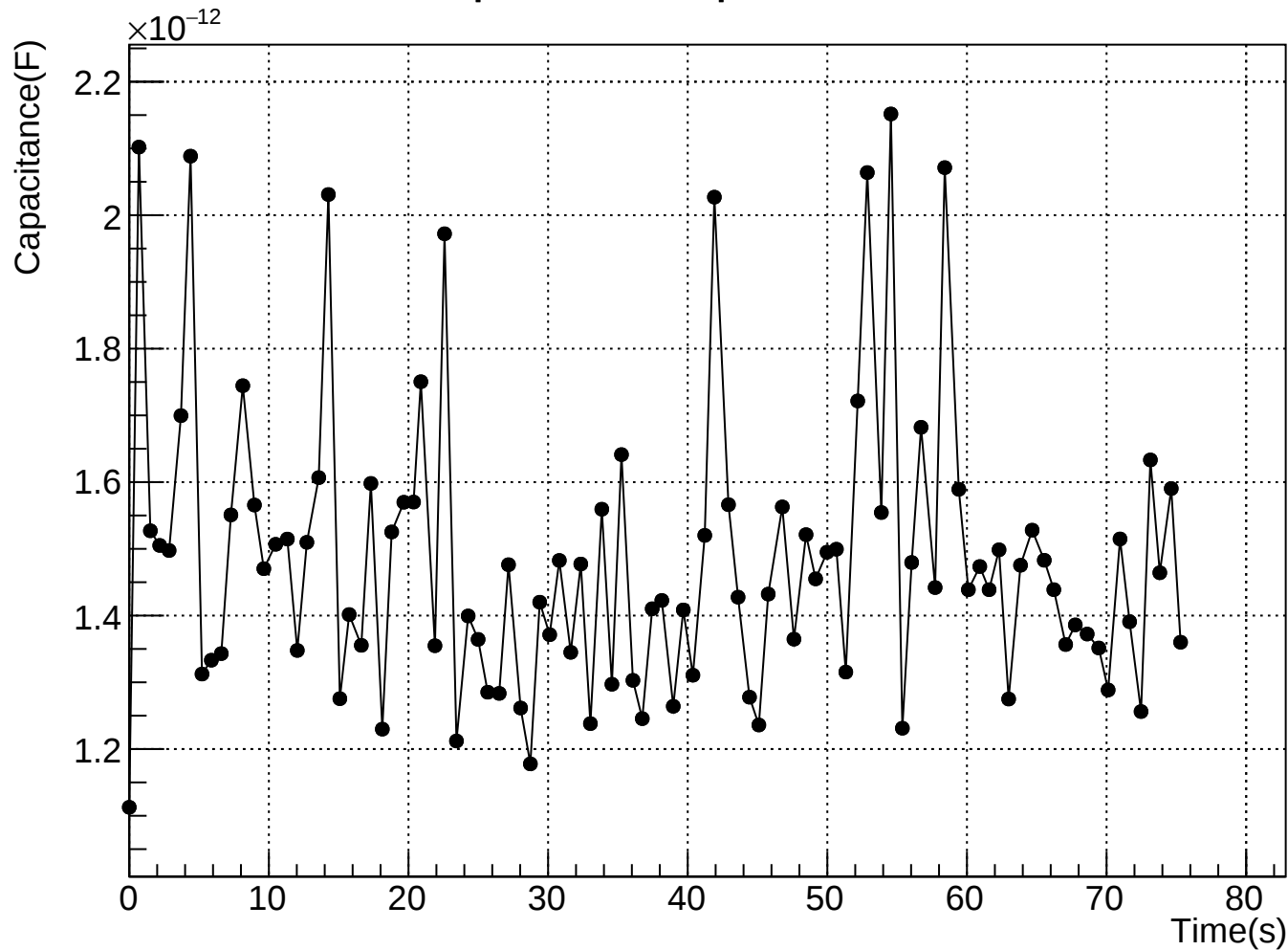
Sample101 Chip5 Channel125



Sample101 Chip5 Channel126



Sample101 Chip5 Channel127



Sample101 Chip5 Channel128

